Technion Israel Institute of Technology
VLSI Laboratory – Department of Electrical Engineering

Academic Use of Cadence Software Tools

Individual End Use Agreement (EUA) for University Account Students and Staff

Statement Acknowledging Responsibility To Use Cadence Tools in Accordance with End User Agreement

EVERY university professor, student, or staff member who has access to proprietary software tools and documents from Cadence must sign the following agreement before they are granted access.

STATEMENT ACKNOWLEDGING RESPONSIBILITY TO PROTECT PROPRIETARY MATERIAL

My signature below acknowledges that:
1. I have read and fully understand the conditions under which I may use the Cadence tools as stated in the Cadence Europractice EUA: http://www.europractice.stfc.ac.uk/eua/univs/cadence.pdf.
2. I understand that proprietary material supplied through the VLSI lab at the Technion or any other means must be viewed only by myself and not shown to any other person. These documents may not be copied to or stored in any medium (paper or on a computer) that is not under strictly secure control at all times.
3. I am aware that use of all Cadence licenses are monitored and recorded by the VLSI lab as required by Cadence.

Student 1 :   **Student 2 :**
Name:_____ (Printed) Name:_____

Signature:_____   Signature:_____

Email :   _____   Email :   _____

User Account name in VLSI Lab.   _____ (ep _ _ _ _   cp_ _ _ _   mm_ _ _ _   rf_ _ _ _   mrn_ _ _ _   ko_ _ _ _)

Date:_____

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