RASPBERRY UPS 3A

Variant: w/ CPU

12.4.2016

Version: B0

DRAFT

Picture is symbolic

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Schematic Status Explanation

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - Schematic has been verified and validated.

Code Explanation

ProjectCode [0-999] - VariantCode[0-999] - Revision [A0, B0, C0, D0]

DESIGN NOTE: Example text for debug notes. DESIGN CONSIDERATIONS

DESIGN NOTE: Example text for informational design notes .

DESIGN NOTE: Example text for cautionary design notes. LAYOUT NOTE: Example text for critical layout guidelines.

DESIGN NOTE: Example text for critical design notes.

Technical data:

Nominal voltage: 12V d.c.
Power consumption*: max. 400W
Recommended inlet wires: min. 6 mm²

	Title: RASPBERRY UPS 3A Variant: w/ CPU			Code: xxx-xxx-B0			
				Date: 10.4.2015	Sheet: * of 3	Size: A3	
	SCH Revision:	SCH Name:		SCH date:	Document name:		
DrawnBy: R. LIPAR	A0 RASPBERRY UPS SMART B0 RASPBERRY UPS 3A AA AA BB BB			5.1.2015 10.4.2015	0.4.2015 [01] COVER PAGE		
CheckedBy: R. LIPAR				AA BB	Revision:	B0	

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