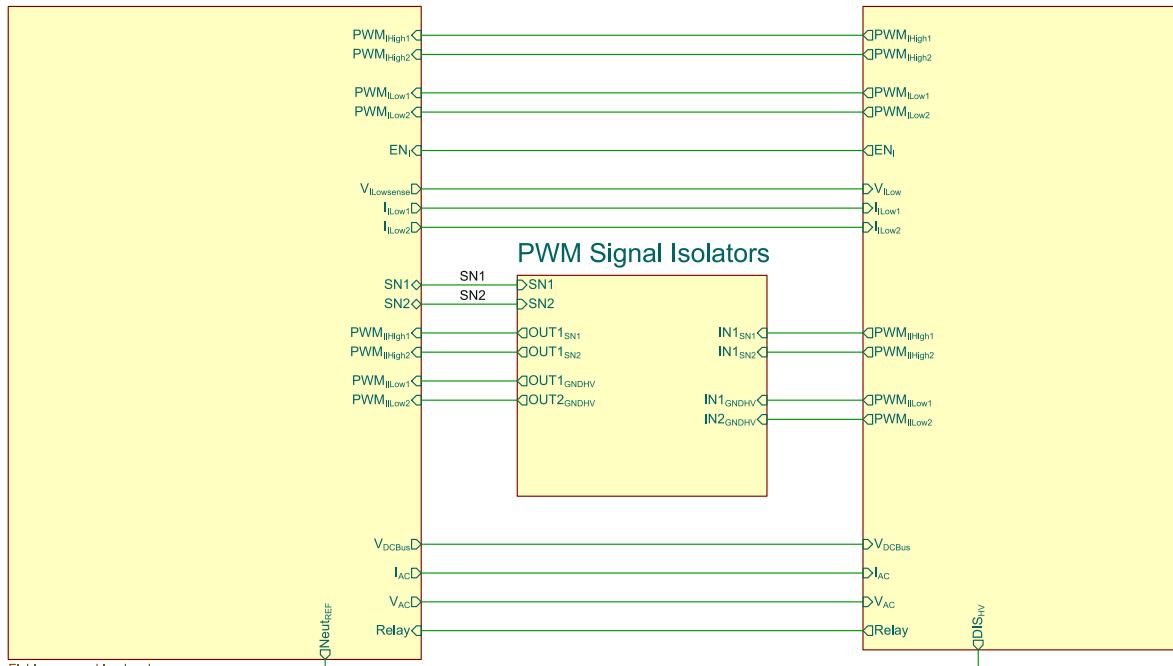
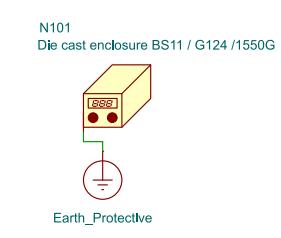
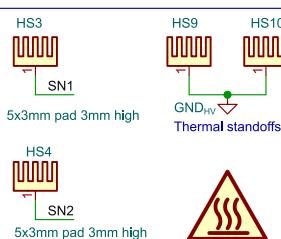
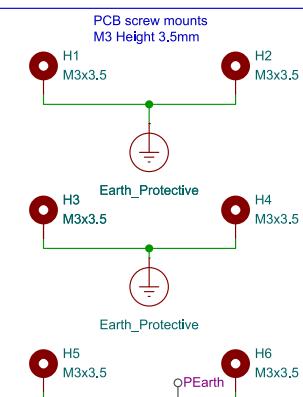


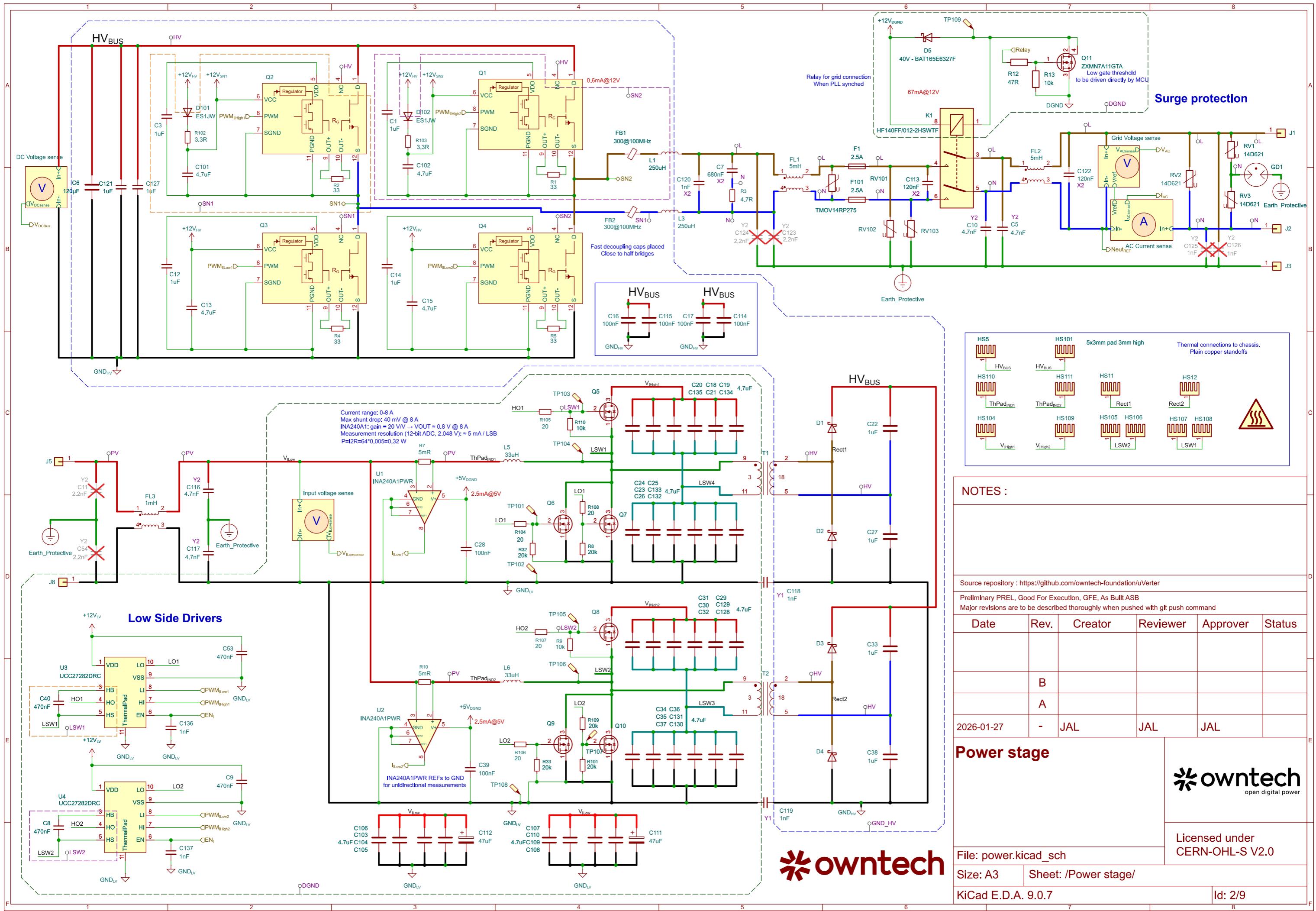
1 2 3 4 5 6

Power stage**owntech**

○ FID1 Fiducial
○ FID2 Fiducial
○ FID3 Fiducial
Fiducials

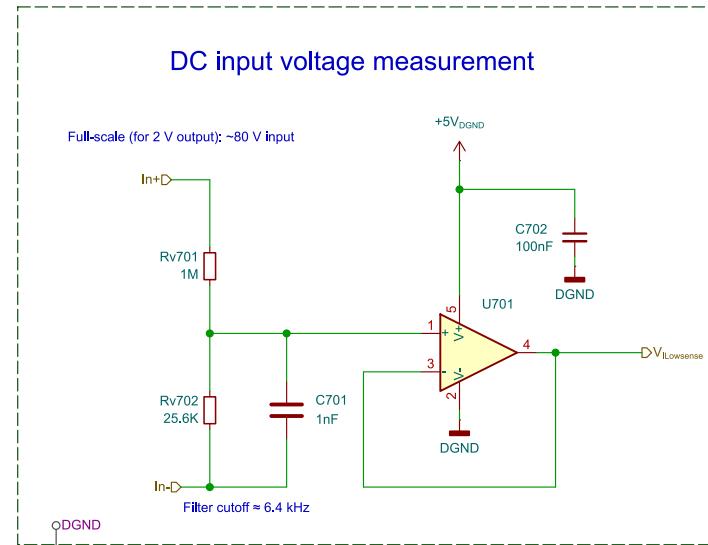


1 2 3 4 5 6



A

A



B

B

NOTES :Source repository : <https://github.com/owntech-foundation/uVerter>

Preliminary PREL, Good For Execution, GFE, As Built ASB
Major revisions are to be described thoroughly when pushed with git push command

| Date | Rev. | Creator | Reviewer | Approver | Status |
|------------|------|---------|----------|----------|--------|
| | | | | | |
| | B | | | | |
| | A | | | | |
| 2026-01-27 | - | JAL | JAL | JAL | |

DC Input voltage measurement

Licensed under
CERN-OHL-S V2.0

File: input_voltage_sense.kicad_sch

Size: A4 Sheet: /Power stage/Input voltage sense/

KiCad E.D.A. 9.0.7

Id: 3/9

