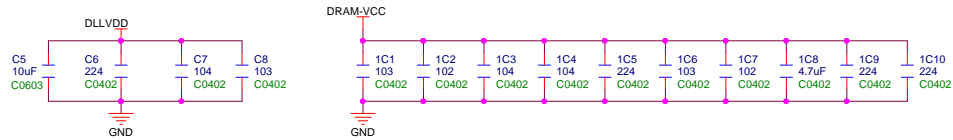
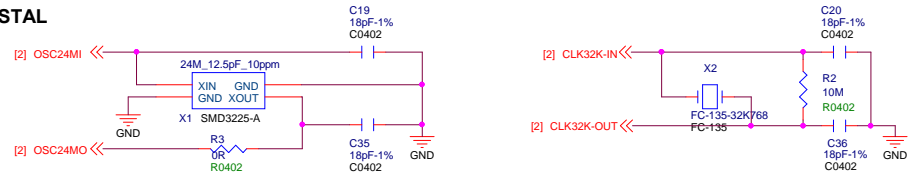


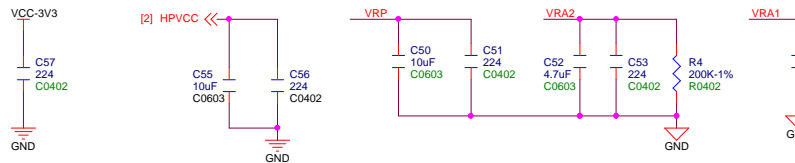
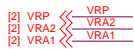
DRAM-DLL



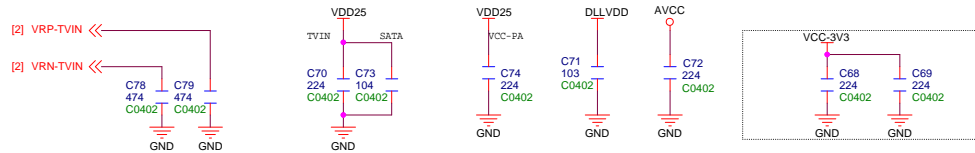
CRYSTAL



AUDIO-LRADC



USB-TV-CSI

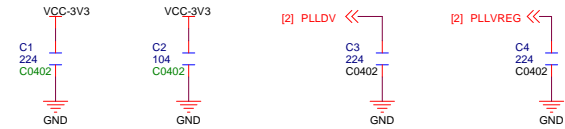


HDMI

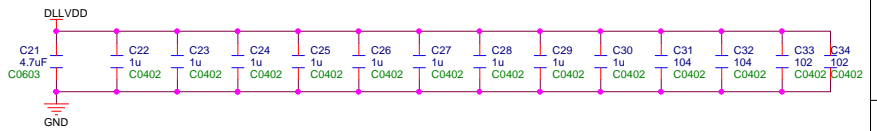
place close to SOC pin



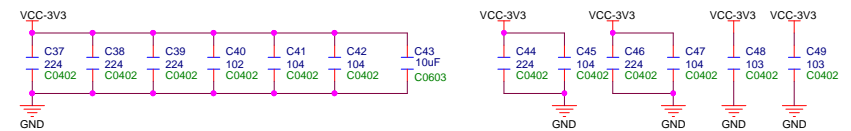
PLL-NAND



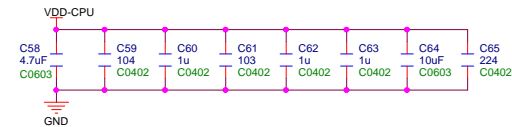
CORE



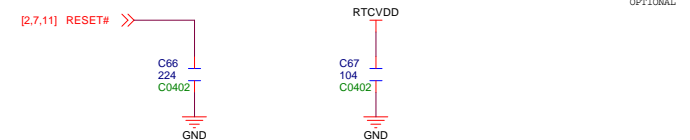
PIO-INTERFACE



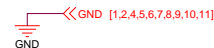
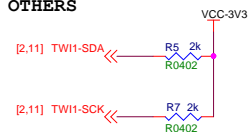
CPU



RESET-RTC



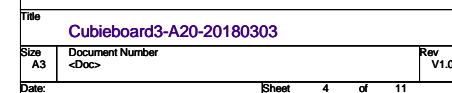
OTHERS

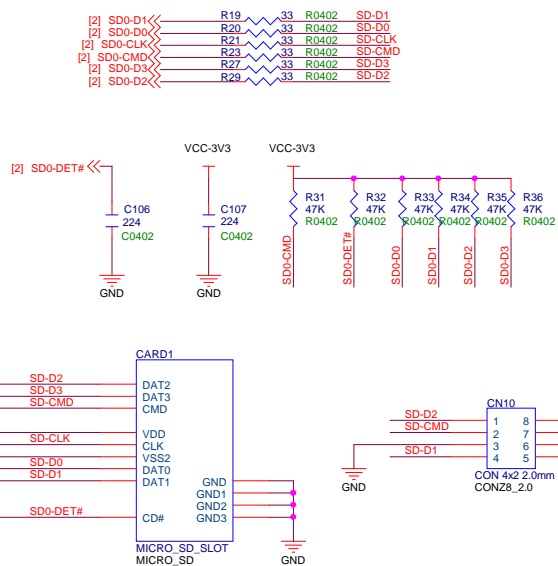


Title			
Cubieboard3-A20-20180303			
Size A3	Document Number <Doc>		Rev V1.0
Date:	Sheet	3 of 11	

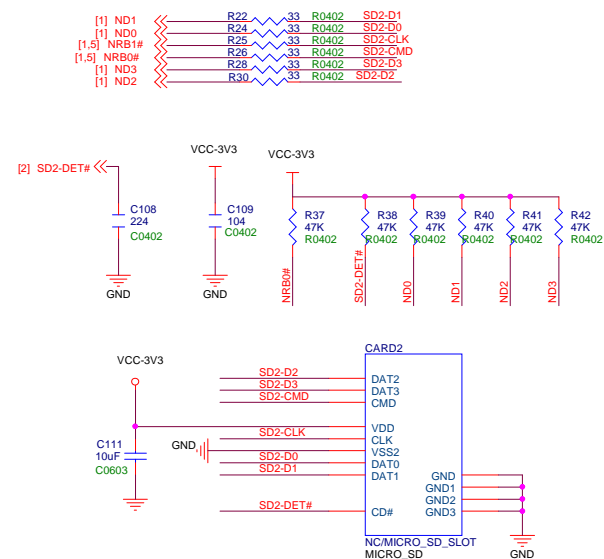
DDR封装为TFBGA82

Equilong BUS
Z0= 50 ohm

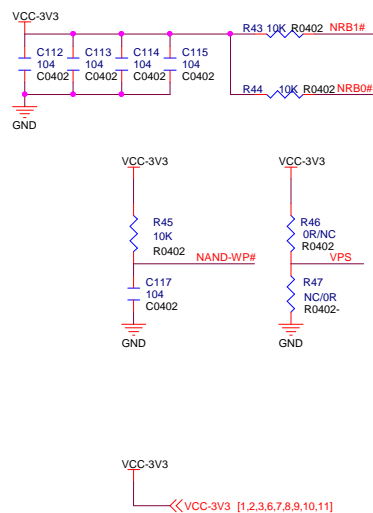
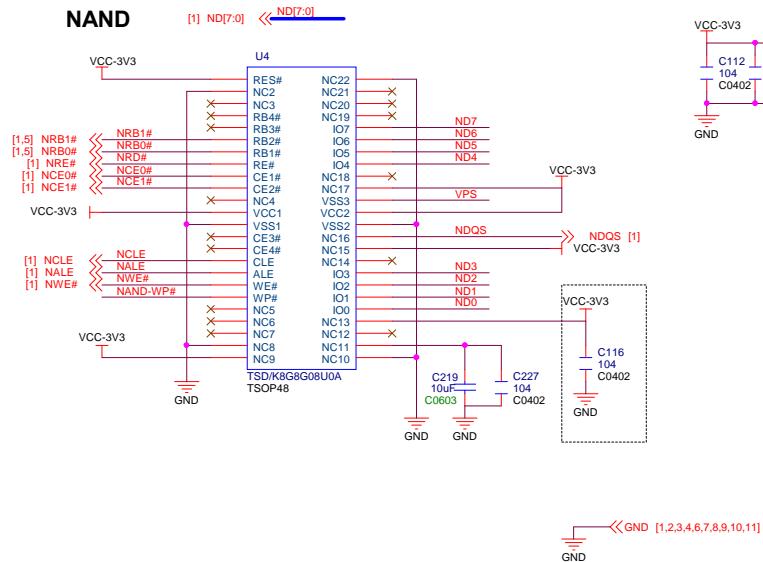


CARD0

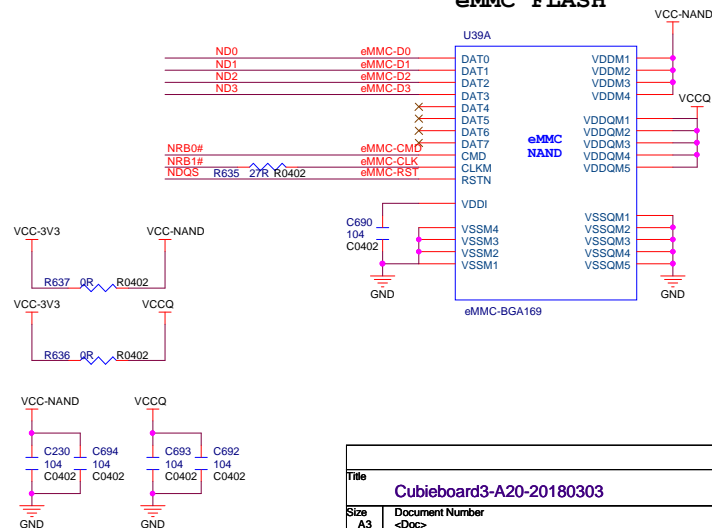
CARD2 (option with NAND flash)



NAND

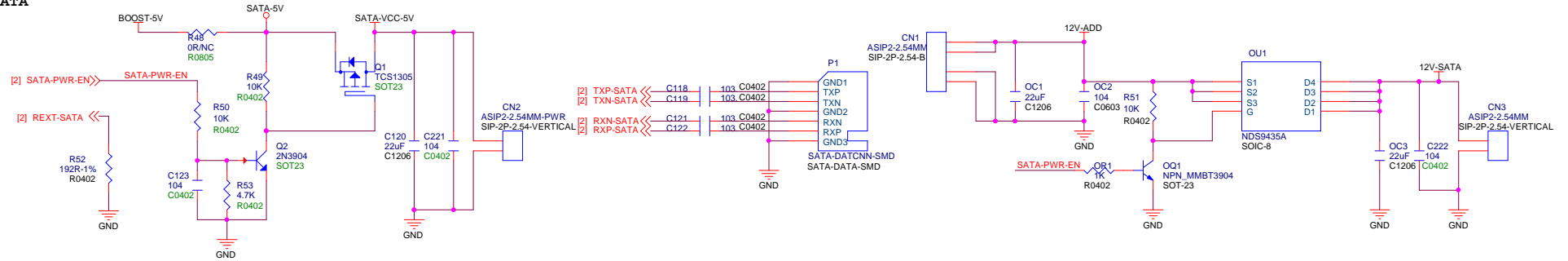


eMMC FLASH

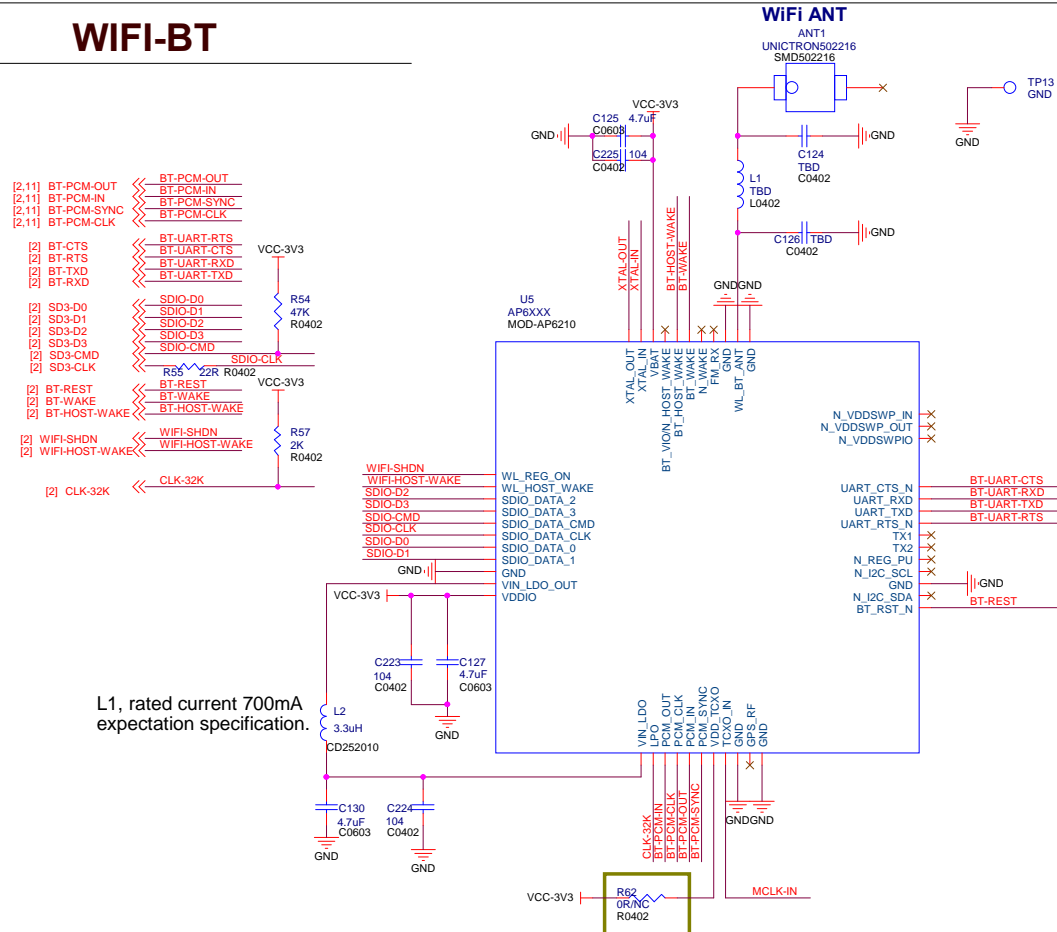


Title				
Cubieboard3-A20-20180303				
Size	Document Number			Rev
A3	<Doc>			V1
Date:	Sheet	5	of	11

[2] SATA-PWR-EN)



WiFi ANT

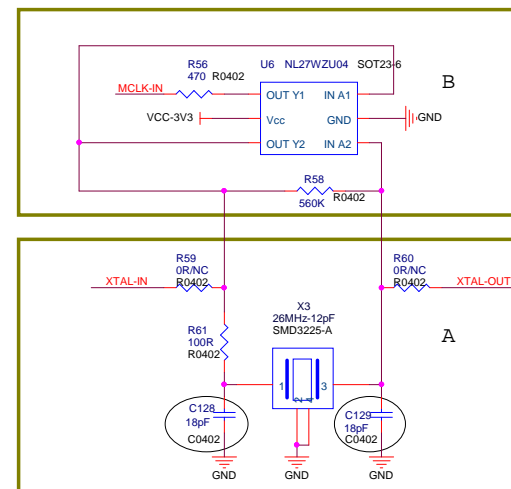


L1, rated current 700mA
expectation specification.

AP6210接上此电阻,AP6181和AP6330不接

还需要增加FM天线

AP6210需要框内A和B部分,R47,R48就NC
AP6181,AP6330只需要框内A部分

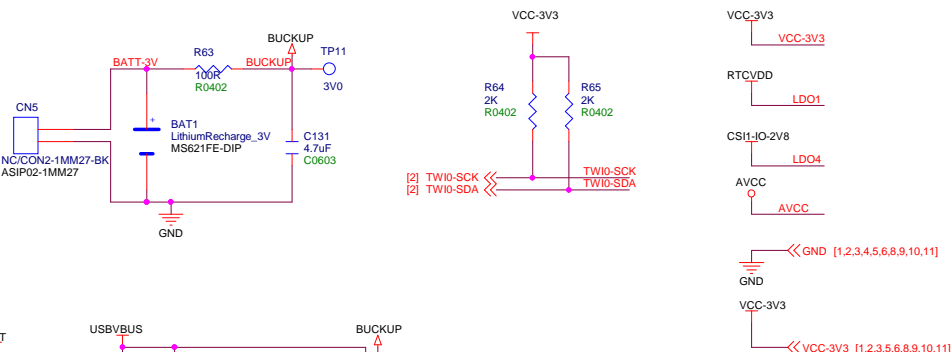
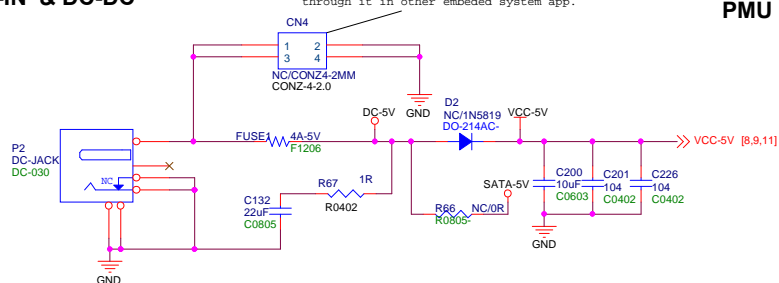


Title				
Cubieboard3-A20-20180303				
Size A3	Document Number <Doc>			Rev V1.0
Date:	Sheet		6	of 11

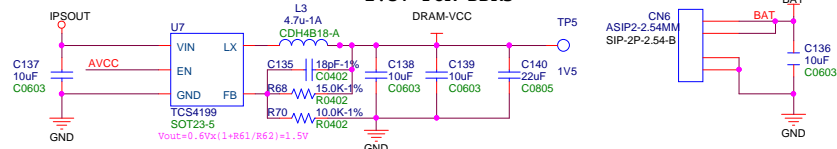
DC-IN & DC-DC

Solder option with DC-JACK, coreboard
can be powered from extended board
through it in other embeded system app.

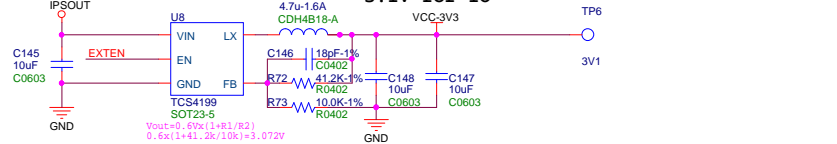
PMU



1.5V FOR DDR3

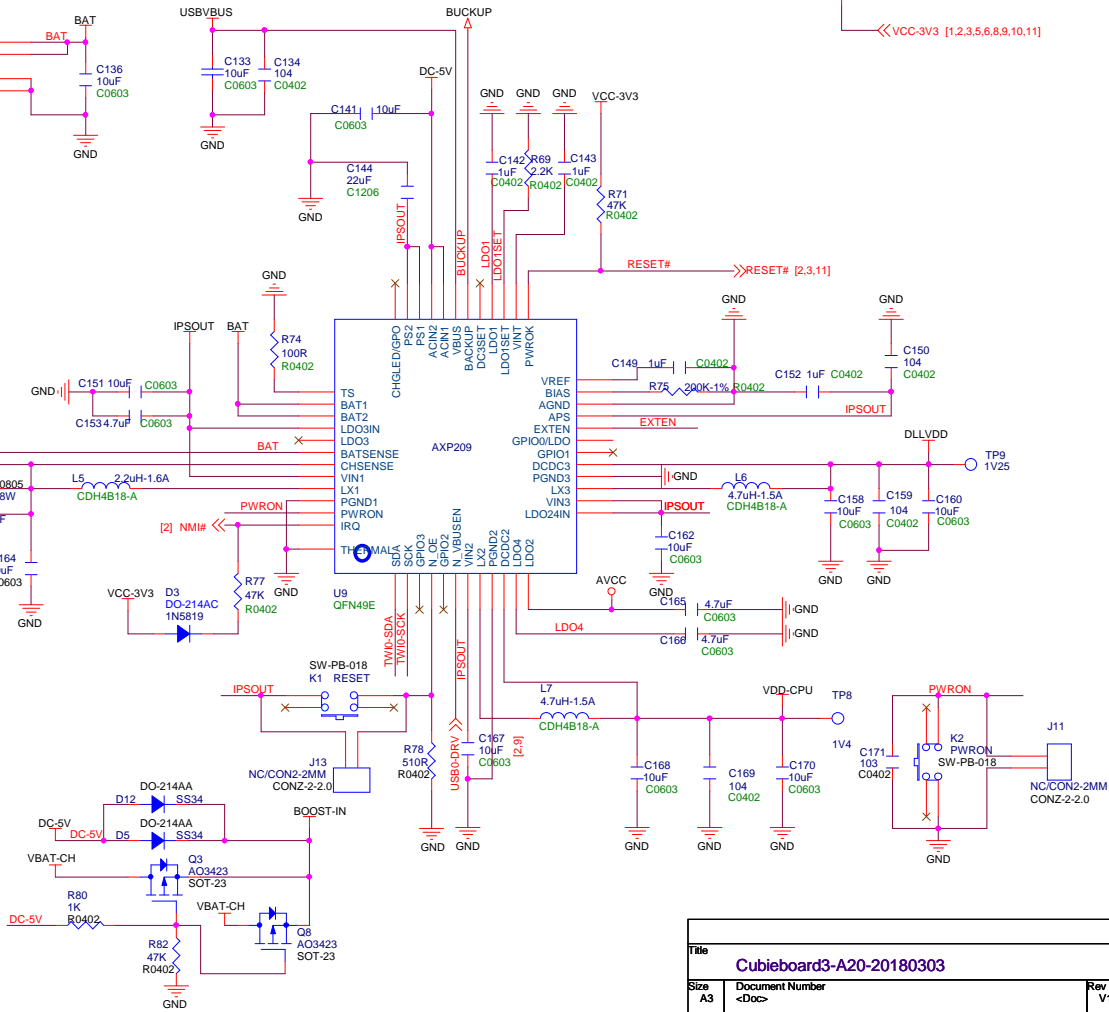
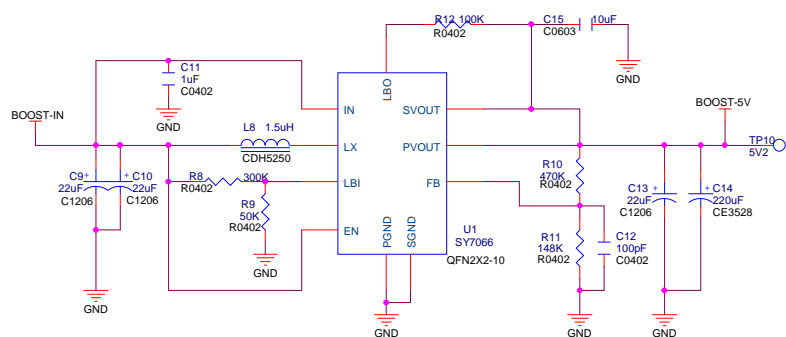
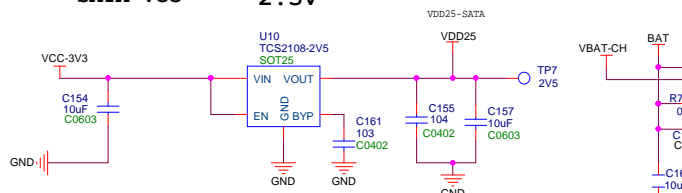


3.1V for IO



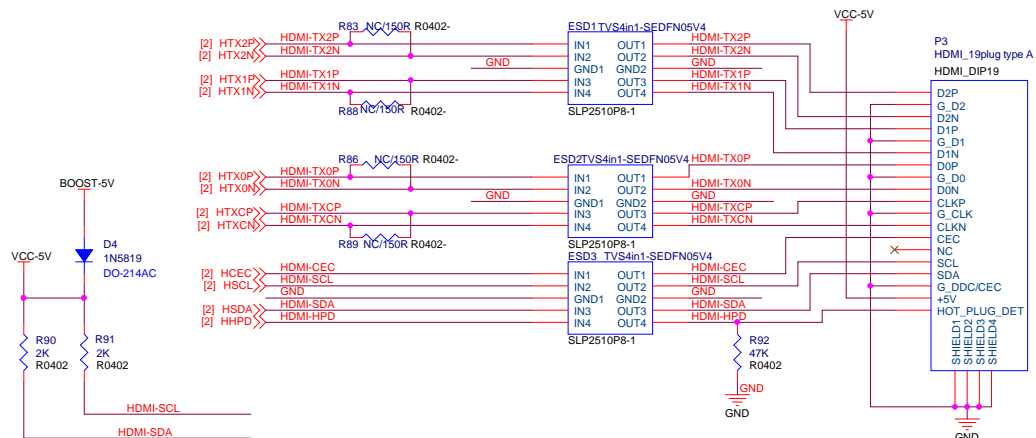
SATA-VCC

2.5V

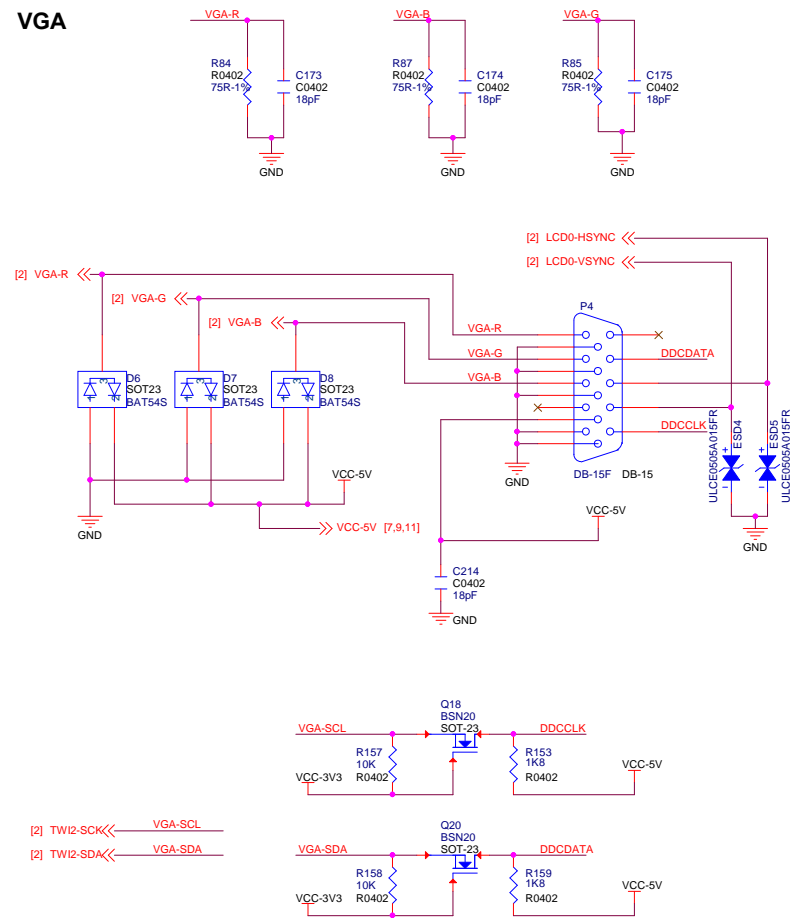


Title				
Cubieboard3-A20-20180303				
Size	Document Number			Rev
A3	<Doc>			V1.
Date:	Sheet		7	of 11

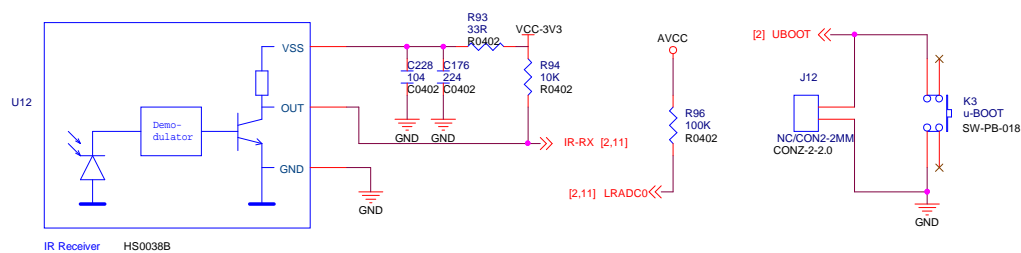
HDMI



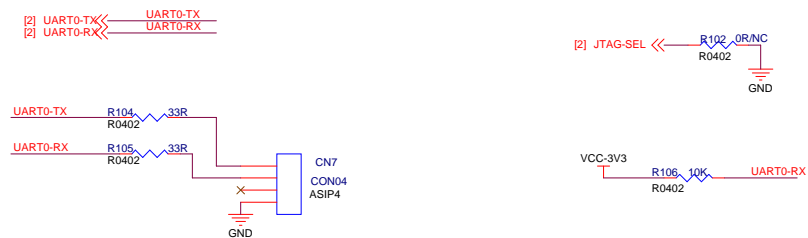
VGA



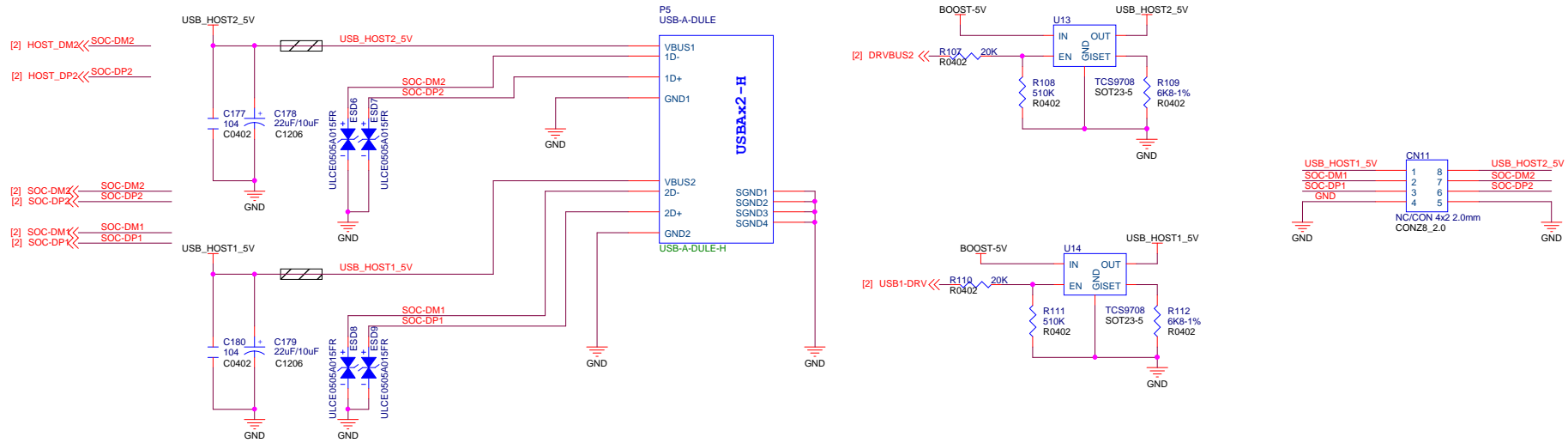
IR MODULE



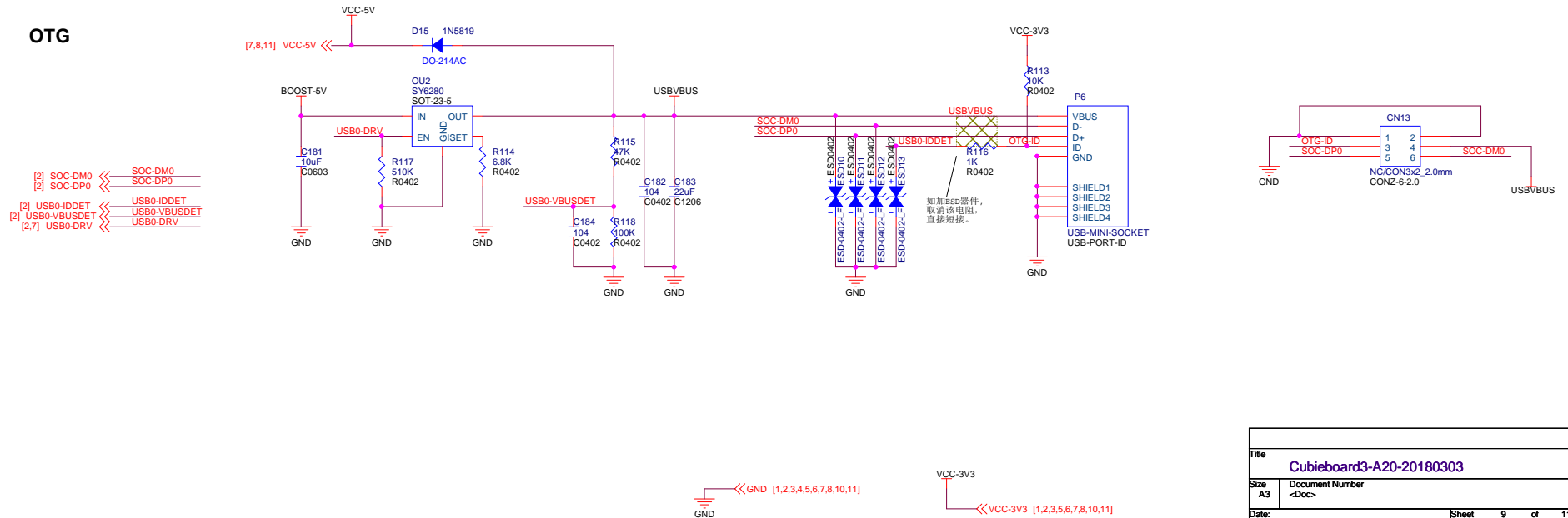
DEBUG



USB HOST x 2

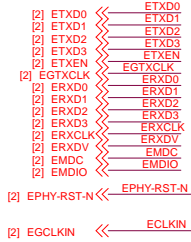


OTG

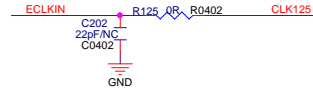


Title		
Cubieboard3-A20-20180303		
Size	Document Number	Rev
A3	<Doc>	V1.0
Date:	Sheet	9 of 11

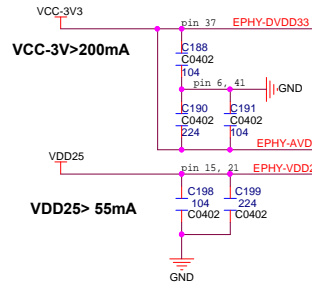
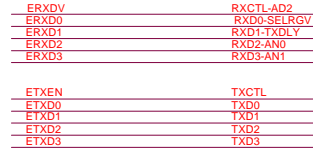
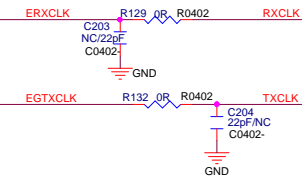
GMAC



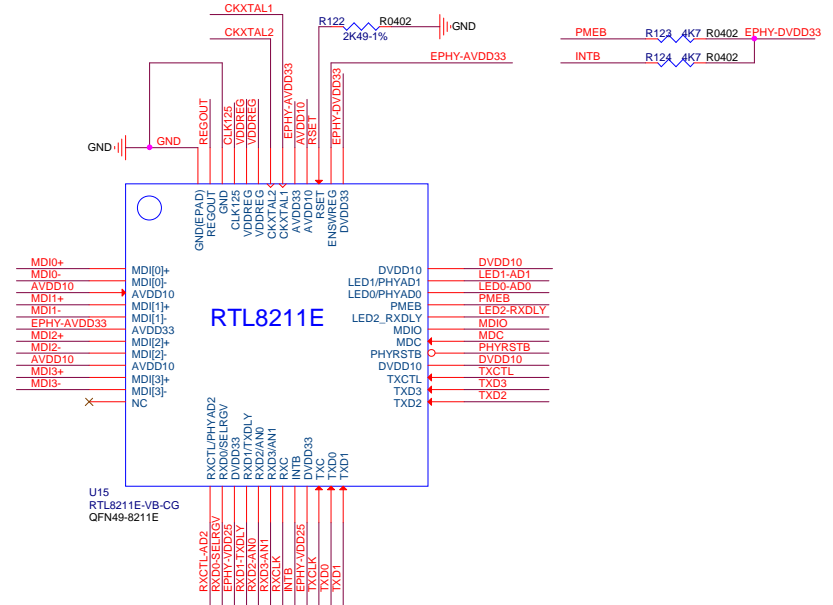
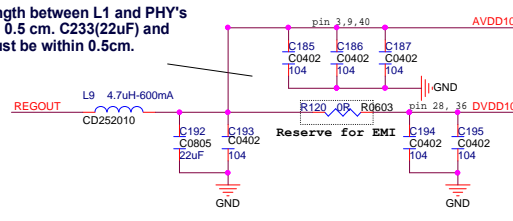
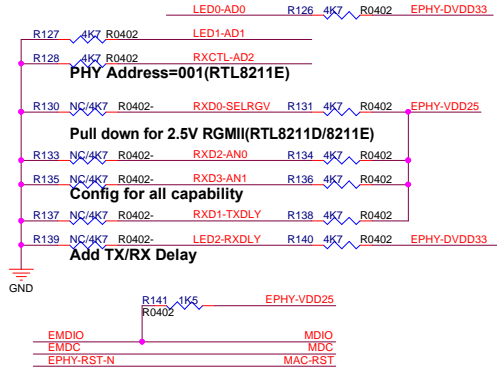
Place filter network close to CLK125.
Reserved for EMI



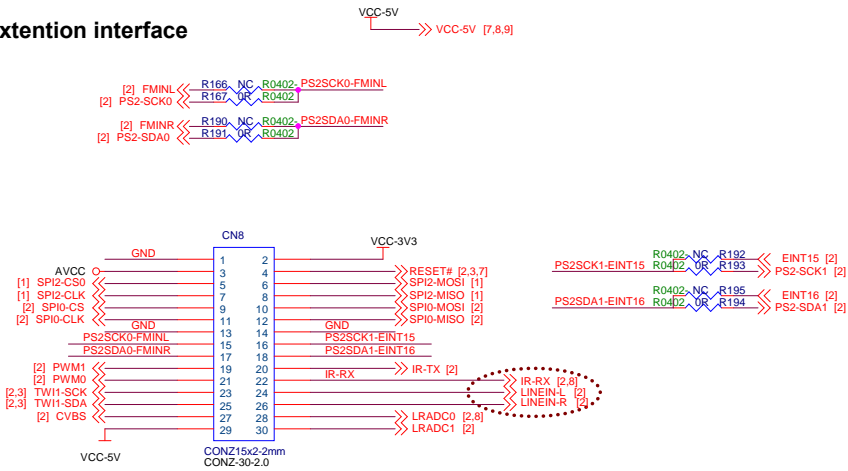
Place filter network close to RX_CLK.
Reserved for EMI



Note 2: The Trace length from C246(22uF), C232(0.1uF) to Pin 44,45(VDDREG) must be within 0.5 cm. The trace width from AVDD33 to Pin 44,45 should>40mils



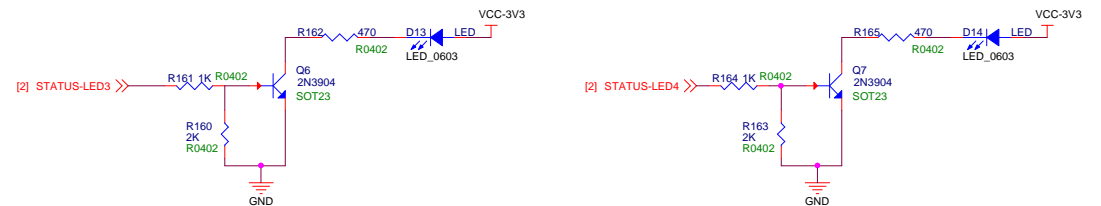
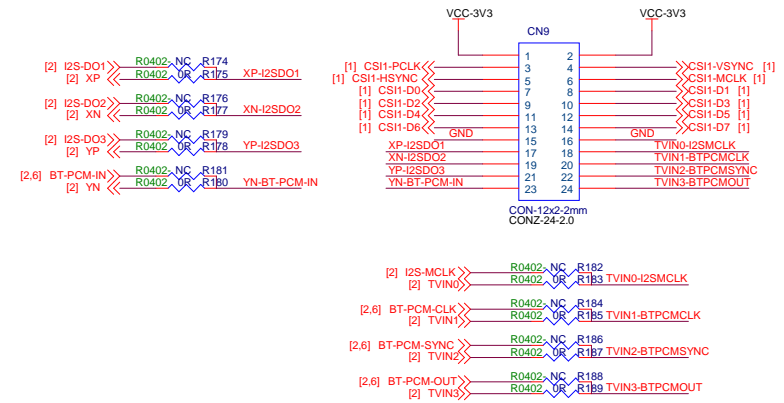
Extention interface



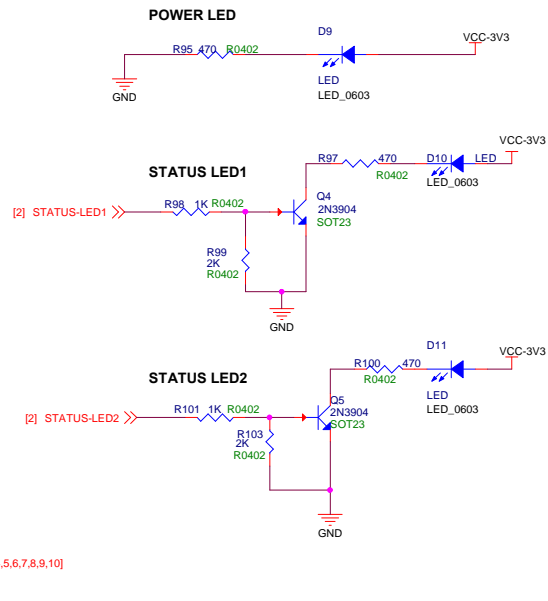
还需要分配GPIO

I2S, I2C, SPI, CVBS, LRADC0 x2, UART, PS2, PWMx2
TS/CSI, IRDA, 2 x LINEIN

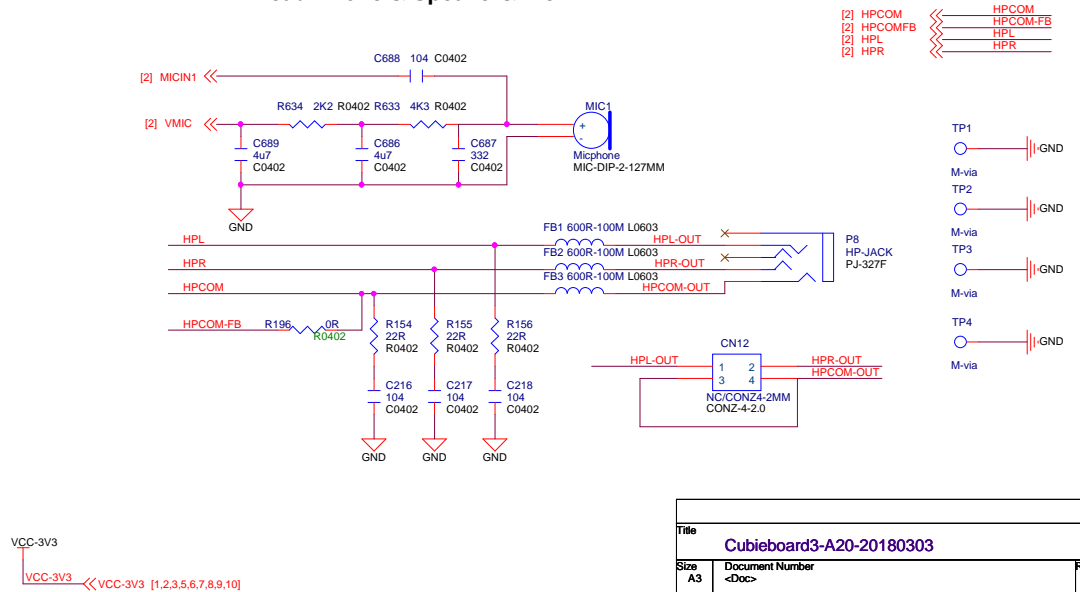
5V, 3.3V, GND



LEDs



Head Phone & Speaker&mic



Title			
Cubieboard3-A20-20180303			
Size A3	Document Number <Doc>		Rev V1.0
Date:	Sheet	11	of 11