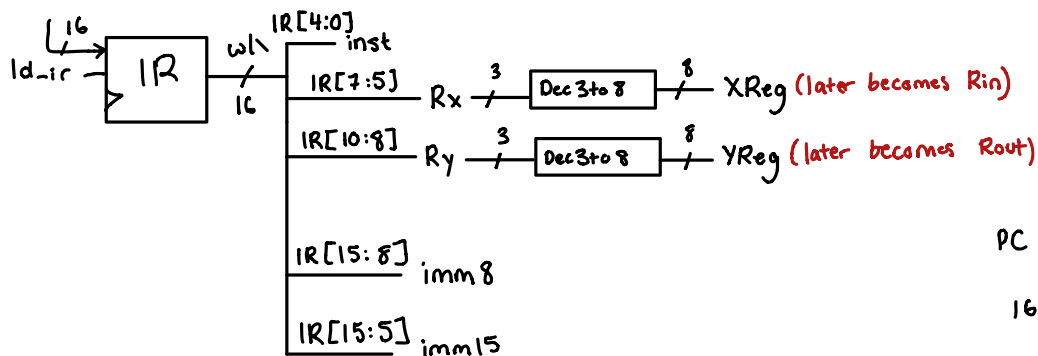


i-mem-rd data



if $wr=0$, rd
 \therefore read from PC
 if $wr=1$, writing
 to add from $r0-r7$
 (buswires)

