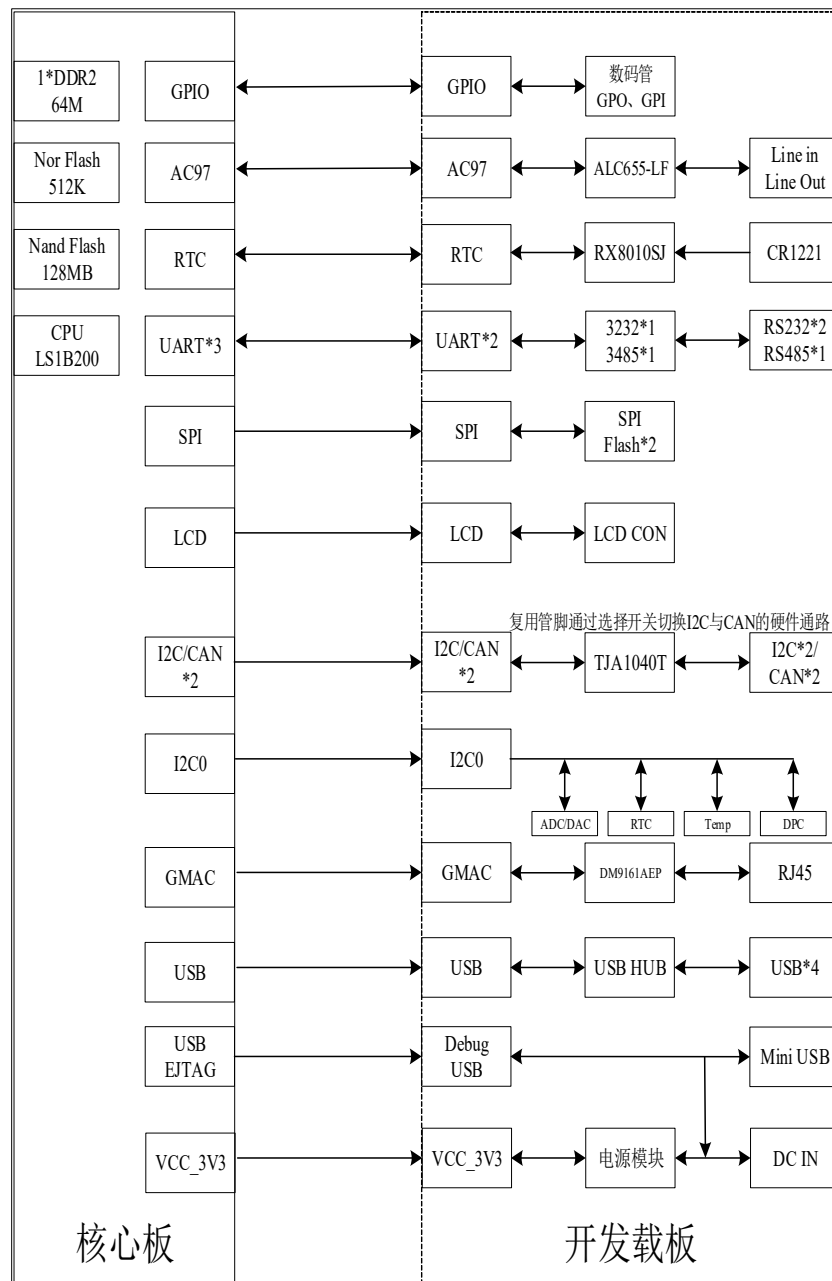
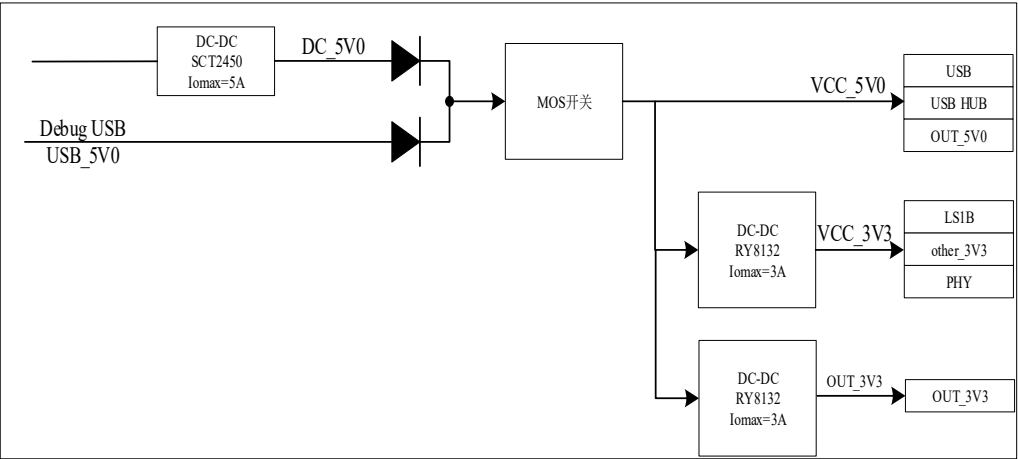


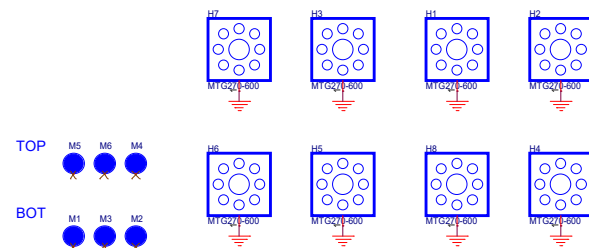
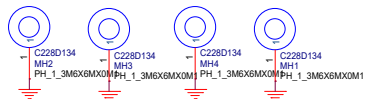
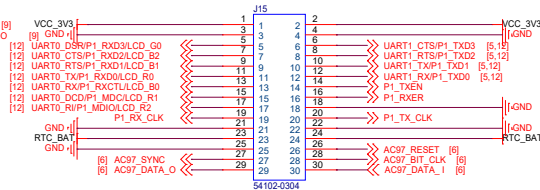
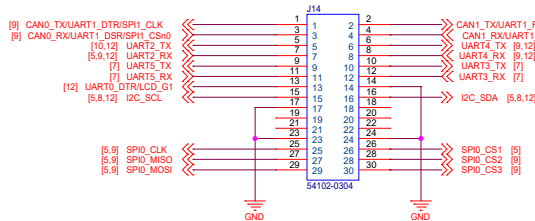
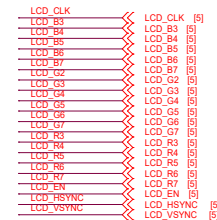
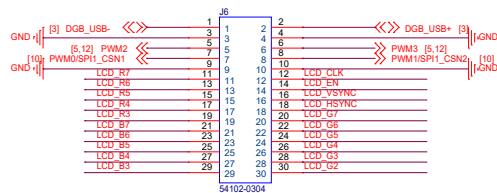
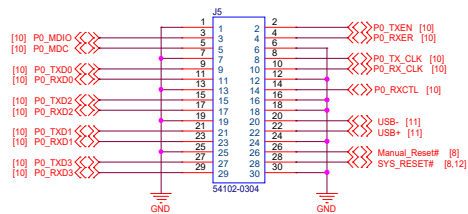
MCZ-PP20-C220



设计	unknown	名称	01_BLOCK_DIAGRAM			
校对	<reviewer>	图号	MCZ-PP20-C220			
审核	<checker>	版本	<RevCode>	图幅	C	第 1 页
批准	<approver>	日期	Tuesday, April 06, 2021		共 13 页	

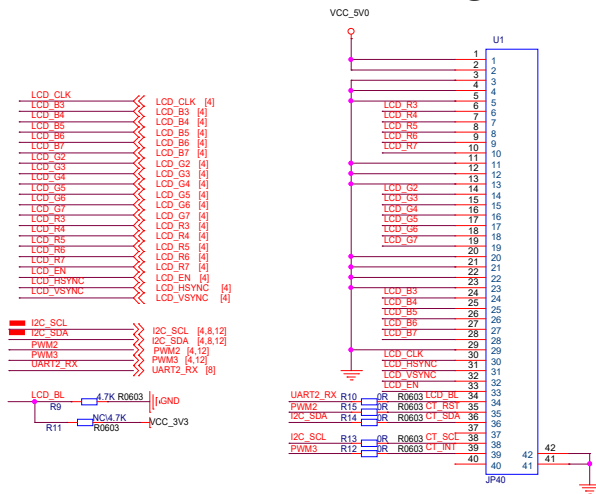


设计	unknown	名称	02_POWER_TREE			
校对	<reviewer>	图号	MCZ-PP20-C220			
审核	<checker>	版本	<RevCode>图幅	C	第 2 页	
批准	<approver>	日期	Tuesday, April 06, 2021		共 13 页	



设计	unknown	名称	04_Core_Board			
校对	<reviewer>	图号	MCZ-PP20-C220			
审核	<checker>	版本	<RevCode>图幅	C	第 4 页	
批准	<approver>	日期	Tuesday, April 06, 2021		共 13 页	

LCD

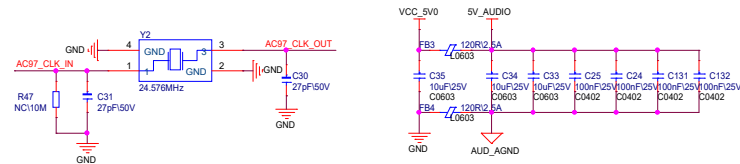


设计	unknown	名称	05_LCD			
校对	<reviewer>	图号	MCZ-PP20-C220			
审核	<checker>	版本	<RevCode>	图幅	C	第 5 页
批准	<approver>	日期	Tuesday, April 06, 2021			共 13 页

AL655-LF

For AL650:JD Block=K
For AL655:JD4=K JD2=0
For AL658:JD4=0 JD2=K

For AL650:JD Block=K
For AL655:JD3=K JD1=0
For AL658:JD3=0 JD1=K



The schematic diagram illustrates the audio output section of the GM4015P-2C. It features four main output paths: LINEOUT, VREFOUT, MIC_IN, and MICIN-JD. Each path is connected to a specific output pin on the GM4015P-2C package.

LINEOUT Section:

- LINEOUT_JD:** Connected to pin 1 of J13. It includes a 10K resistor (R167) and a 1uF/25V capacitor (C121).
- LINE_OUT_R:** Connected to pin 2 of J13. It includes a 1uF/25V capacitor (C121).
- LINE_OUT_L:** Connected to pin 3 of J13. It includes a 1uF/25V capacitor (C121).
- SURR_OUT_R:** Connected to pin 4 of J13. It includes a 1uF/25V capacitor (C121).
- SURR_OUT_L:** Connected to pin 5 of J13. It includes a 1uF/25V capacitor (C121).

VREFOUT Section:

- VREFOUT:** Connected to pin 6 of J13. It includes a 2.2K resistor (R38) and a 1uF/25V capacitor (C121).

MIC_IN Section:

- MIC_IN1:** Connected to pin 7 of J13. It includes a 1uF/25V capacitor (C121).
- MIC_IN2:** Connected to pin 8 of J13. It includes a 1uF/25V capacitor (C121).

MICIN-JD Section:

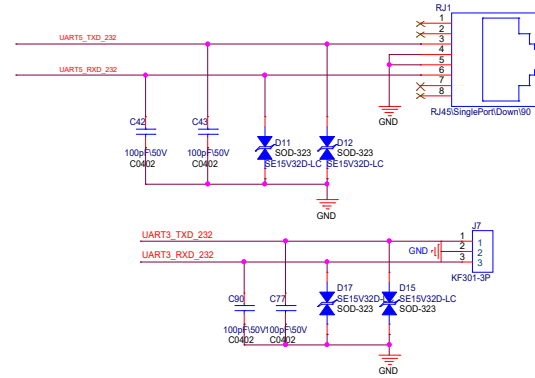
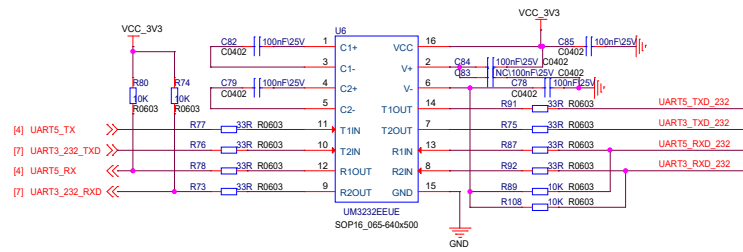
- MICIN-JD:** Connected to pin 9 of J13. It includes a 10K resistor (R174) and a 1uF/25V capacitor (C119).

Other Components:

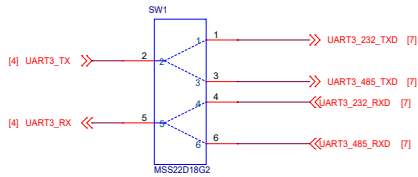
- Resistors:** R170 (22K), R48 (22K), R170 (22K), R48 (22K), R160 (22K), R154 (22K), R40 (22K), R37 (22K), R40 (22K).
- Capacitors:** C124 (100pF), C134 (100pF), C124 (100pF), C134 (100pF), C114 (100pF), C108 (100pF), C23 (100pF), C20 (100pF).
- Diodes:** D27 (WPE3V3030ULA), D28 (WPE3V3030ULA), D9 (WPE3V3030ULA), D8 (WPE3V3030ULA).
- Inductors:** 120R2.5A, 120R2.5A, 120R2.5A, 120R2.5A.
- Other:** SOD-323, SOD-323, SOD-323, SOD-323, SOD-323, SOD-323.

设计	unknown	名称	06_AC97			
校对	<reviewer>	图号	MCZ-PP20-C220			
审核	<checker>	版本	<RevCode>	图幅	C	第 6 页
批准	<approver>	日期	Tuesday, April 06, 2021			共 13 页

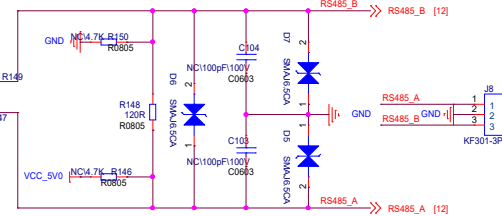
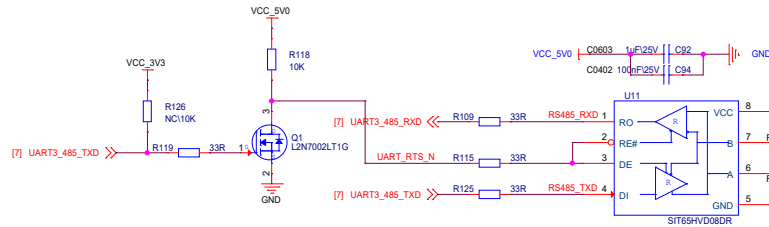
RS232



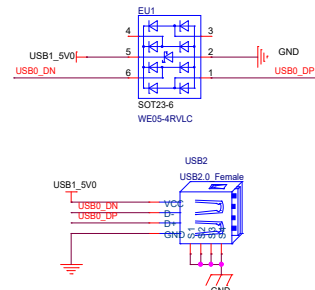
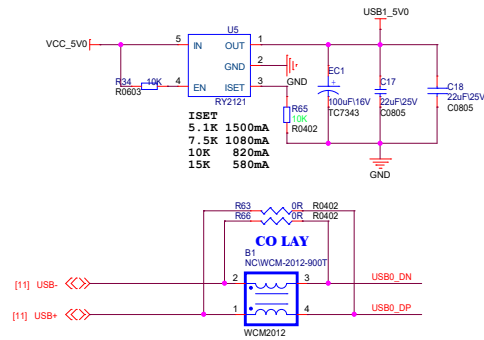
双联切换开关



RS485

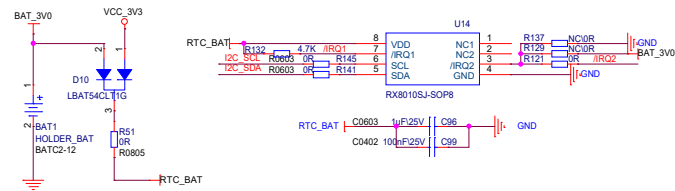


USB

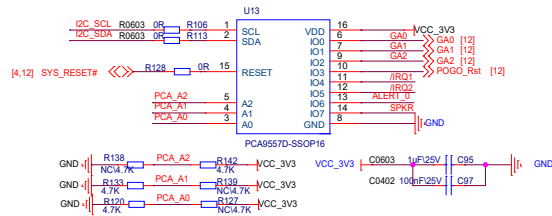


设计	unknown	名称	07_UART_USB			
校对	<reviewer>	图号	MCZ-PP20-C220			
审核	<checker>	版本	<RevCode>图幅	C	第 7 页	
批准	<approver>	日期	Tuesday, April 06, 2021		共 13 页	

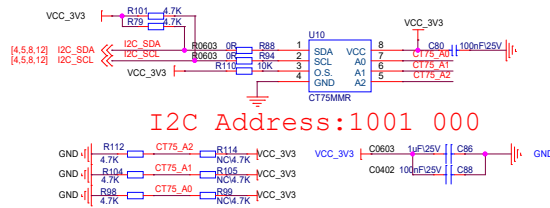
RTC



I2C GPIO

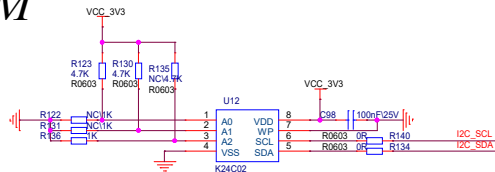


TEMP

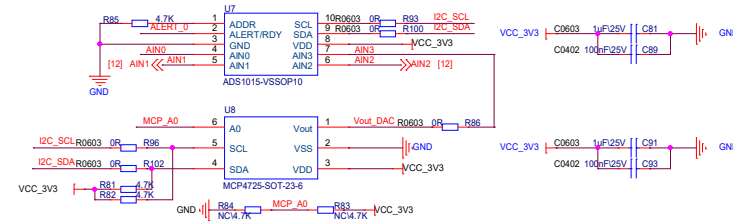
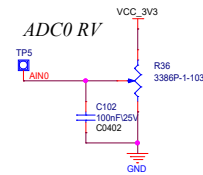


I2C Address:1001 000

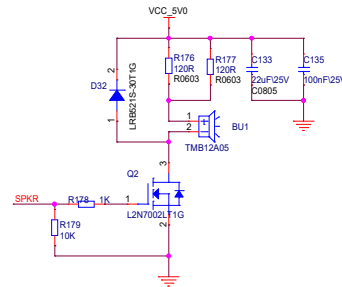
EEPROM



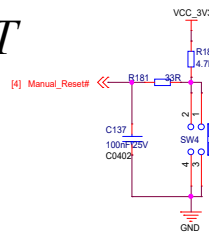
ADC/DAC



BUZZER

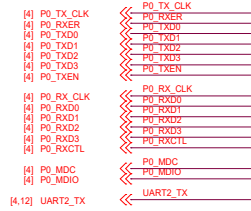


RESET



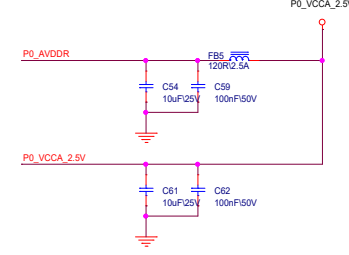
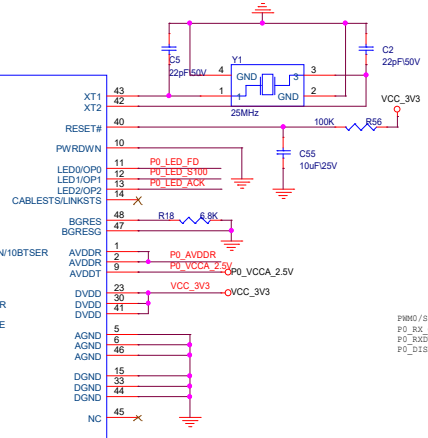
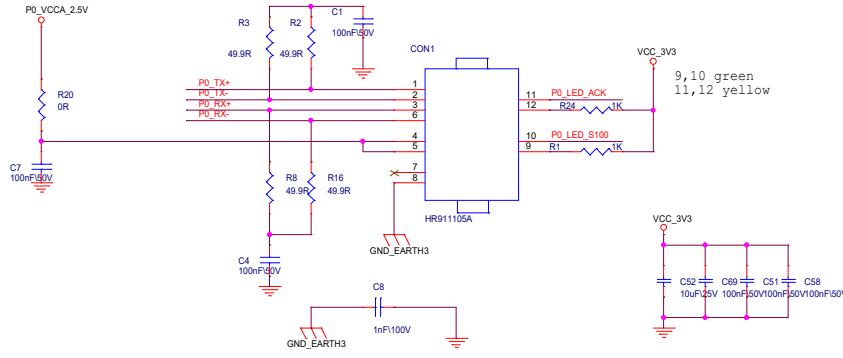
设计	unknown	名称	08_ADC_DAC_RTC			
校对	<reviewer>	图号	MCZ-PP20-C220			
审核	<checker>	版本	<RevCode>图幅	C	第 8 页	
批准	<approver>	日期	Tuesday, April 06, 2021		共 13 页	

LAN

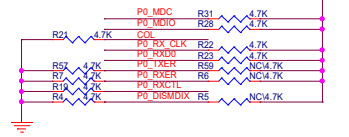


COL 0 MII
1 RMII
when power up

CRS and COL are used for half-duplex as output,
and they are used for input as power up!

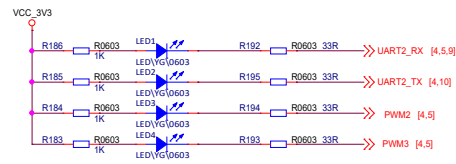


PWM0/SP1_CS1 is pulled down because of choosing the MII mode
P0_RX_CLK is pulled up because of choosing the MII mode in 10Mbps
P0_RXD0 is pulled down, indicates the normal operation
P0_DISMDIX is pulled down indicates that enable SR Auto-MDIX mode

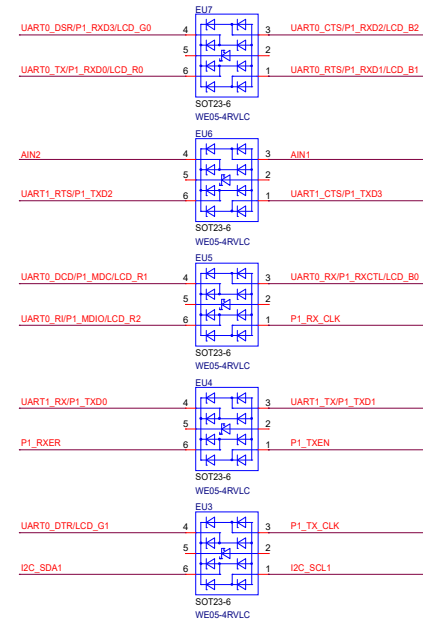
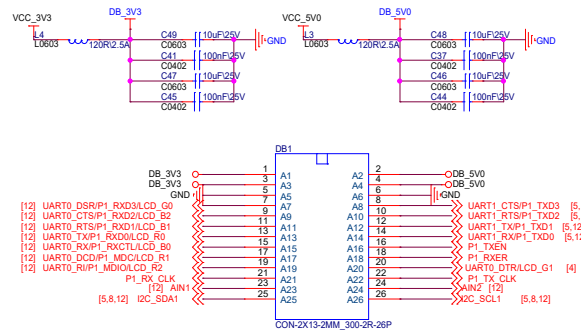
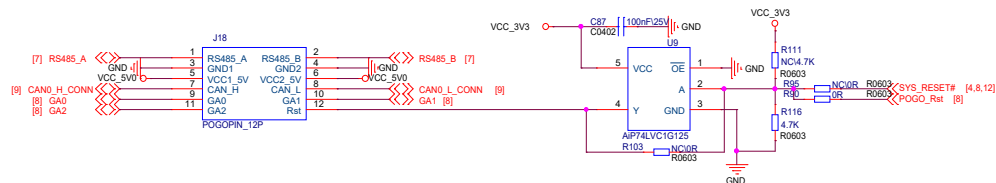
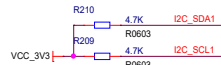
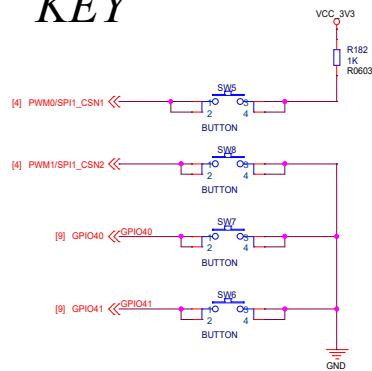


设计	unknown	名称	10_GMAC			
校对	<reviewer>	图号	MCZ-PP20-C220			
审核	<checker>	版本	<RevCode>	图幅	C	第 10 页
批准	<approver>	日期	Tuesday, April 06, 2021		共 13 页	

GPO



KEY



设计	unknown	名称	11_GPIO		
校对	<reviewer>	图号	MCZ-PP20-C220		
审核	<checker>	版本	<RevCode>图幅	C	第 12 页
批准	<approver>	日期	Tuesday, April 06, 2021 共 13 页		

MCZ-PP20-C220

Revision History

Date	Revision	Change
2021-02-06	A00	

设计	unknown	名称	12_Revision History		
校对	<reviewer>	图号	MCZ-PP20-C220		
审核	<checker>	版本	<RevCode>图幅	C	第 13 页
批准	<approver>	日期	Tuesday, April 06, 2021		共 13 页