初始化复位和时钟控制RCC的相关寄存器

寄存器I/O port clock enable register(RCC\_IOPENR)

MOVS R1, #0x00000040

MOVS R3, #12

LSLS R1, R3

MOVS R3, #0x00000021

ADDS R1, R3

MOVS R3, #12

LSLS R1, R3 ;Base address #40021000

MOVS R2, #0x34 ;Address offset #0x34

MOVS R0, #0x01 ;Set value#0x01

STR R0, [R1,R2]

初始化GPIO输出模式，寄存器GPIO port mode regester

MOVS R1, #0x00000050

MOVS R3, #24

LSLS R1, R3 ;Base address #50000000

MOVS R2, #0x00 ;Address offset #0x00

MOVS R0, #0xEB

MOVS R3, #8

LSLS R0, R3

MOVS R3, #0xFF

ADDS R0, R3

MOVS R3, #8

LSLS R0, R3

MOVS R3, #0xF7

ADDS R0, R3

MOVS R3, #8

LSLS R0, R3

MOVS R3, #0xFF

ADDS R0, R3 ;Set value #0xEBFFF7FF

STR R0, [R1,R2]

LD4闪烁运行部分

LEBEL ;The big cycle,LD4 twinlle

MOVS R4, #0xFF ;The number one cycle,LD4 light

MOVS R3, #8

LSLS R4, R3

LEBEL1

MOVS R1, #0x00000050

MOVS R3, #24

LSLS R1, R3

MOVS R2, #0x14

MOVS R0, #0x20

STR R0, [R1,R2] ;failing

SUBS R4, #1

CMP R4, #1

BGE LEBEL1

MOVS R5, #0xFF ;The number two cycle,LD4 don't light

MOVS R3, #8

LSLS R5, R3

LEBEL2

MOVS R1, #0x00000050

MOVS R3, #24

LSLS R1, R3

MOVS R2, #0x14

MOVS R0, #0x00

STR R0, [R1,R2]

SUBS R5, #1

CMP R5, #1

BGE LEBEL2

B LEBEL ;back to the big cycle