1 Macros

\litmus{<name>} - adds a figure of the litmus, if one does not already exist, typesets the litmus name and hyperlinks it to the figure, like this: MP (Figure 1).

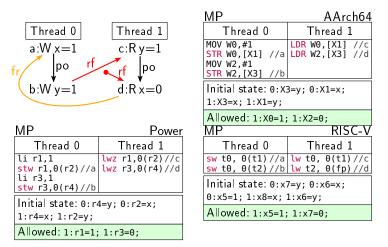


Figure 1: Litmus test MP

\litmus+{<name>} - similar to \litmus{<name>} except that it forces a new figure, even if a previous one already exists.

 $\label{liming-second} $$ - similar to <math>\label{liming-second} \end{substitute} $$ - similar to \limin{substitute} \end{su$

\litmus*{<name>} - just typesets the litmus name (without a figure or a link), like this: MP.

\litmusref{<name>} - reference a litmus figure, like this: Figure 1 (the MP litmus figure).

2 litmuscfg

The followings are options that can be changed by \litmuscfg{<options>}.

index - can only be used in the preamble. Loads the index package and generates an index of litmus tests (use \printindex[litmus] to typeset the index).

hyperref - can only be used in the preamble. Loads the hyperref package and links litmus names to their figures. extern=false|true - (initially false, default true) can only be used in the preamble. Externalise the litmus figures, that is, generates a separate pdf for each diagram (once), and then include those pdf in the document instead of typesetting them every time the document is built.

strict translation=false|true - (initially false, default true) be strict when translating litmus name between architectures (strict will fail if an edge has no mapping, non-strict will use the source name of the edge).

default arch=<arch> - (initially GEN) the default value of the optional argument of $\lower_{\rm MP+fens}$ parses the litmus name as a generic (GEN) name and $\lower_{\rm MP+dmb.sys}$ parses the litmus name as an AArch64 name.

target arch=<arch> - (initially empty) the target architecture of litmus names of \litmus (empty means same as the source arch). For example \litmuscfg{target arch=PPC}\litmus*[A64]{MP+dmb.sys} will translate the AArch64 name MP+dmb.sys to the Power name MP+syncs.

target figs=<archs> - (initially empty) a CSV of the architectures that are to appear in litmus diagrams (empty means same as the source arch).

litmus ref=<macro> - (initially equivalent to \ref{#1}) <macor> should take three arguments and typeset a reference to a litmus figure. The arguments are: #1 - label of the litmus figure; #2 - target architecture; #3 - target name.

context=<name> - (initially main) by changing context you can include multiple figures of the same litmus test and reference each one specifically.

exp results midrule=none|<n> - (initially none) determines if a midrule should be drawn every <n> lines in experimental results tables. With no argument the option is enabled with the previous value of <n> (initially 5).

3 litmus.names.cfg

This file defines how litmus names are translated between architectures.

The translation does not change the basic shape name (e.g. MP), which is the prefix of the name made of anything that is not a lower case letter, but not including the last '+' if the prefix ends with one (e.g. the name of 2+2W+dmb.sys is 2+2W).

The suffix of the name is then split to edges, separated by '+' and '-', And each edge is translated according to the mapping.

\litmusedgetrans{<src>}{<er}{<trg1>:<e1>,...} - defines an edge mapping from the source architecture <src> edge <e> to the target architecture <trgn> edge <en> for every n in the list. For example \litmusedgetrans{GEN}{fen}{A64:dmb \litmusedgetransannot{<src>/<trg>}{<s1>/<t1>,...} - defines a mapping for edge annotations from the source architecture <src> to the target architecture <trg>. For example \litmusedgetransannot{GEN/A64}{p/p,aq/a,rl/l}. \newlitmusnametrans{<src>}{<name>}{<trg>}{<name'>} - defines a special case for the architecture <src> litmus <name> and target architecture <trg>. For example: \newlitmusnametrans{GEN}{MP+addrs}{A64}{MP+addrs+V2}. \renewlitmusnametrans{<src>}{<name>}{<trg>}{<name>}{<trg>}{<name}}{<name}

Index of Litmus Tests

Numbers written in bold refer to the page where a figure of the litmus test is displayed.

Generic MP, 1, 1 AArch64 MP, 1 MP+dmb.sys, 1 Power MP, 1 MP+syncs, 1 RISC-V MP, 1