

# 1. Description

## 1.1. Project

Project Name	CompositeBuilderExample	
Board Name	NUCLEO-L412KB	
Generated with:	STM32CubeMX 6.7.0	
Date	08/10/2023	

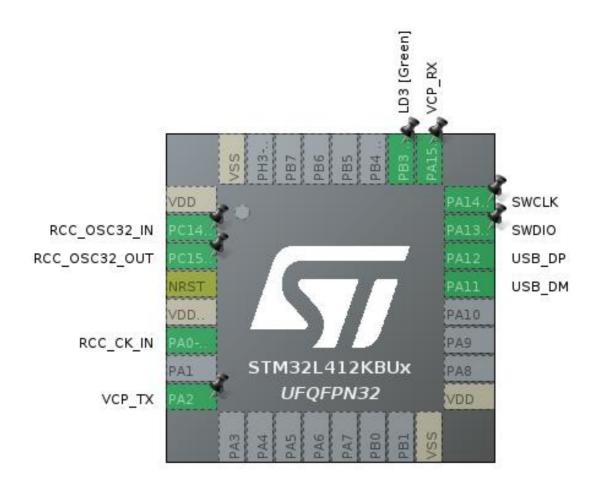
### 1.2. MCU

MCU Series	STM32L4
MCU Line	STM32L4x2
MCU name	STM32L412KBUx
MCU Package	UFQFPN32
MCU Pin number	32

## 1.3. Core(s) information

Core(s)	Arm Cortex-M4

# 2. Pinout Configuration

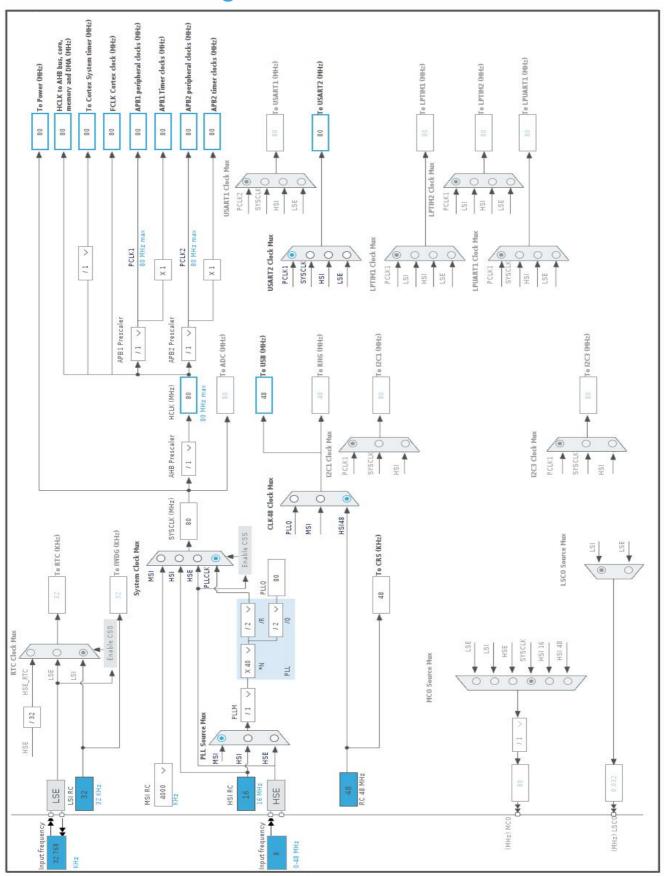


# 3. Pins Configuration

Pin Number UFQFPN32	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VDD	Power		
2	PC14-OSC32_IN (PC14)	I/O	RCC_OSC32_IN	
3	PC15-OSC32_OUT (PC15)	I/O	RCC_OSC32_OUT	
4	NRST	Reset		
5	VDDA/VREF+	Power		
6	PA0-CK_IN	I/O	RCC_CK_IN	
8	PA2	I/O	USART2_TX	VCP_TX
16	VSS	Power		
17	VDD	Power		
21	PA11	I/O	USB_DM	
22	PA12	I/O	USB_DP	
23	PA13 (JTMS/SWDIO)	I/O	SYS_JTMS-SWDIO	SWDIO
24	PA14 (JTCK/SWCLK)	I/O	SYS_JTCK-SWCLK	SWCLK
25	PA15 (JTDI)	I/O	USART2_RX	VCP_RX
26	PB3 (JTDO/TRACESWO) *	I/O	GPIO_Output	LD3 [Green]
32	VSS	Power		

<sup>\*</sup> The pin is affected with an I/O function

## 4. Clock Tree Configuration



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# 5. Software Project

## 5.1. Project Settings

Name	Value	
Project Name	CompositeBuilderExample	
Project Folder	/media/Projekte/037/037083/Versuche/CompositeBuilderExample	
Toolchain / IDE	STM32CubeIDE	
Firmware Package Name and Version	STM32Cube FW_L4 V1.17.2	
Application Structure	Advanced	
Generate Under Root	Yes	
Do not generate the main()	No	
Minimum Heap Size	0x2000	
Minimum Stack Size	0x400	

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

### 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name	
1	SystemClock_Config	RCC	
2	MX_GPIO_Init	GPIO	
3	MX_USART2_UART_Init	USART2	
4	MX_USB_DEVICE_Init	USB_DEVICE	

# 6. Power Consumption Calculator report

### 6.1. Microcontroller Selection

Series	STM32L4
Line	STM32L4x2
MCU	STM32L412KBUx
Datasheet	DS12469_Rev0

### 6.2. Parameter Selection

Temperature	25
Vdd	3.0

### 6.3. Battery Selection

Battery	Li-SOCL2(AAA700)
Capacity	700.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	10.0 mA
Max Pulse Current	30.0 mA
Cells in series	1
Cells in parallel	1

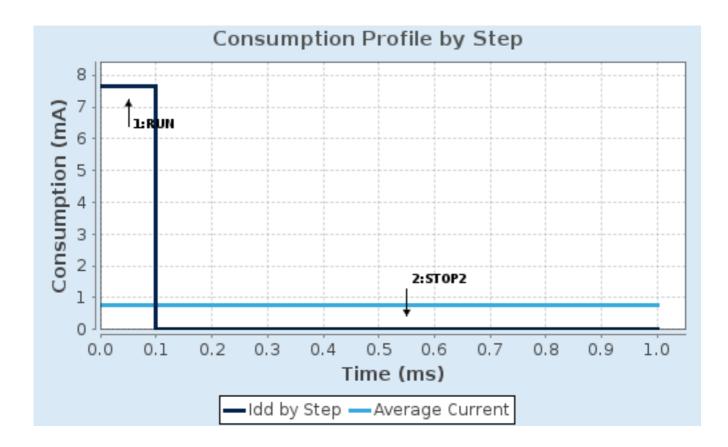
## 6.4. Sequence

Step	Step1	Step2
<u>Mode</u>	RUN	STOP2
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoRange
Fetch Type	FLASH	NA
CPU Frequency	80 MHz	0 Hz
Clock Configuration	HSE BYP PLL	ALL CLOCKS OFF Regulator ULP
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	7.65 mA	815 nA
Duration	0.1 ms	0.9 ms
DMIPS	100.0	0.0
Ta Max	104.1	105
Category	In DS Table	In DS Table

### 6.5. Results

Sequence Time	1 ms	Average Current	765.73 µA
Battery Life	1 month, 7 days,	Average DMIPS	100.0 DMIPS
	15 hours		

### 6.6. Chart



## 7. Peripherals and Middlewares Configuration

#### 7.1. RCC

mode: High Speed Clock (HSE)

Low Speed Clock (LSE): Crystal/Ceramic Resonator

**CRS SYNC: CRS SYNC Source USB** 

7.1.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Disabled
Data Cache Enabled

Flash Latency(WS) 4 WS (5 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value

MSI Calibration Value

0

MSI Auto Calibration

HSE Startup Timout Value (ms)

LSE Startup Timout Value (ms)

5000

LSE Drive Capability

LSE oscillator low drive capability

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

**CRS Parameters:** 

CRS Synchro Divider 1

CRS Synchro Polarity Active on rising edge

CRS Synchro Reload Value Type Automatic
CRS Synchro frequency (Hz) 1000
Error limit Value 34
HSI48 Calibration Value 32

#### 7.2. SYS

**Debug: Serial Wire** 

**Timebase Source: SysTick** 

**7.3. USART2** 

**Mode: Asynchronous** 

#### 7.3.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples
Single Sample Disable

**Advanced Features:** 

Auto Baudrate Disable Disable TX Pin Active Level Inversion RX Pin Active Level Inversion Disable Disable Data Inversion TX and RX Pins Swapping Disable Overrun Enable DMA on RX Error Enable MSB First Disable

#### 7.4. USB

mode: Device (FS)

#### 7.4.1. Parameter Settings:

#### **Basic Parameters:**

Speed Full Speed 12MBit/s

Physical interface Internal Phy
Sof Enable Disabled

**Power Parameters:** 

Low PowerDisabledLink Power ManagementDisabledBattery ChargingDisabled

#### 7.5. USB DEVICE

Class For FS IP: Custom Human Interface Device Class (HID)

2 \*

#### 7.5.1. Parameter Settings:

**Class Parameters:** 

CUSTOM\_HID\_FS\_BINTERVAL 0x1 \*

USBD\_CUSTOM\_HID\_REPORT\_DESC\_SIZE (Total length for Report descriptor (IN HID MULTI TOUCH REPORT ENDPOINT)) \_DESCRIPTOR\_SIZE+0 \*

 ${\tt USBD\_CUSTOMHID\_OUTREPORT\_BUF\_SIZE}~({\tt Maximum~report~buffer~size}~({\tt OUT}$ 0x40 \*

ENDPOINT))

**Basic Parameters:** 

USBD\_MAX\_NUM\_INTERFACES (Maximum number of supported interfaces)

USBD\_MAX\_NUM\_CONFIGURATION (Maximum number of supported configuration)

USBD\_MAX\_STR\_DESC\_SIZ (Maximum size for the string descriptors) 512

USBD\_SELF\_POWERED (Enabled self power) Disabled \*

USBD\_DEBUG\_LEVEL (USBD Debug Level) 0: No debug message

USBD\_LPM\_ENABLED (Link Power Management) 0: Link Power Management not supported \*

7.5.2. Device Descriptor:

**Device Descriptor:** 

VID (Vendor IDentifier) 0x05BD \*

LANGID\_STRING (Language Identifier) English(United States)

MANUFACTURER\_STRING (Manufacturer Identifier) RAFI GmbH & Co. KG \*

**Device Descriptor FS:** 

PID (Product IDentifier) 0x3104 \*

PRODUCT\_STRING (Product Identifier) **RAFI Glasscape Touch \*** 

CONFIGURATION\_STRING (Configuration Identifier) Custom HID Config Custom HID Interface

INTERFACE\_STRING (Interface Identifier)

\* User modified value

# 8. System Configuration

## 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PC14- OSC32_IN (PC14)	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T (PC15)	RCC_OSC32_O UT	n/a	n/a	n/a	
	PA0-CK_IN	RCC_CK_IN	n/a	n/a	n/a	
SYS	PA13 (JTMS/SWDI O)	SYS_JTMS- SWDIO	n/a	n/a	n/a	SWDIO
	PA14 (JTCK/SWC LK)	SYS_JTCK- SWCLK	n/a	n/a	n/a	SWCLK
USART2	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	VCP_TX
	PA15 (JTDI)	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	VCP_RX
USB	PA11	USB_DM	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA12	USB_DP	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
GPIO	PB3 (JTDO/TRA CESWO)	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LD3 [Green]

### 8.2. DMA configuration

nothing configured in DMA service

## 8.3. NVIC configuration

## 8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority		
Non maskable interrupt	true	0	0		
Hard fault interrupt	true	0	0		
Memory management fault	true	0	0		
Prefetch fault, memory access fault	true	0	0		
Undefined instruction or illegal state	true	0	0		
System service call via SWI instruction	true	0	0		
Debug monitor	true	0	0		
Pendable request for system service	true	0	0		
System tick timer	true	0	0		
USB event interrupt through EXTI line 17	true	0	0		
PVD/PVM1/PVM3/PVM4 interrupts through EXTI lines 16/35/37/38	unused				
Flash global interrupt	unused				
RCC global interrupt	unused				
USART2 global interrupt	unused				
FPU global interrupt	unused				
CRS global interrupt	unused				

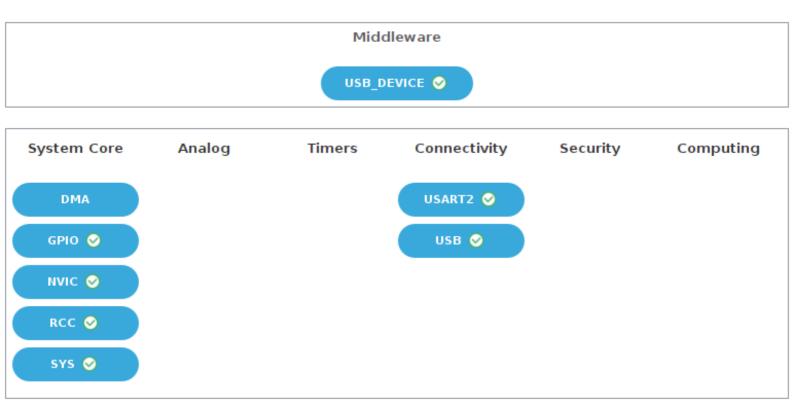
## 8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
USB event interrupt through EXTI line 17	false	true	true

#### \* User modified value

# 9. System Views

- 9.1. Category view
- 9.1.1. Current



### 10. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32l4\_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis\_model/stm32l4\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32l4\_svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

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microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

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recommendations-and-package-information-for-leadfree-ecopack-mcus-

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