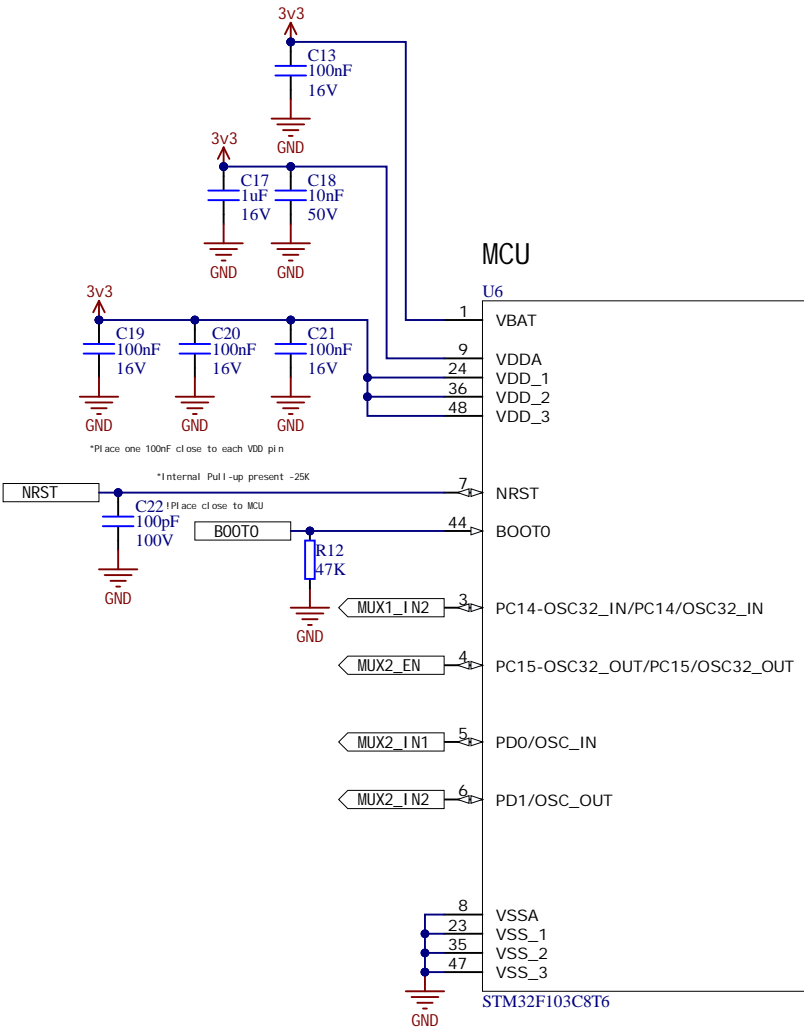
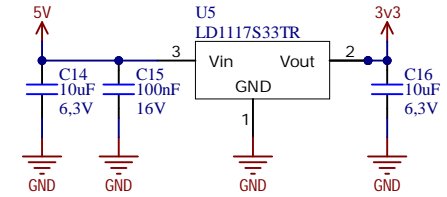
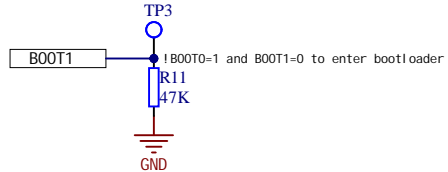


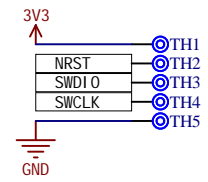
LD0 Power supply



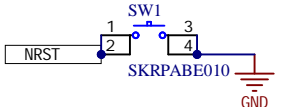
#Expansion (optional)



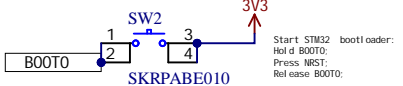
ISP/Debug STLINK V3



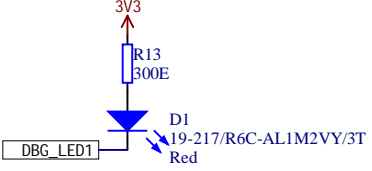
Reset Button



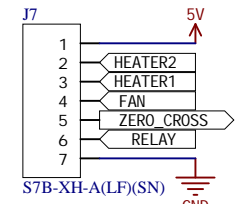
Bootloader Button



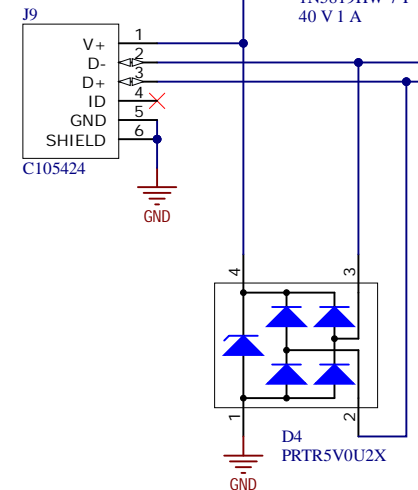
Debug LED



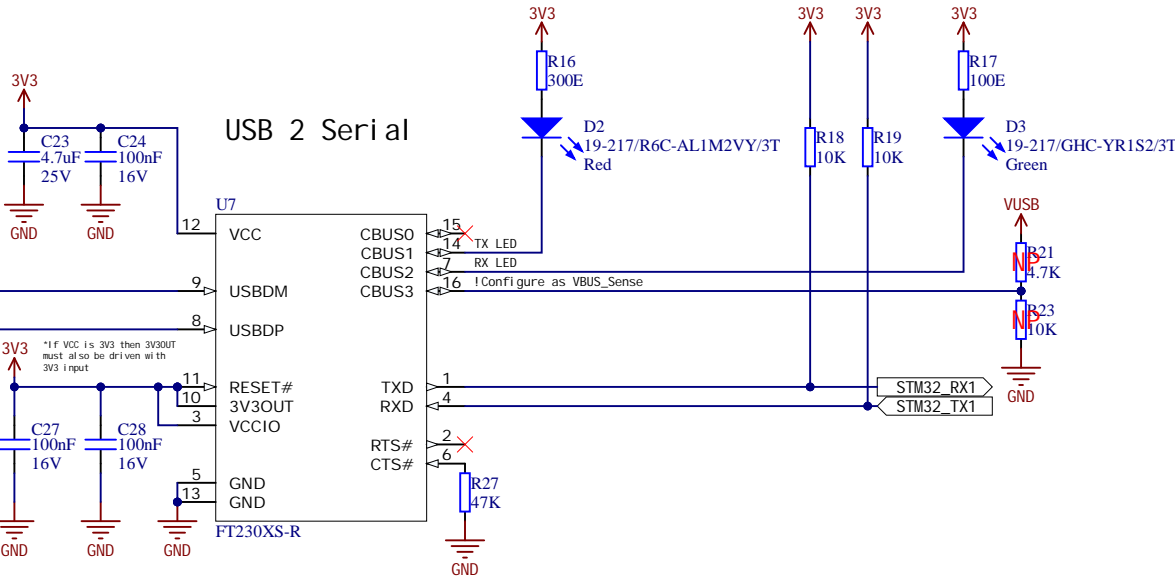
Power board Connector



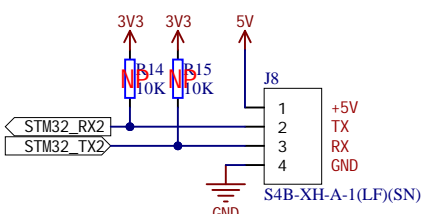
Mini USB



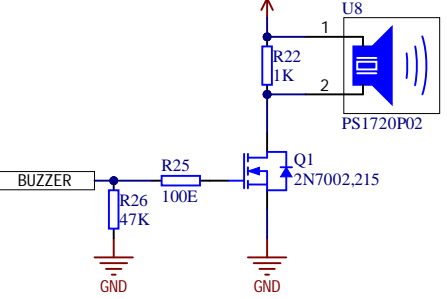
USB 2 Serial



HMI Display



Buzzer



Project: Vapour Phase Control board	Title: Main	Revision: V01
Client: Open Source	Number: P200621	Size: A3
Date: 30-7-2020	Sheet: 1 of 2	
File: P200621V01-01.SchDoc	Author: Tom Klijn	

RTD Interface Notes:  
For 2-wire configuration use an external wire to short IFORCE+ to VSENSE+ at the terminal

PCB Layout Notes:  
1. Place MAX31865 close to RTD terminal strip to minimize errors due to trace resistance  
2. Use thick traces (more copper) for all 4 traces to RTD terminal strip  
3. Balance trace lengths for all 4 traces to RTD terminal strip  
4. Decoupling cap close to pin

