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Roll No

EX-304

B.E. III Semester

Examination, December 2016

Electronics Devices

Time : Three Hours

Maximum Marks : 70

- Note:* i) Attempt any five questions.
ii) All questions carry equal marks.

1. a) Define Cut-in and Breakdown voltages in Si and Ge diodes. 7
b) Explain the capacitances found in a p-n junction diode. 7
2. a) Draw a clipper circuit which clip off the I/P sine wave at two independent levels. 7
b) Draw and explain the use of zener diode as a voltage regulator. 7
3. a) Draw and explain construction and applications of Schottky diode. 7
b) Differentiate LED and Photo diodes. 7
4. a) Draw and explain Ebers Moll model of a transistor. 7
b) Differentiate CE,CB and CC Configuration of BJT. 7
5. a) Draw and explain input and output characteristics of CE configuration. 7
b) Draw and explain early effect and punch through. 7

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6. a) With the help of diagrams explain different biasing techniques. 7
b) Explain Miller capacitance and its effect on voltage gain. 7
7. a) Explain construction and features of FET. 7
b) Differentiate depletion and Enhancement MOSFET. 7
8. Write short notes on any two 14
a) h parameters
b) UJT
c) Comparators
d) Clampers

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