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Roll No

MEVD-302(A) M.E./M.Tech. III Semester

Examination, December 2016

Communication RF IC Design (Elective-IV)

Time: Three Hours

Maximum Marks: 70

Note: Attempt any five questions. All questions carry equal marks.

- Give a brief on measurement techniques used in RF design.
 - Give the definition and properties of S-parameters. What do you understand by reduced order models of S-parameters?
- What are the practical issues for modeling with S-parameters?
 - Discuss the applications of S-parameters.
- Discuss briefly about the trans-receiver architecture for wireless communication.
 - Discuss the effect of non linearities on a nonlinear, memory less time variant system.
- Explain how the receiver is desensitized by the interferer. Also explain blocking.
 - b) What is Cross modulation? Explain IMD and inter symbol interface.

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PTO

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- 5. a) Discuss the design concepts of RF low noise amplifiers and Mixers.
 - b) Discuss the design of oscillators and synthesisers.
- Discuss the design concepts of power amplifiers.
 - b) Explain briefly about the noises and distortions in LNA, PA and Mixers.
- With the help of circuit diagram. Explain the concept of phase lock loop.
 - Discuss the single chip radio concept.
- Write short notes on any two of the following:
 - a) DECT

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- **GSM**
- Bluetooth