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Roll No

MEVD-301(B)**M.E./M.Tech., III Semester**

Examination, November 2018

System On Chip (SOC) Design**(Elective-IV)****Time : Three Hours****Maximum Marks : 70**

- Note:** i) Attempt any five questions.
ii) All questions carry equal marks.

1. a) Transcribe the different applications of Embedded Systems which pressure the system level hardware and software co-design. 7
b) Details about the logic synthesis of SOC. 7
2. Disclose the following:
a) Tools used to design SOC 7
b) HDL operation 7
3. a) Sketch the block diagram of ARM SOC Architecture and explain. 7
b) Analyze the features of ARM. 7
4. Outline the following:
a) Give the detail of Recent advances in semi conductor Technology. 7
b) Sketch the FPGA block diagram and give features. 7

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5. a) Particular about the Tools and Techniques are used to implement the SOC. 7
b) Documentation about FPGA development board. 7
6. Make a Record on:
a) Memory Hierarchy 7
b) 2x2 Multiplier 7
7. a) Reveal the simulation in HDL, Also write down the steps which verifies simulation in model sin. 7
b) Describe about very log synthesizable sub set. 7
8. a) What are the difference between CISC and RISC. 7
b) Characterize logic synthesis of SOC. 7

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