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Roll No .....

**MEVD-105****M.E./M.Tech. I Semester**

Examination, June 2017

**Embedded Micro Controller Programming***Time : Three Hours**Maximum Marks : 70*

- Note:-** i) Attempt any Five questions.  
ii) All questions carry equal marks.

1. a) Discuss briefly about pipelined and very long instruction word processor.  
b) Explain briefly about functioning and architecture of Microcontrollers.
2. a) Discuss briefly about embedded processors in VLSI circuit.  
b) Discuss the architectural issue of CISC and RISC processors.
3. a) Discuss about Harvard Architectures.  
b) Explain the principle of EEPROM, FLASH and SRAM memories.
4. a) Discuss about Interfacing using Glue logic.  
b) Discuss about parallel communication protocol and wireless protocol.

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5. a) Explain the working and architecture of 8051.  
b) Explain the working and architecture of USART.

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6. a) Discuss about interfacing of Microcontroller using SPI, PWM and WDT.  
b) Discuss about CAN Bus architecture.
7. a) Give a brief on ARM architecture and organization.  
b) Differentiate between ARM7 and ARM9.
8. Write short notes on any two of the following:
  - a) DSP processor.
  - b) EDORAM and DDRAM.
  - c) 68HC11.

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