Roll No EC - 803 **B.E. VIII Semester** Examination, June, 2013 Nanoelectronics Time: Three Hours Maximum Marks: 100 Minimum Pass Marks 435 *Note:* i) Attempt any one question from each unit. ii) All questions carry equal marks. Unit 1 20 Define the following: Nanoelectronics. a) b) Nanotechnology. Top down and bottom up approach. c) Energy bands in Solids. d) OR 2. Explain how insulators, conductors and semiconductors a) differ with respect to energy band diagram? 10 Derive the AC electrical conductivity of a metal according b) to Drude model. 10 Unit 2 3. Define the following: 20 Quantum Wells. a) Quantum Wires b) Quantum dots c) Nano particles.

d)

4.	. a)	Discuss the potential energy profile for metal-instructions.	
	b)		10
	0)	triplant the applications of tulinening.	10
5	(ه	Unit 3	
5.	a)	Explain what is coulomb blockade? Explain coublockade in quantum dot circuit.	ılomb 10
	b)	Explain the formation and working of single electransistor. (SET).	ectron 10
		OR	10
6.	a)	Give an account of carbon nanotube FET.	10
	b)	Explain the working principle of molecular SET.	10
		Unit 4	
7.	a)	Explain the free electron gas model of elec-	ctron
		conduction.	10
	b)	Define the following:	10
		i) Quantum resistance and conductance.	
		ii) Electron collisions and length scales.	
		OR	-
8.	a)	What are the basic structure differences between cananotubes and BN nanotubes?	rbon 10
	b)	What are nanodiamond? Describe some of the impo	
		applications of nanodiamond.	10
		Unit 5	
9.	a)	Explain the process of VLS technique.	10
	b)	What is magnetron sputtering? Explain the differe	nces
		between magnetron and conventional sputtering.	10
		OR	
l 0.	a)	Describe various stages of a PLD process.	10
	b)	Give an account of lithographic process. What ar	e its
		limitations.	10
