

Roll No .....

**MCSE-204**  
**M.E./M.Tech. II Semester**  
 Examination, December 2014  
**System Programming**

Time : Three Hours

Maximum Marks : 70

**Note:** Attempt one question from each unit. Assume missing data/value if required. All questions carry equal marks.

**Unit - I**

1. a) Draw flow chart for Instruction Fetch and Execution of General Machine Structure. 7
- b) Give the instruction formats of SIC/ XE machine. 7

OR

2. a) Explain machine independent assembler features. 7
- b) What are the different features of a macro facility? 7

**Unit - II**

3. a) Write about different approaches for compiler development. 7
- b) Discuss register allocation techniques for concurrentisation. 7

OR

4. a) List the various codes optimization techniques. 7
- b) Write short note on symbol table. 7

[2]

**Unit - III**

5. a) Explain compilation process for distributed machine. 7
- b) Write unified algorithm for data flow analysis. 7

OR

6. Write short note on. 14
- i) Code generation for pipelined
- ii) SSA form.

**Unit - IV**

7. a) How deadlock detection in distributed system? 7
- b) Explain about distributed scheduling. 7

OR

8. Explain briefly about 14
- i) RPC
- ii) Message Passing.

**Unit - V**

9. a) Describe about Access Matrix Model. 7
- b) Draw multiprocessor system architecture. 7

OR

10. Write short note on. 14
- i) Amoeba
- ii) Fault Tolerance.

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