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Roll No

MSE-204**M.E./M.Tech., II Semester**

Examination, December 2015

Software Verification Validation and Testing*Time : Three Hours**Maximum Marks : 70*

- Note:** i) Attempt any five questions.
 ii) All questions carry equal marks.
 iii) Assume Data/Value if required.

1. a) Explain the following : 7
 - i) Error ii) Fault
 - iii) Failure iv) Defect
- b) Explain the different stages of Software Testing Life Cycle. 7
2. a) What is the role of test plans in a V and V diagram? 7
- b) Explain the equivalence class testing technique. How is it different from boundary value analysis technique? 7
3. a) Discuss the guidelines for the creation of use cases for designing of any system. Is there any limit for the number of use cases in any system. 7
- b) Describe about any Reliability Models. 7

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4. a) What are the advantages of static testing as compared to dynamic testing? 7
- b) Explain all the key process areas at each Testing Maturity Model Level. 7
5. a) What are the consequences? If the cyclomatic complexity of a system is greater than 10? As a designer of the system, what strategy will you choose to handle it? 7
- b) Explain about Gorilla Testing. 7
6. a) What is the procedure for performing thread-based integration testing? 7
- b) Make a list of some important testing tools which are open-source on the Internet. Also explain briefly. 7
7. a) What are the roles and responsibilities of a test manager? 7
- b) Describe test strategy format. 7
8. Write short note on : 14
 - a) Win Runner
 - b) Array Box Testing
 - c) Stress Testing
 - d) Alpha and Beta Testing
