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Roll No

CS-601 (GS)**B.E. VI Semester**

Examination, December 2017

**Grading System (GS)
Microprocessor & Interfacing***Time : Three Hours**Maximum Marks : 70*

Note: i) Attempt any five questions.
ii) All questions carry equal marks.

1. Discuss the evolution of microprocessor in detail.
2. Draw a block diagram of microprocessor based system and explain the function of each component.
3. Draw the functional block diagram of 8085 microprocessor and explain function of each block.
4. What is instruction set? Explain 10 instructions of 8085 microprocessor.
5. Draw and discuss internal architecture of 8086 microprocessor.

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6. Write a Assembly language program to convert 16 bit binary number into equivalent BCD number.
7. Explain the features of programmable interrupt controller 8259A.
8. Answer any four of the following:
 - a) Define address bus, data bus and control bus. Why data bus are bidirectional.
 - b) Write a Assembly language program for addition of two 8 bit number.
 - c) Explain the physical memory organization of 8086 microprocessor.
 - d) Explain the significant of different bit of control word register of 8253.
 - e) Discuss the following signal description of 8051.
 - i) $\overline{\text{ALE/PROG}}$
 - ii) $\overline{\text{PSEN}}$
 - iii) $\overline{\text{RD}}$
 - iv) $\overline{\text{WR}}$
 - f) Explain the addressing modes of 8051 microcontroller.
