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Examination, June 2017

Choice Based Credit System (CBCS)
Computer System Organization

Time: Three Hours

Maximum Marks: 60

Note: i) Attempt any five questions.

- ii) All questions carry equal marks.
- 1. a) What are different addressing modes? Explain with the help of example.
 - b) Explain about control word with the help of diagram.
- 2. a) Explain general register organization.
 - b) Draw instruction format and explain.
- 3. a) Describe about fetch and execution cycle.
 - b) Explain the steps of execution of micro instruction.
- 4. a) Using 8 bit two's complement integers, perform the following computations.
 - i) -35 + (-11)

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ii) 19-(-4)

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Find which is the best case for implementing Booth's algorithm for a multiplier.
 Multiplier 01111100 01110110 00000111 01010101

- 5. a) Write comparison between serial and parallel data transfer.
 - b) Draw the block diagram of DMA and explain.
- a) What are different Mapping schemes? Explain.
 - Explain the concept of virtual memory.
- 7. a) Write comparison between RISC and CISC.
 - b) Explain vector processing and Array processing.
- 8. Write short notes on:
 - a) Multiprocessor
 - b) BUS structure

c) Cache structure

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