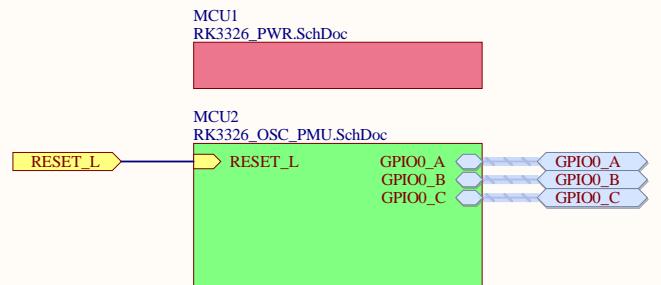
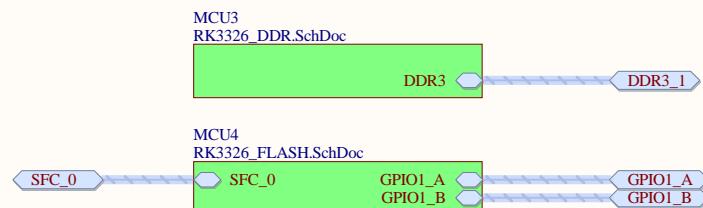


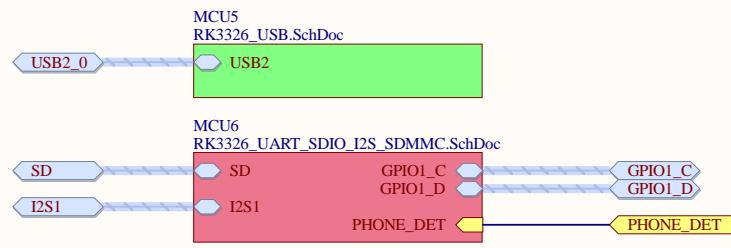
A



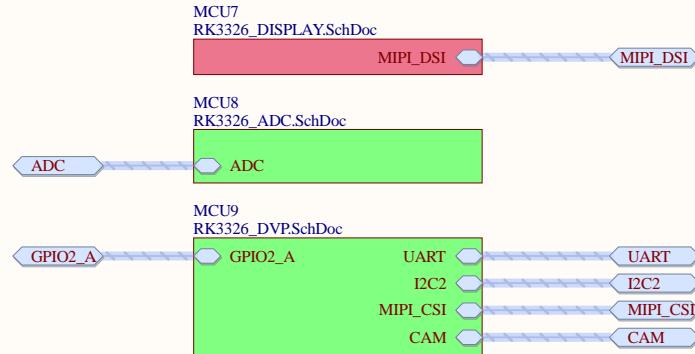
B



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Title RK3326		
Size: A4	Number: 3.	Revision: 1.0
Date: 12/6/2019	Time: 4:09:44 PM	Sheet 3 of 24
File: RK3326.SchDoc		Designed by: *@hardkernel.com
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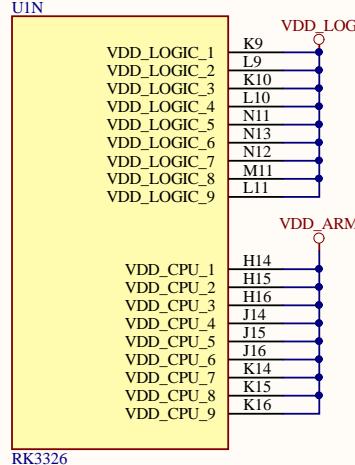
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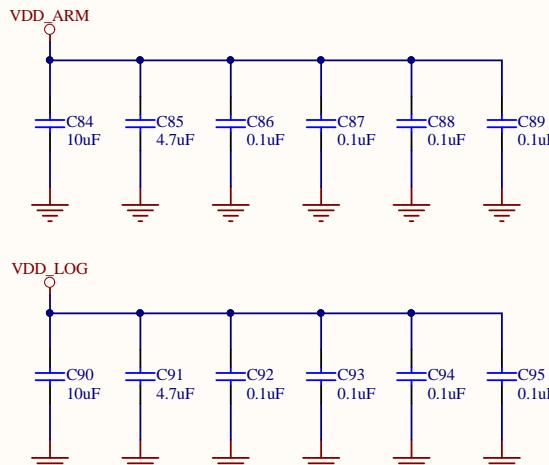
4

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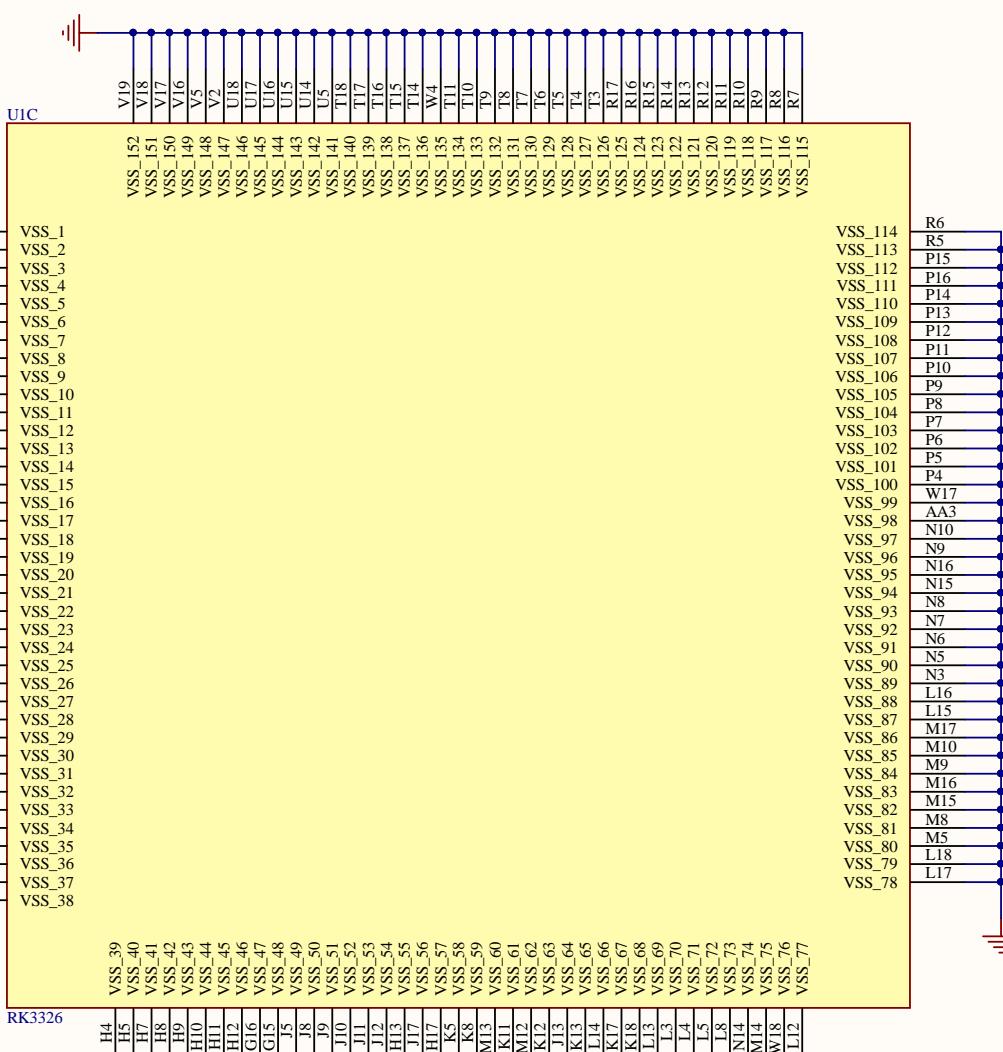


RK3326

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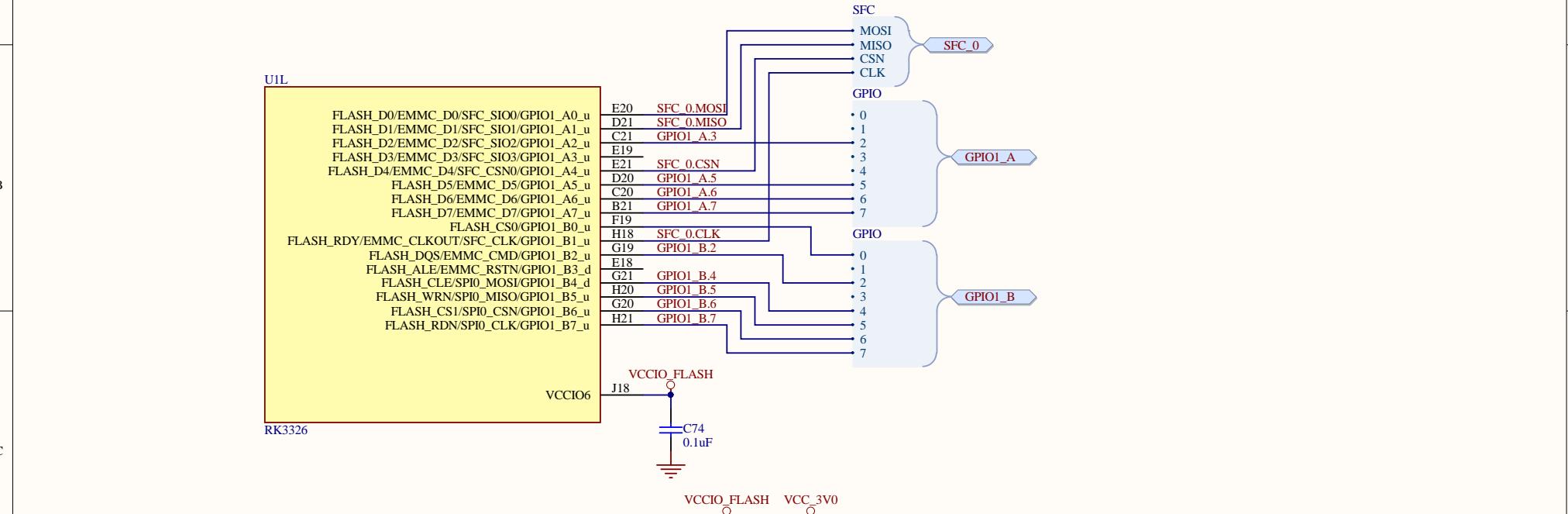
2

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Title: RK3326 FLASH Controller

Size: A4 Number: 5 Revision: 1.0

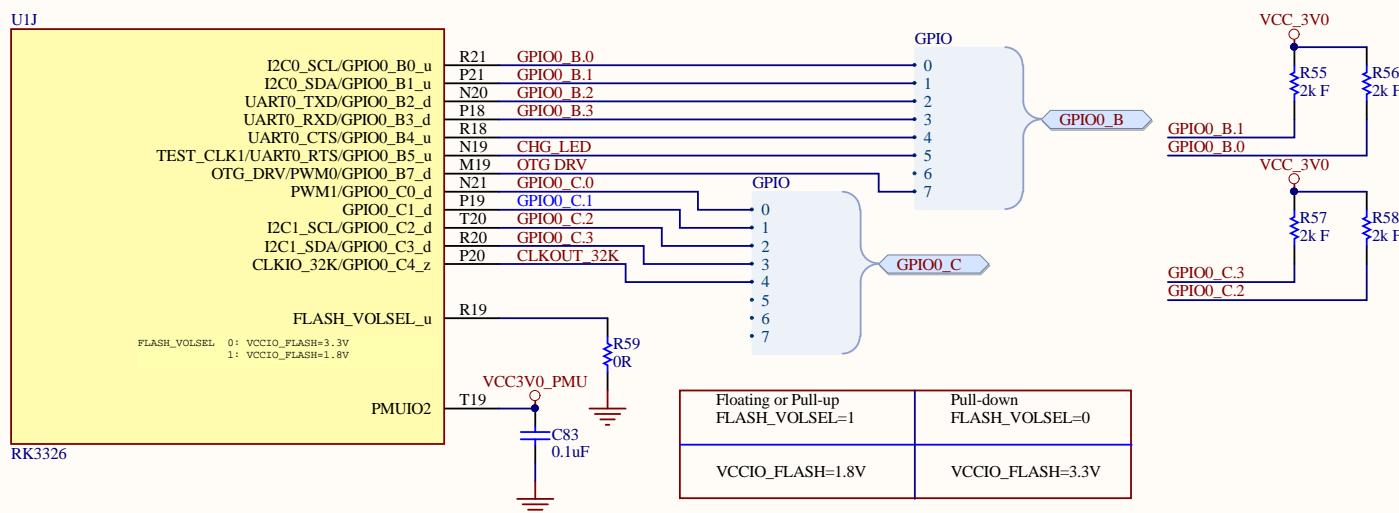
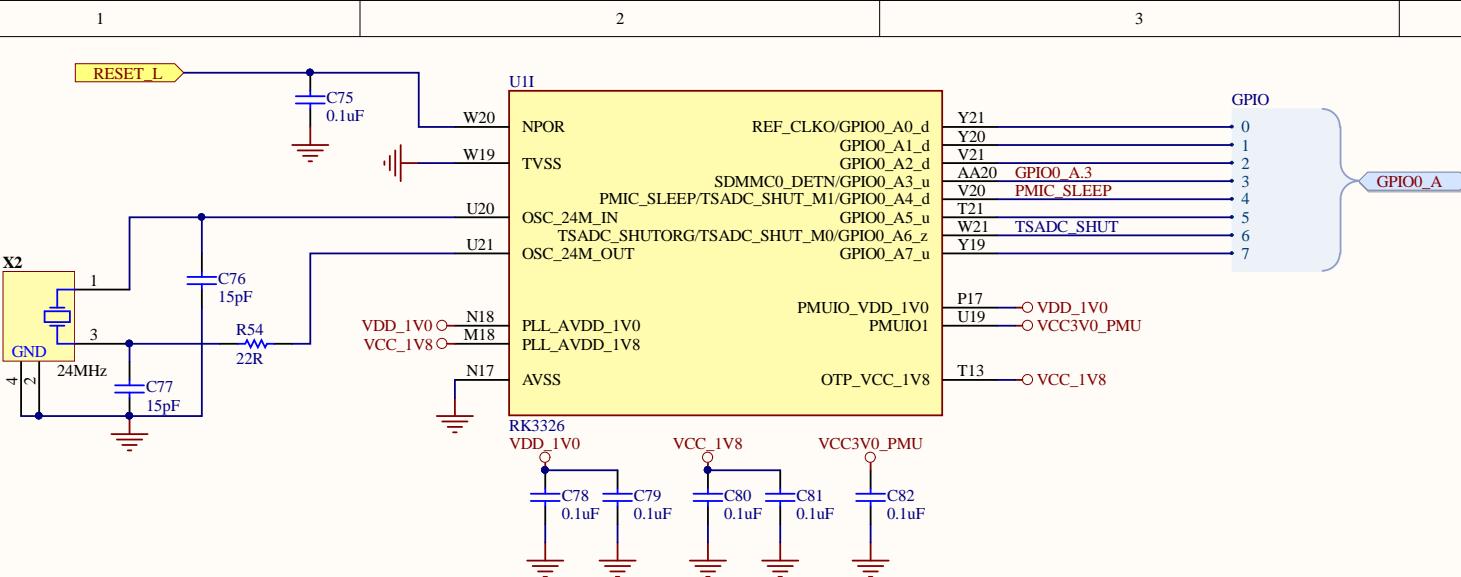
Date: 12/6/2019 Time: 4:09:45 PM Sheet 5 of 24

File: RK3326_FLASH.SchDoc

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Title **RK3326 OSC/PMUI01/PMUI02**
Size: **A4** Number: **6.** Revision:
Date: **12/6/2019** Time: **4:09:45 PM** Sheet **6** of **1**
File: **RK3326_QSC_PMU.SchDoc**

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UIM

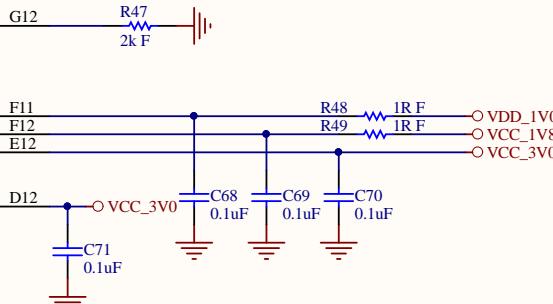
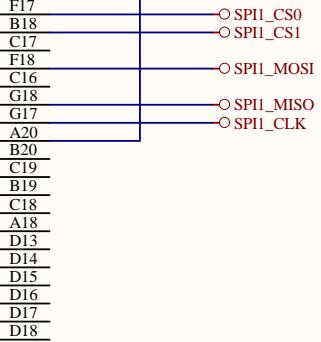
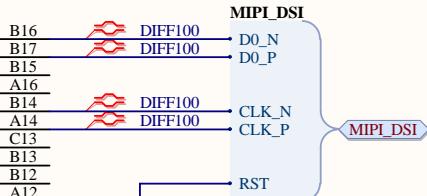
A

```

LVDS/MIPI_TX_D0N/LCDC_D11_M1
LVDS/MIPI_TX_D0P/LCDC_D8_M1
LVDS/MIPI_TX_D1N/LCDC_D1_M1
LVDS/MIPI_TX_D1P/LCDC_D10_M1
LVDS/MIPI_TX_CLKN/LCDC_D4_M1
LVDS/MIPI_TX_CLKP/LCDC_D3_M1
LVDS/MIPI_TX_D2N/LCDC_VSYNC_M1
LVDS/MIPI_TX_D2P/LCDC_D5_M1
LVDS/MIPI_TX_D3N/LCDC_HSYNC_M1
LVDS/MIPI_TX_D3P/LCDC_DEN_M1
LCDC_CLK GPIO3_A0_d
UART5_RXD/I2S2_2CH_MCLK/LCDC_HSYNC_M0 GPIO3_A1_d
UART5_TXD/I2S2_2CH_SCLK/LCDC_VSYNC_M0 GPIO3_A2_d
UART5_CTS/I2S2_2CH_LRCK/CIF_D2_M1/LCDC_DEN_M0 GPIO3_A3_d
LCDC_D0 GPIO3_A4_d
UART5_RTS/I2S2_2CH_SD/CIF_D3_M1/LCDC_D1_M0 GPIO3_A5_d
LCDC_D2 GPIO3_A6_d
I2S2_2CH_SDO/CIF_D4_M1/LCDC_D3_M0 GPIO3_A7_d
I2S0_8CH_SD13/CIF_D5_M1/LCDC_D4_M0 GPIO3_B0_d
SPI1_CS0/I2S0_8CH_SD12/CIF_D6_M1/LCDC_D5_M0 GPIO3_B1_d
SPI1_CS1/I2S0_8CH_SD1/LCDC_D6 GPIO3_B2_d
I2S0_8CH_SD1/LCDC_D7 GPIO3_B3_d
SPI1_MOSI/I2S0_8CH_SCLKRX/CIF_D7_M1/LCDC_D8_M0 GPIO3_B4_d
I2S0_8CH_LRCKRX/LCDC_D9 GPIO3_B5_d
SPI1_MISO/I2S0_8CH_SDO3/CIF_D8_M1/LCDC_D10_M0 GPIO3_B6_d
SPI1_CLK/I2S0_8CH_SDO2/CIF_D9_M1/LCDC_D11_M0 GPIO3_B7_d
I2S0_8CH_SD01/LCDC_D12 GPIO3_C0_d
I2S0_8CH_MCLK/LCDC_D13 GPIO3_C1_d
TDM_FSYNC/LCDC_D14/I2S0_8CH_LRCKTX GPIO3_C2_d
TDM_SCLK/I2S0_8CH_SCLKTX/LCDC_D15 GPIO3_C3_d
TDM_SDO/I2S0_8CH_SDO0/LCDC_D16 GPIO3_C4_d
TDM_SDI/I2S0_8CH_SD10/LCDC_D17 GPIO3_C5_d
PDM_CLK0_M0/LCDC_D18 GPIO3_C6_d
PDM_CLK1/LCDC_D19 GPIO3_C7_d
PDM_SD11/CIF_CLKOUT_M1/LCDC_D20 GPIO3_D0_d
ISP_PRELIGHT_TRIG/PDM_SD12/CIF_VSYNC_M1/LCDC_D21 GPIO3_D1_d
ISP_FLASH_TRIGOUT/PDM_SD13/CIF_HREF_M1/LCDC_D22 GPIO3_D2_d
ISP_FLASH_TRIGIN/PDM_SD10_M0/CIF_CLKIN_M1/LCDC_D23 GPIO3_D3_d
LVDS_RBIA5
LVDS/MIPI_AVDD_1V0
LVDS/MIPI_AVDD_1V8
LVDS/MIPI_AVDD_3V3
VCCIO4

```

RK3326



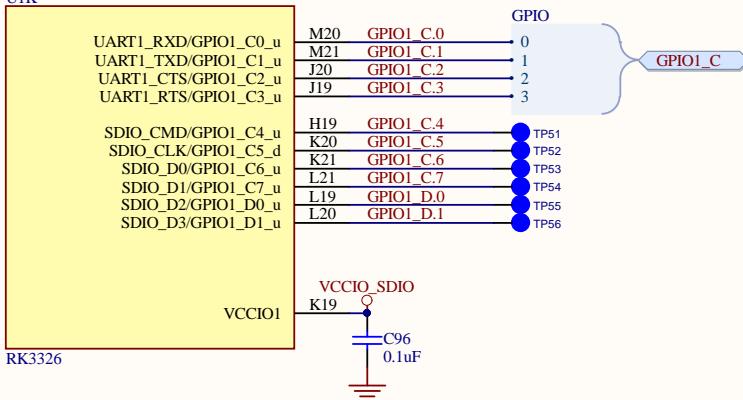
Title RK3326 Display Interface		
Size: A4	Number: 7.	Revision: 1.0
Date: 12/6/2019	Time: 4:09:45 PM	Sheet 7 of 24
File: RK3326_DISPLAY.SchDoc		

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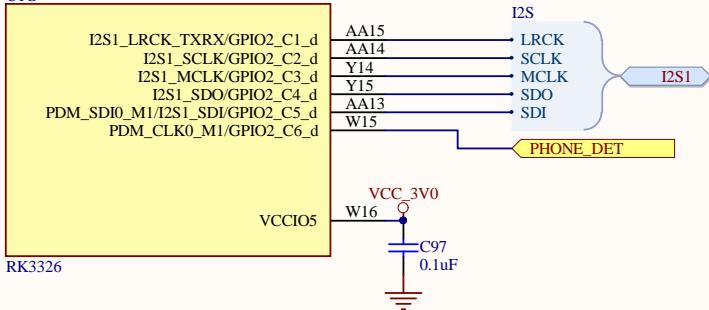
1 2 3 4

A

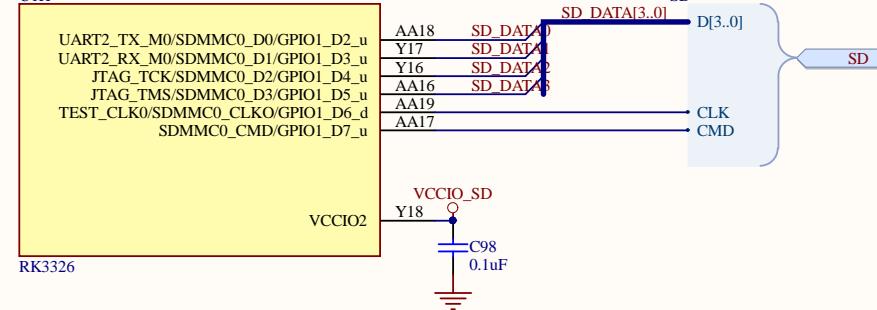


VCCIO_SDIO VCC_3V0

B



C



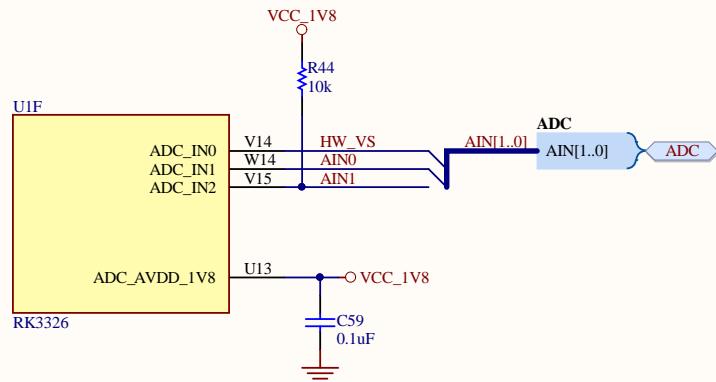
D

Title RK3326 UART/SDIO/I2S/SDMMC			Hardkernel Co.,LTD. 475-1 Mananro Manangu, Anyang City Gyeonggi-do South Korea
Size: A4	Number: 8.	Revision: 1.0	
Date: 12/6/2019	Time: 4:09:45 PM	Sheet 8 of 24	
File: RK3326_UART_SDIO_I2S_SDMMC.SchDoc			Designed by: *@hardkernel.com

1 2 3 4

ODROID

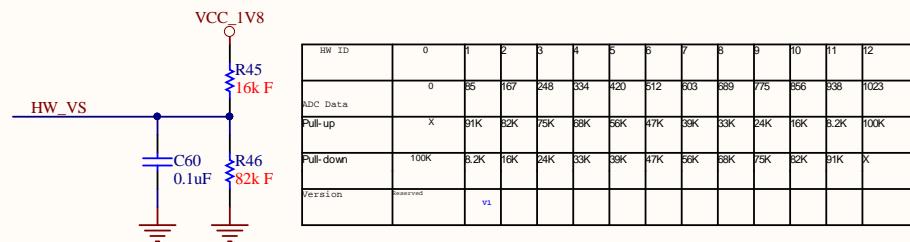
A



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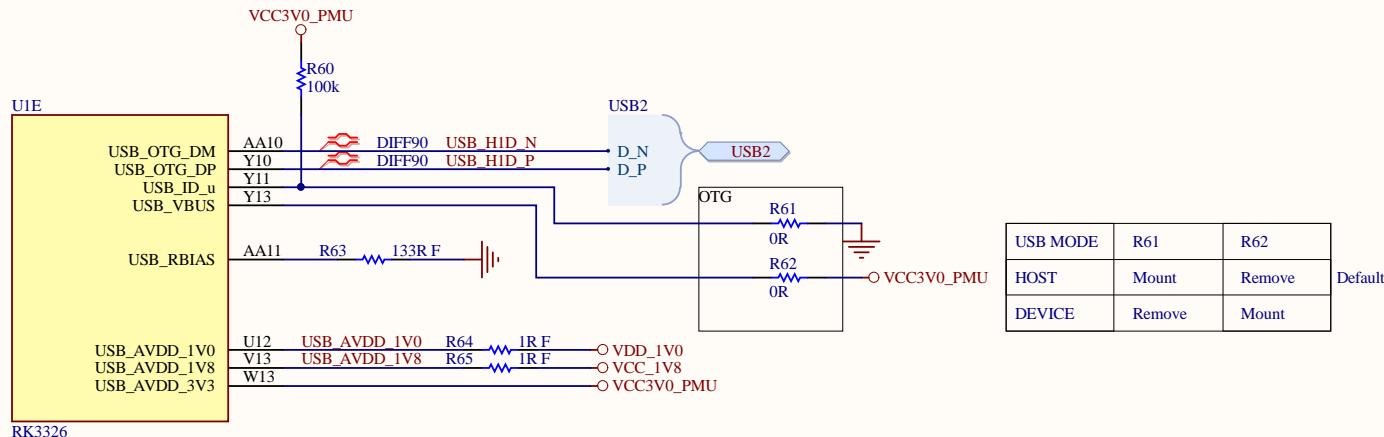
D

D

Title RK3326 SARADC			<i>Hardkernel Co.,LTD. 475-1 Mananro Manangu, Anyang City Gyeonggido South Korea</i>
Size: A4	Number: 9.	Revision: 1.0	
Date: 12/6/2019	Time: 4:09:45 PM	Sheet 9 of 24	ODROID
File: RK3326_ADC.SchDoc			Designed by: *@hardkernel.com

A

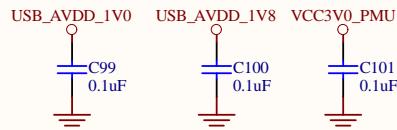
A



RK3326

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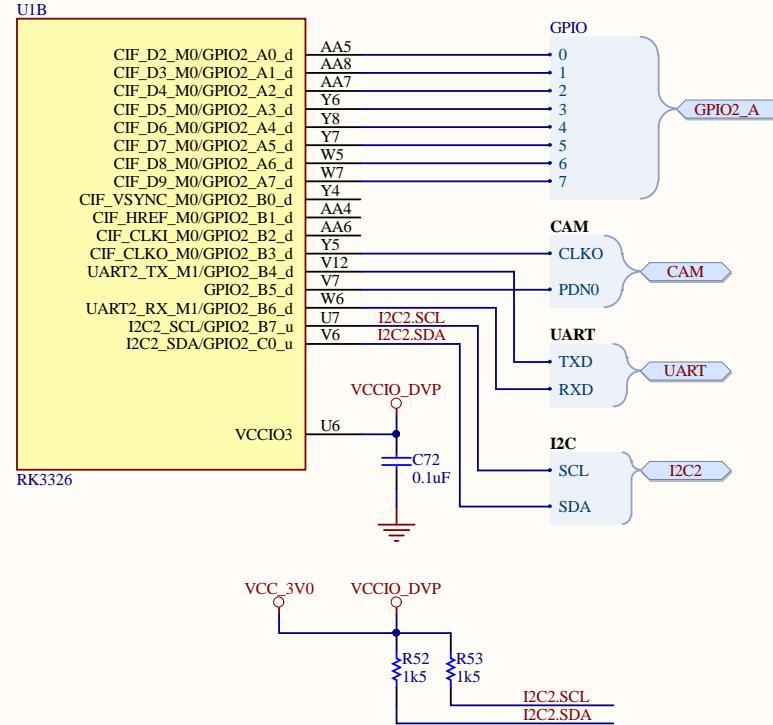
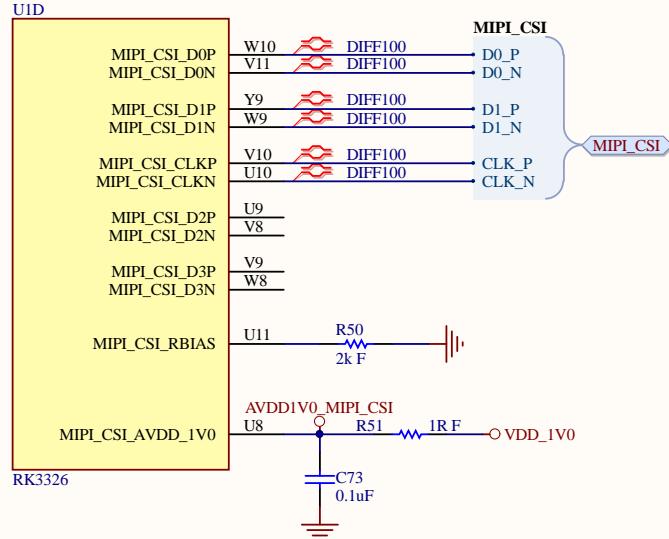
Title RK3326 USB Controller			<i>Hardkernel Co.,LTD.</i>
Size: A4	Number: 10.	Revision: 1.0	<i>475-1 Mananro</i>
Date: 12/6/2019	Time: 4:09:45 PM	Sheet 10 of 24	<i>Manangu, Anyang City</i>
File: RK3326_USB.SchDoc			<i>Gyeonggi-do</i>
			<i>South Korea</i>

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A



Title **RK3326 DVP Interface**

Size: A4 Number: 11 Revision: 1.0

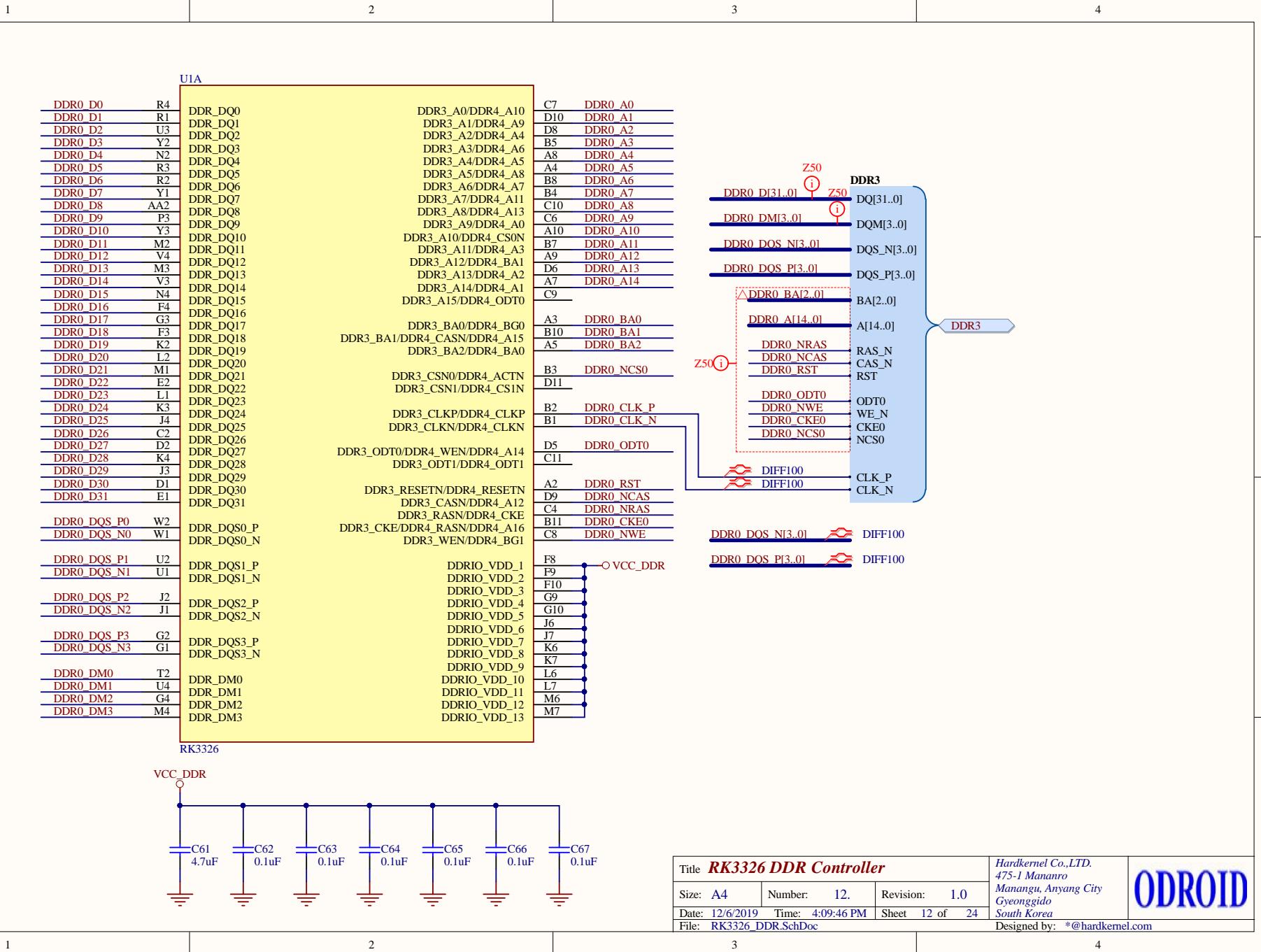
Date: 12/6/2019 Time: 4:09:45 PM Sheet 11 of 24

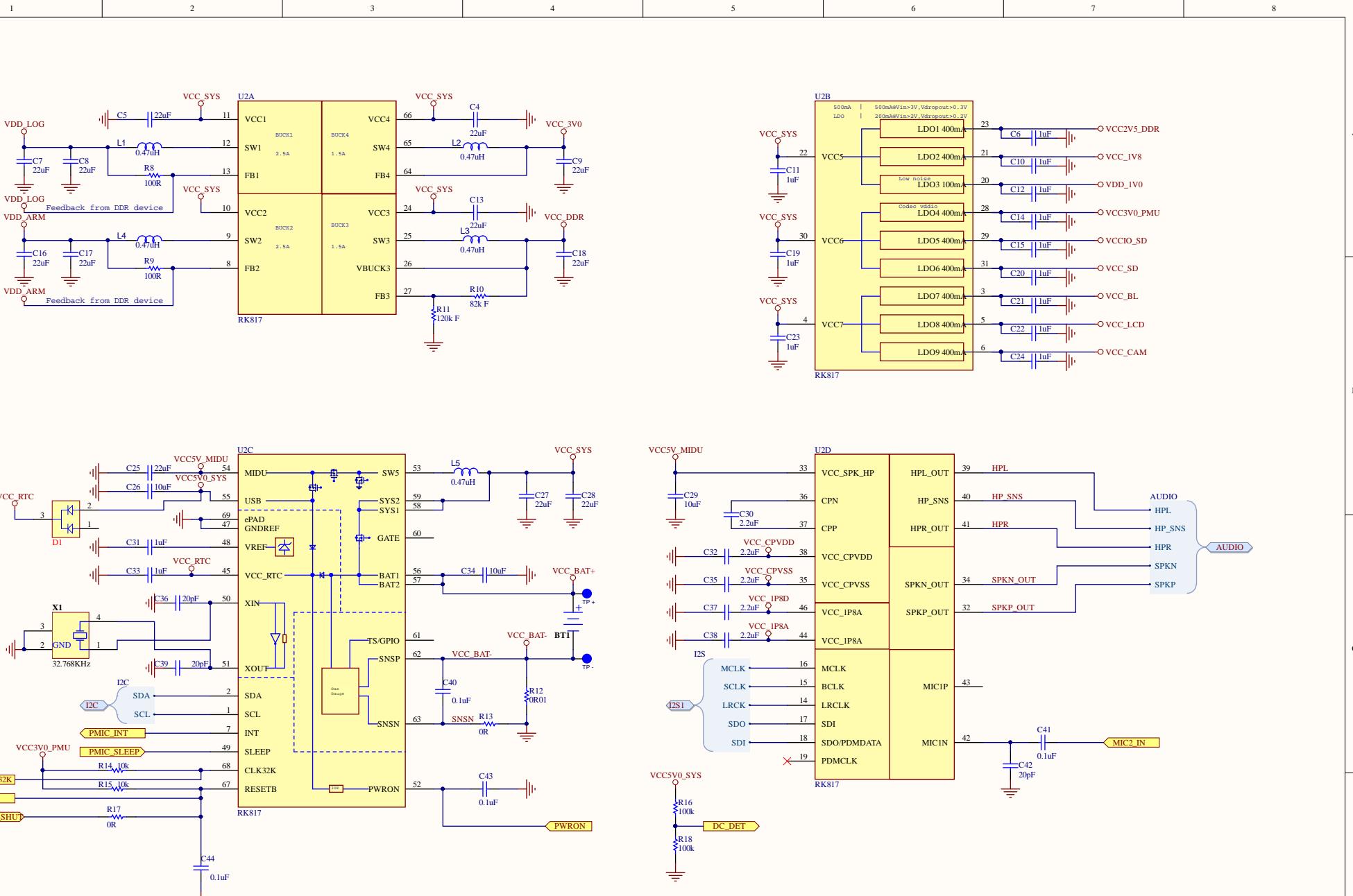
File: RK3326_DVP.SchDoc

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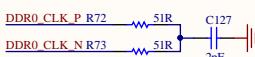
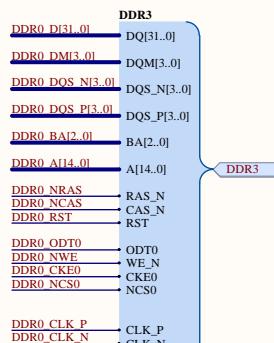
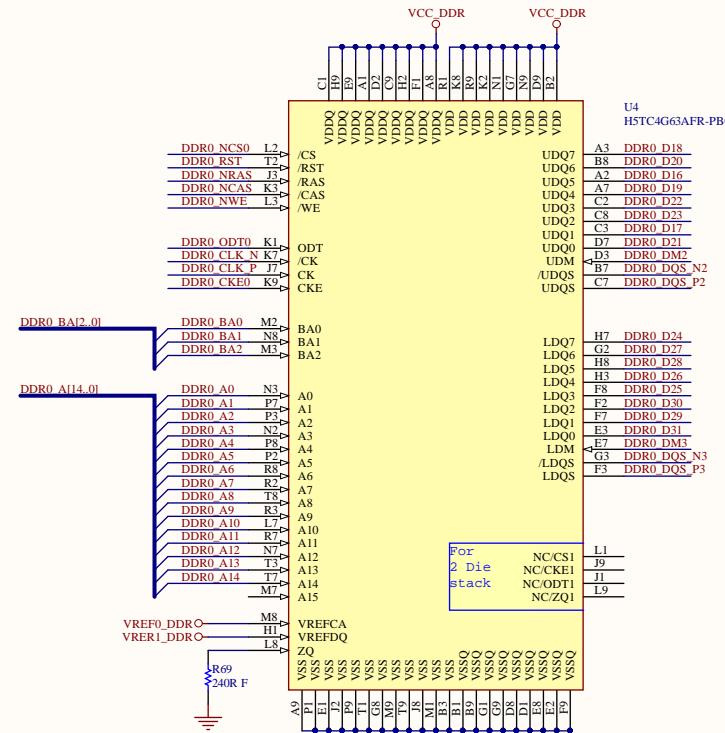
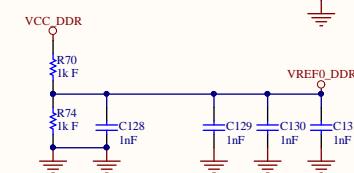
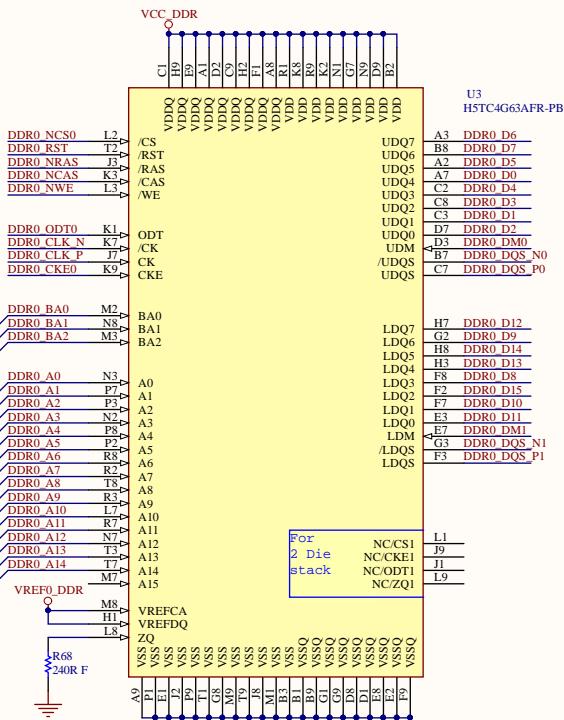
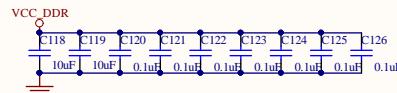
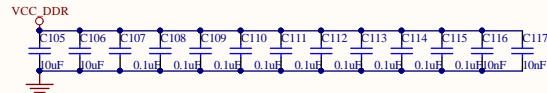
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Title RK817			
Size: A3	Number: 13.	Revision: 1.0	Hardkernel Co., LTD.
Date: 12/6/2019	Time: 4:09:46 PM	Sheet 13 of 24	475-1 Mananro Manangu, Anyang City Gyeonggi-do South Korea
File: PWR_PMIC_RK817.SchDoc			Designed by: ruppi.kim@hardkernel.com

ODROID



A

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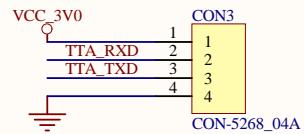
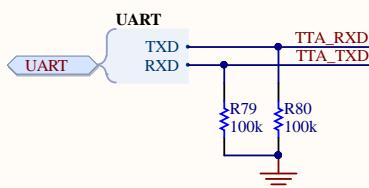
B

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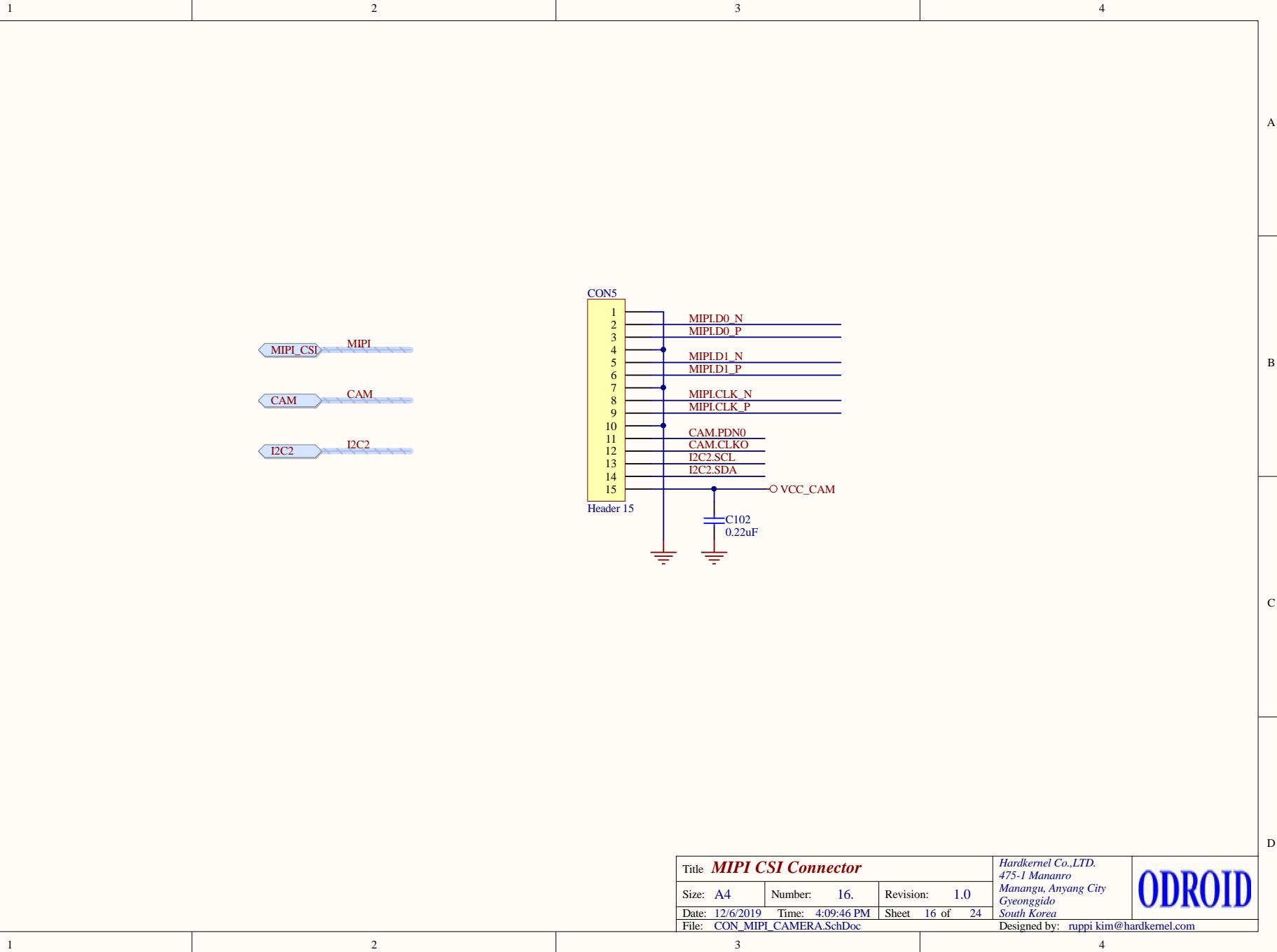
D

D



Title UART Connector			Hardkernel Co.,LTD. 475-1 Mananro Manangu, Anyang City Gyeonggido South Korea
Size: A4	Number: 15.	Revision: 1.0	
Date: 12/6/2019	Time: 4:09:46 PM	Sheet 15 of 24	
File: CON_UART.SchDoc			Designed by: ruppi.kim@hardkernel.com

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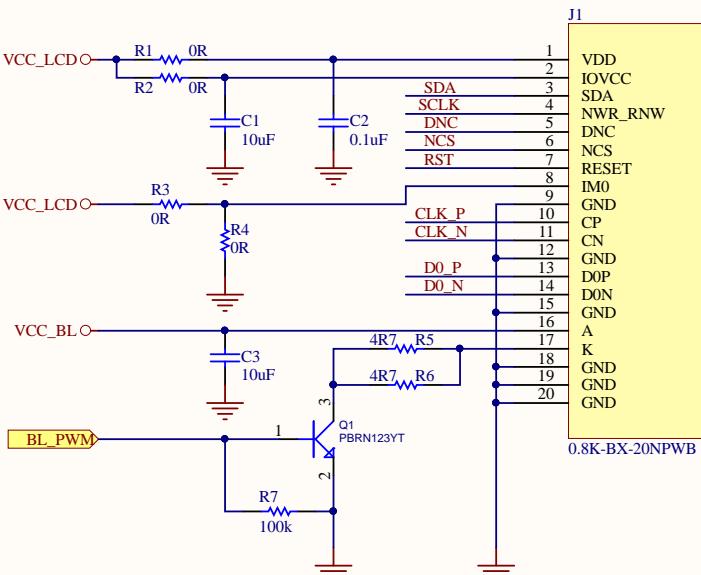
1

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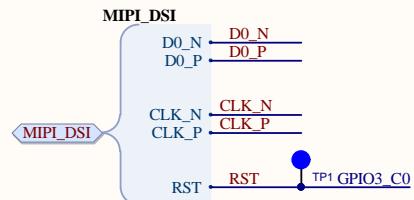
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	LCD MODE	R3	R4
Default	MIPI DSI	Remove	Mount
	4WIRE SPI	Mount	Remove

D

Title **CON LCD KD35T133-A2**

Size: A4 Number: 17 Revision: 1.0

Date: 12/6/2019 Time: 4:09:47 PM Sheet 17 of 24

File: CON_LCD_KD35T133-A2_MIPLSchDoc

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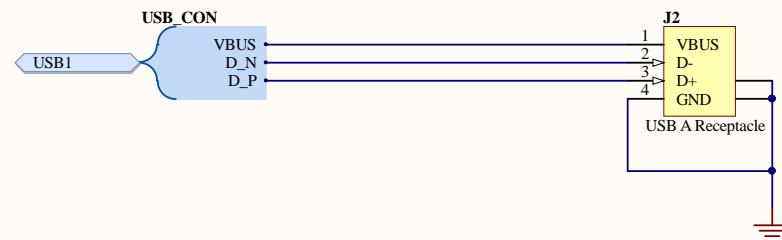
B

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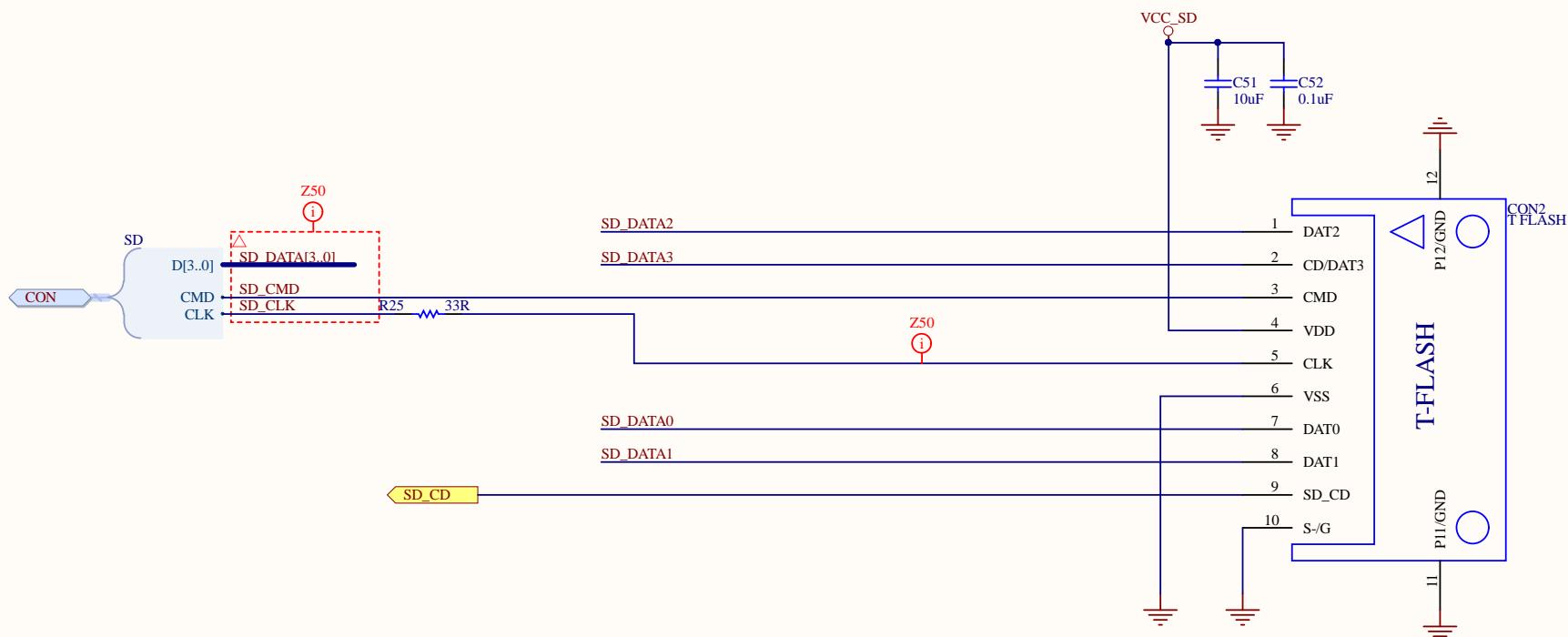
D



Title USB 2.0 Type A Connector			<i>Hardkernel Co.,LTD. 1-605 Shinan Metrokhan 1115 Dalandong, Dongangu, Anyang Gyeonggi-do South Korea</i>
Size: A4	Number: 18.	Revision: 1.0	
Date: 12/6/2019	Time: 4:09:47 PM	Sheet 18 of 24	
File: CON_USB_RA.SchDoc			Designed by: ruppi.kim@hardkernel.com

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1 2 3 4



Title Micro SD Socket			Hardkernel Co.,LTD. 475-1 Mananro Manangu, Anyang City Gyeonggido South Korea
Size: A4	Number: 19.	Revision: 1.0	
Date: 12/6/2019	Time: 4:09:47 PM	Sheet 19 of 24	
File: CON_MICROSD_HD08-AI00335.SchDoc			Designed by: ruppi.kim@hardkernel.com

1 2 3 4

ODROID

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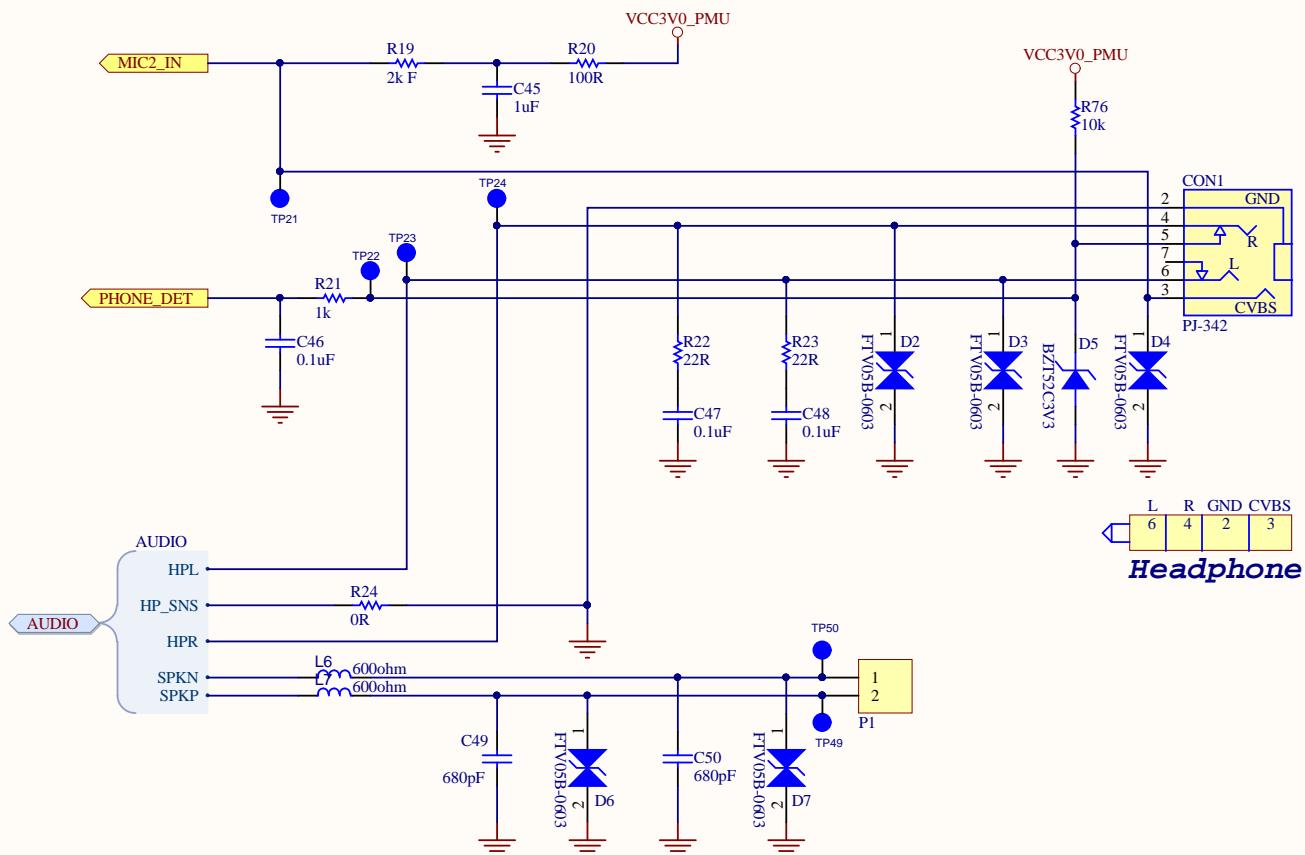
B

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Title: Audio Jack

Size: A4 | Number: 20. | Revision: 1.0

Date: 12/6/2019 | Time: 4:09:47 PM | Sheet 20 of 24

File: CON_AUDIO.SchDoc

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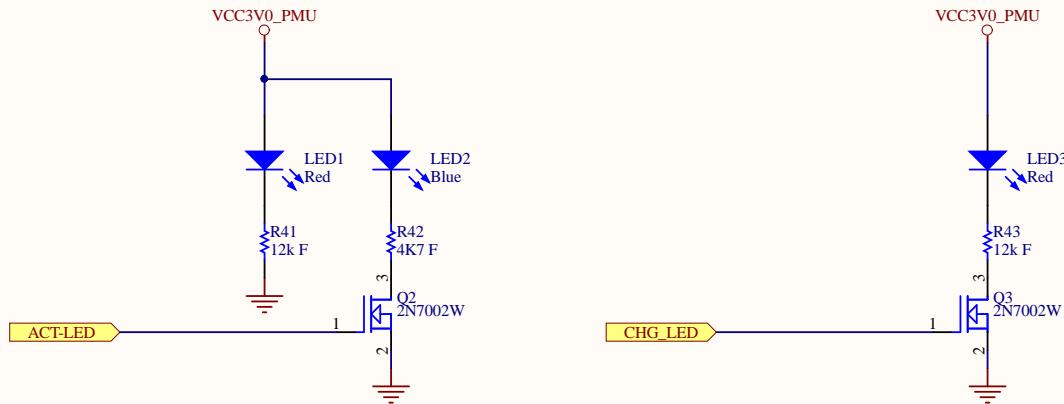
B

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Title *Indicator LEDs*

Size: A4 Number: 21 Revision: 1.0

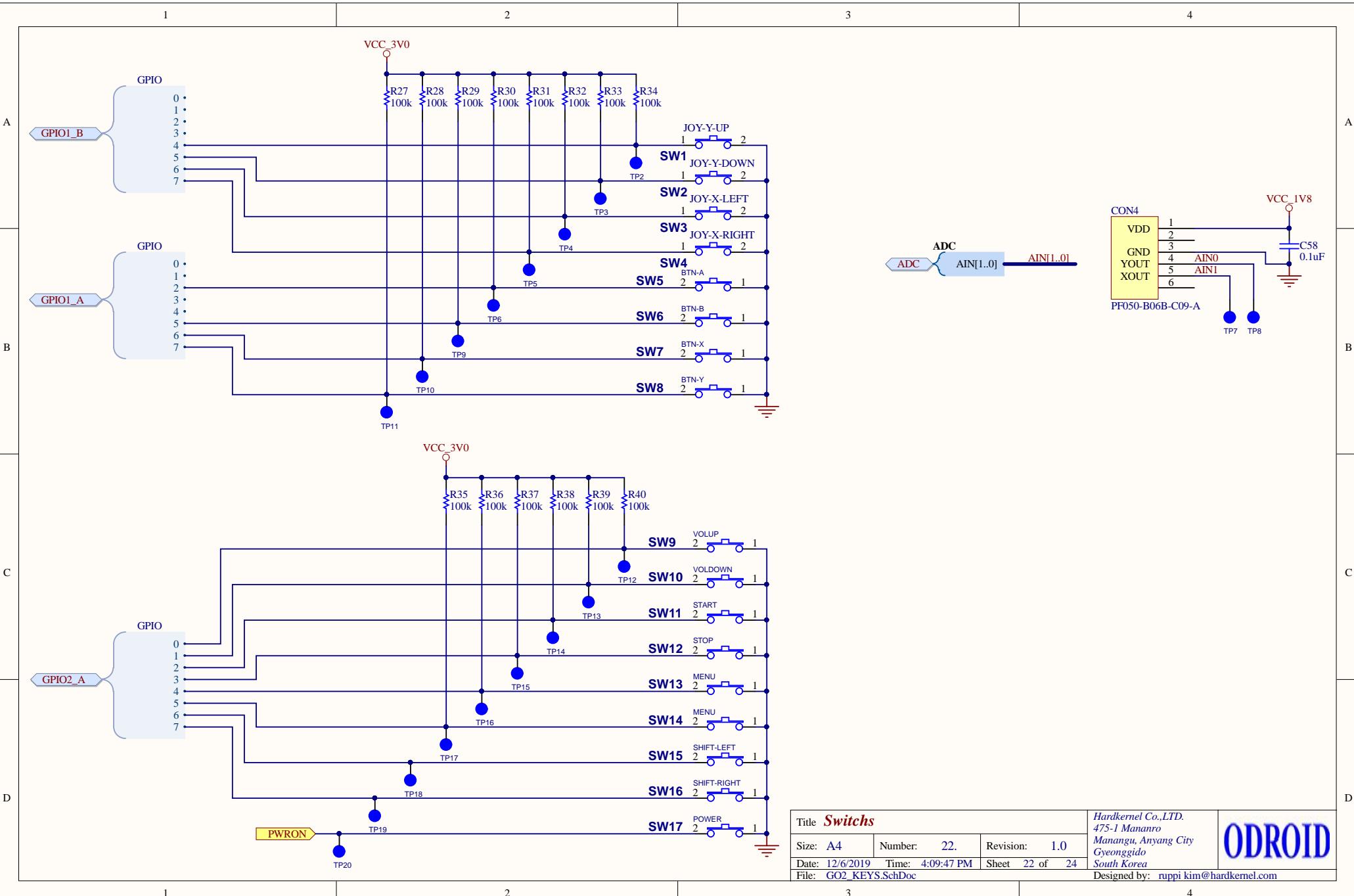
Date: 12/6/2019 Time: 4:09:47 PM Sheet 21 of 24

File: G02_LEDs.SchDoc

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Title Switchs			Hardkernel Co.,LTD. 475-1 Mananro Manang, Anyang City Gyeonggi-do South Korea
Size: A4	Number: 22.	Revision: 1.0	
Date: 12/6/2019	Time: 4:09:47 PM	Sheet 22 of 24	
File: GO2_KEYS.SchDoc			Designed by: ruppi kim@hardkernel.com

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1

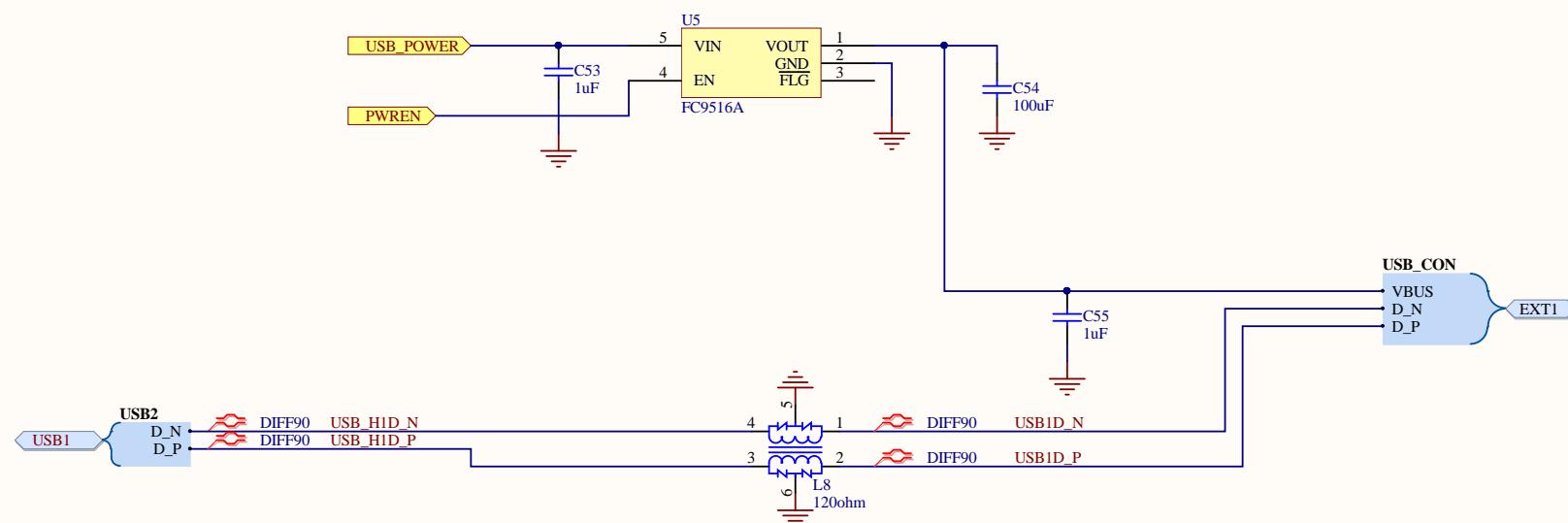
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Title **USB HOST PWR SW**

Size: A4 Number: 23. Revision: 1.0

Date: 12/6/2019 Time: 4:09:47 PM Sheet 23 of 24

File: [USB_HOST_PWR_SW.SchDoc](#)

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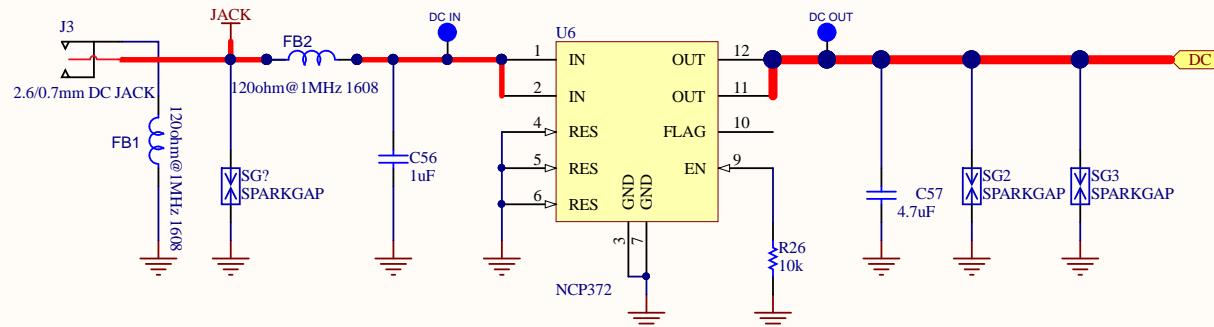
B

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Title **CON DCJACK**

Size: A4 Number: 24 Revision: 1.0

Date: 12/6/2019 Time: 4:09:47 PM Sheet 24 of 24

File: CON DCJACK.SchDoc

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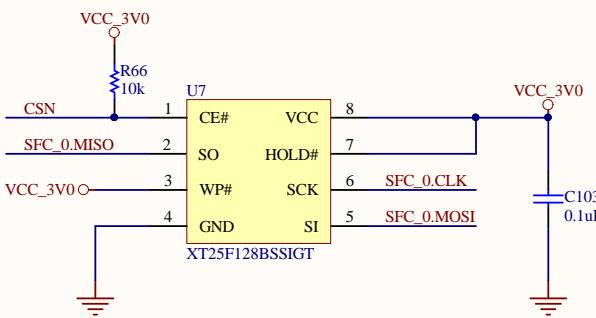
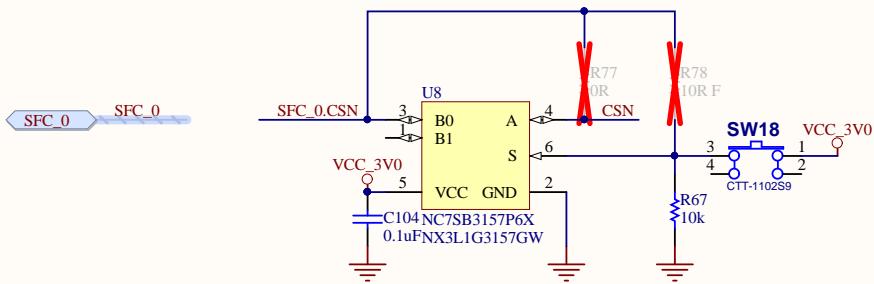
Designed by: *@hardkernel.com

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Title: SPI FLASH MEMORY

Size: A4 Number: 25 Revision: 1.0

Date: 12/6/2019 Time: 4:09:47 PM Sheet 25 of 24

File: SPI_FLASH.SchDoc

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