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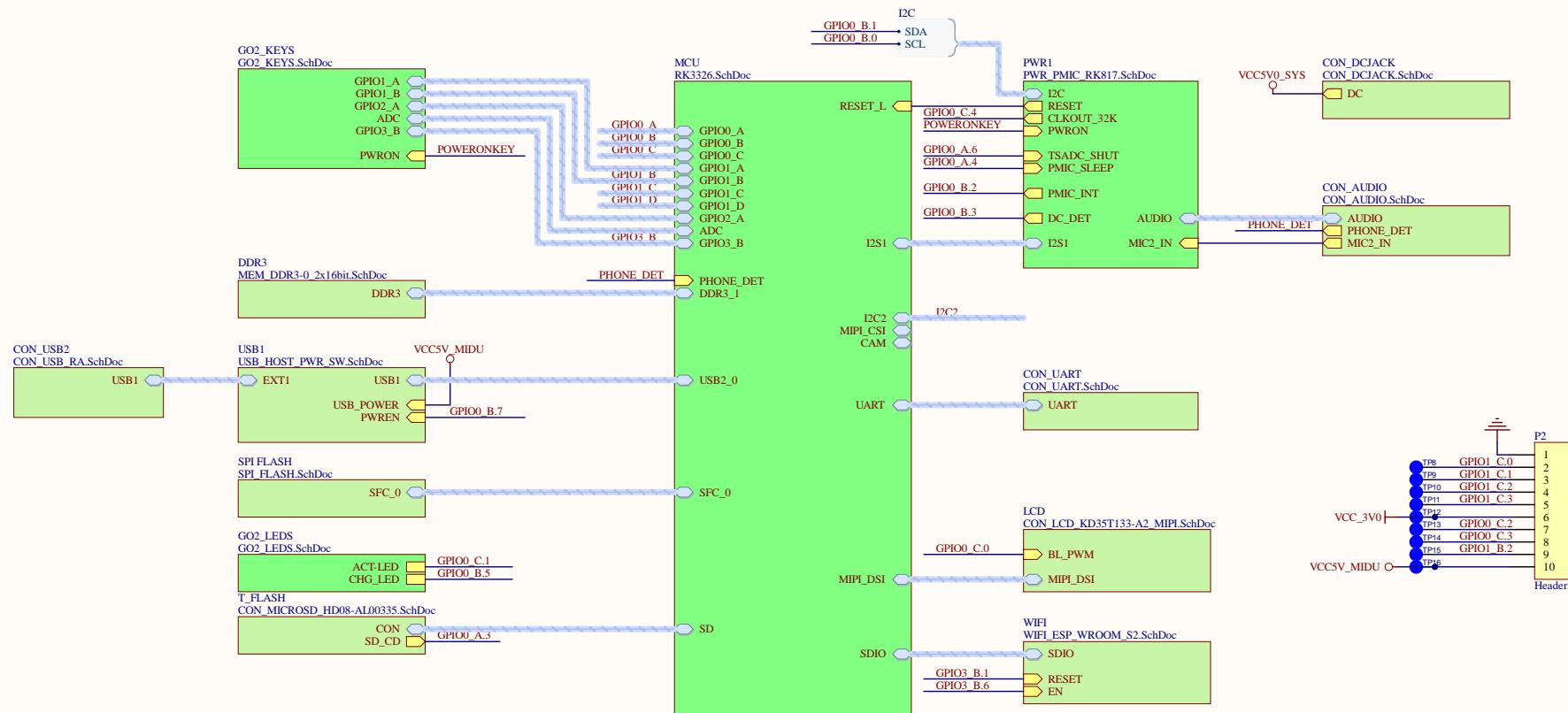
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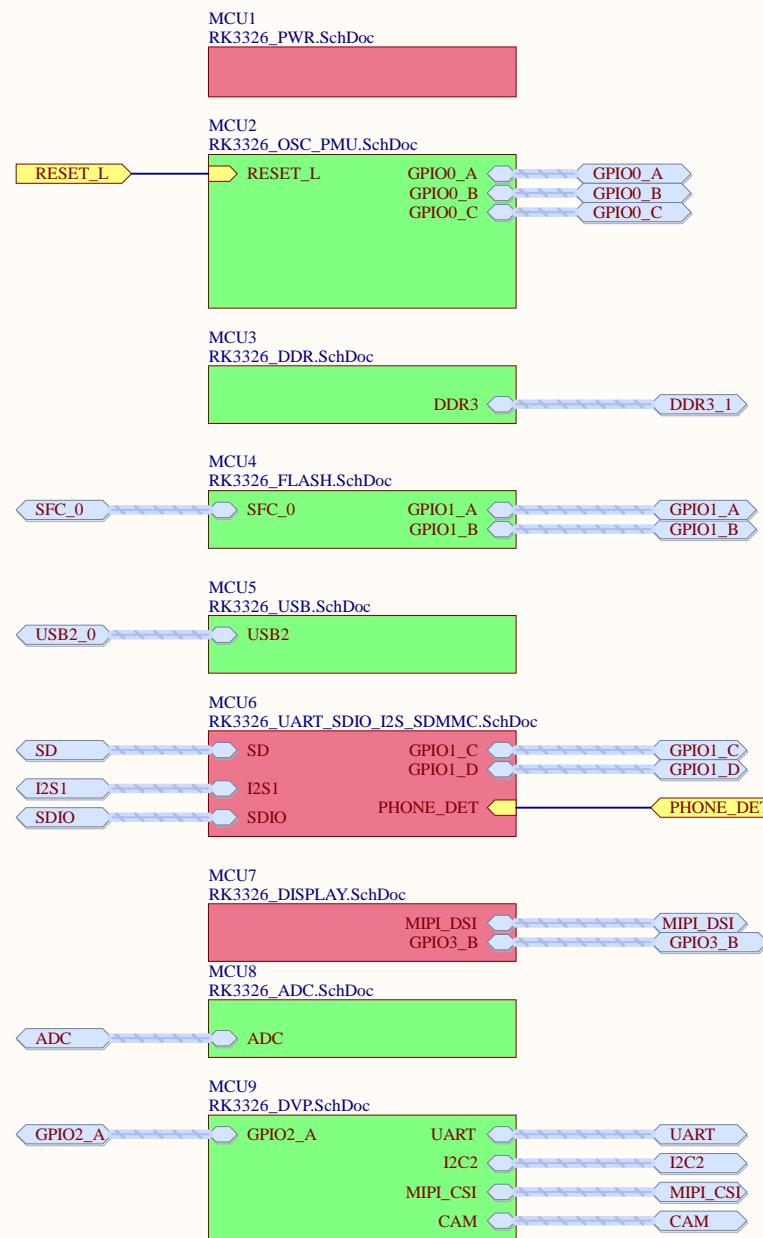
C

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Title **RK3326**

Size: A4

Number: 3.

Revision: 1.0

Date: 2020-05-20 Time: 오후 6:37:57

Sheet 3 of 24

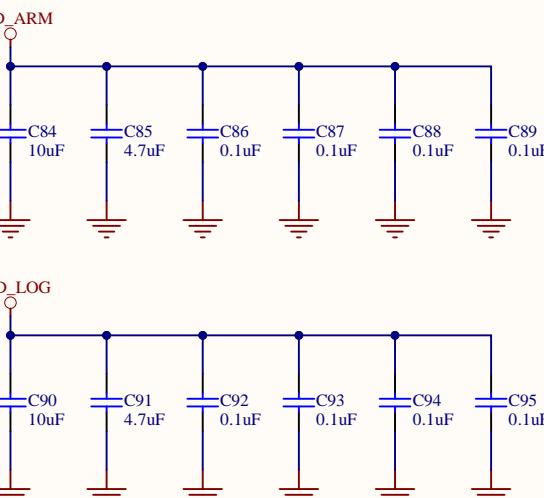
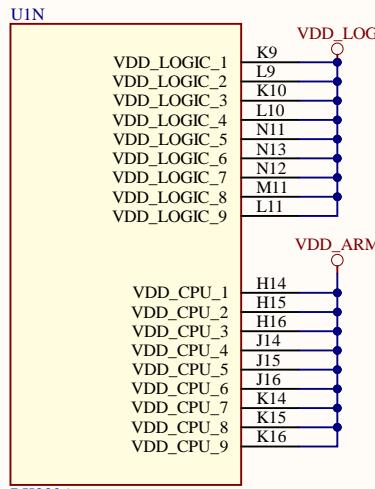
File: RK3326.SchDoc

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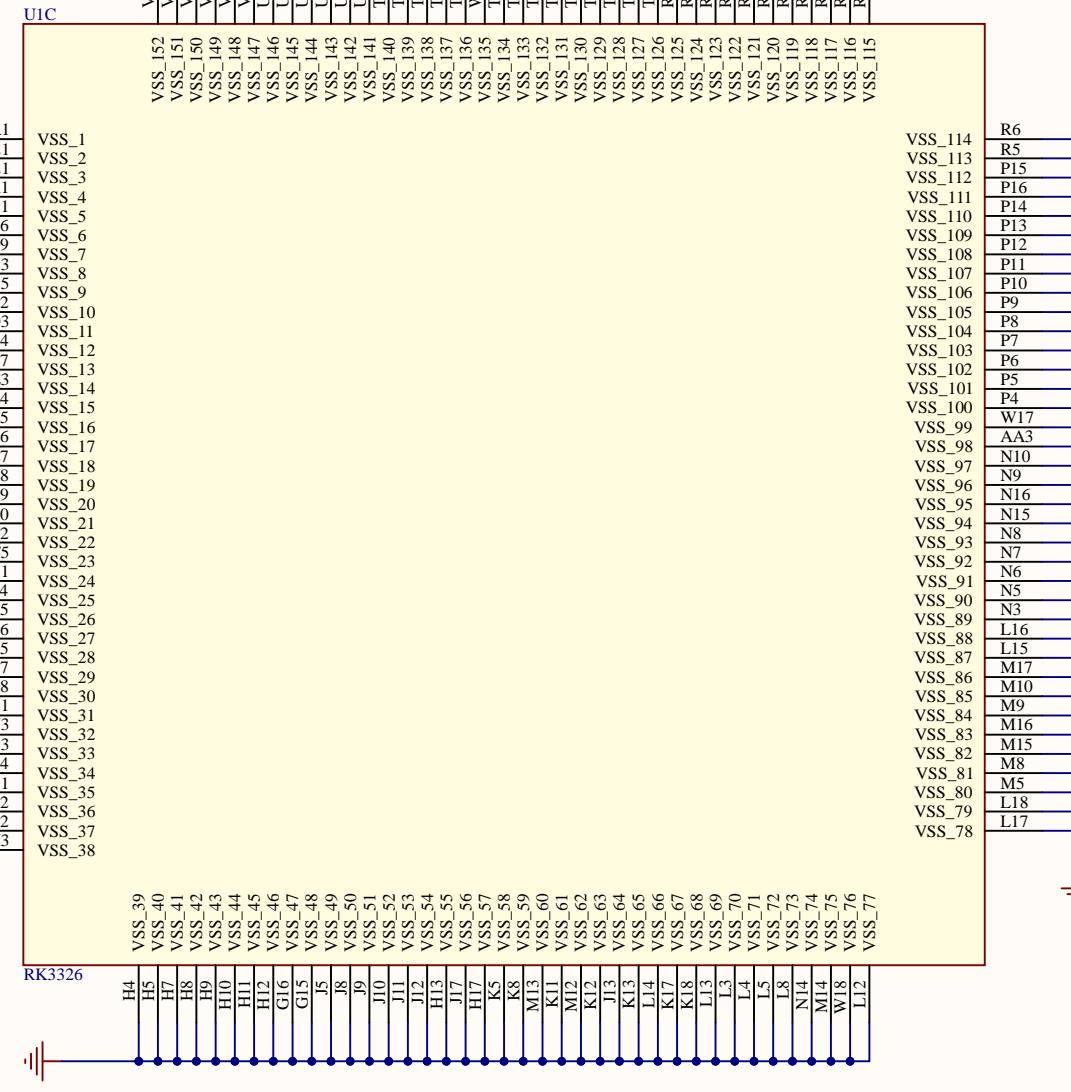
Designed by: *@hardkernel.com

ODROID

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C

Title RK3326 Power			Hardkernel Co.,LTD.
Size: A4			475-1 Mananro
Number: 4.			Manangu, Anyang City
Revision: 0.3			Gyeonggi-do
Date: 2020-05-20 Time: 오후 6:37:57			South Korea
Sheet 4 of 24			
File: RK3326_PWR.SchDoc			Designed by: ruppi.kim@hardkernel.com

ODROID

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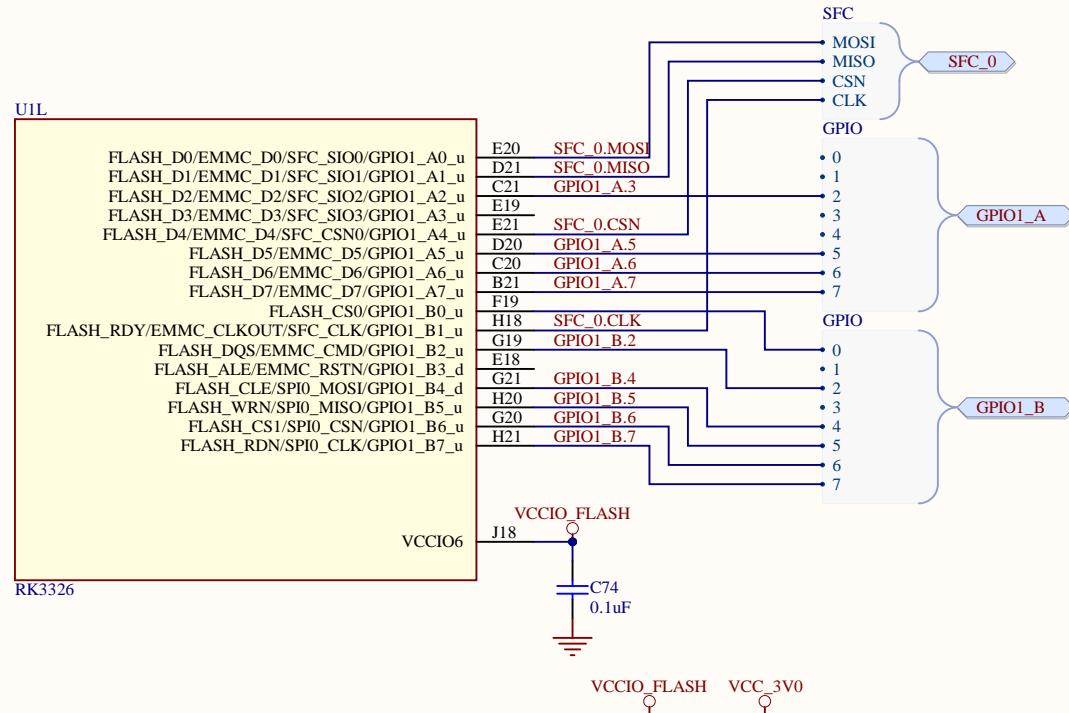
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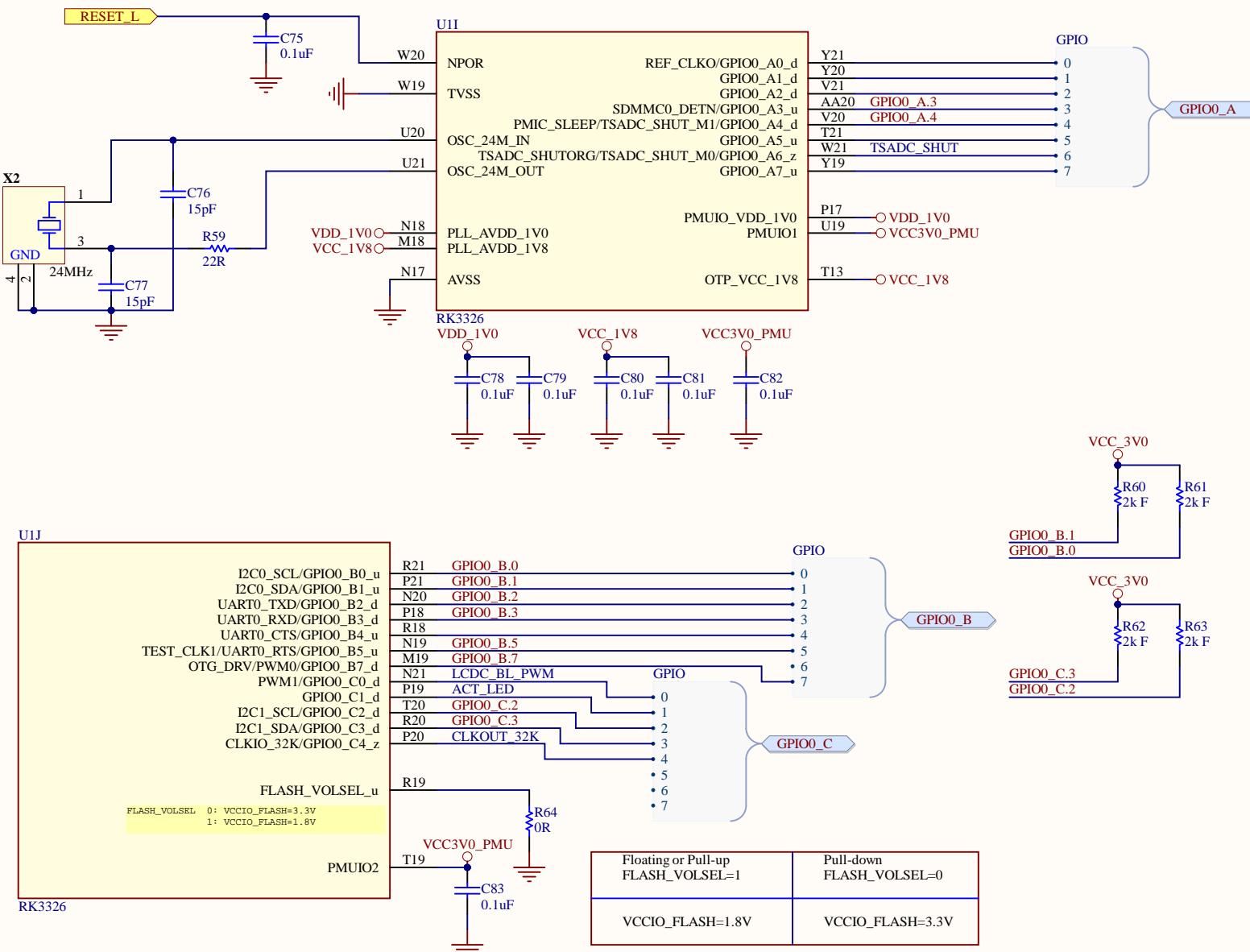
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Title **RK3326 FLASH Controller**Size: **A4** Number: **5.** Revision: **0.3**Date: 2020-05-20 Time: 오후 6:37:57 Sheet **5** of **24**File: **RK3326_FLASH.SchDoc**

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Title	RK3326 OSC/PMUIO1/PMUIO2		
Size:	A4	Number:	6.
Date:	2020-05-20	Time:	오후 6:37:57
File:	RK3326 OSC PMU.SchDoc		

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ODROID

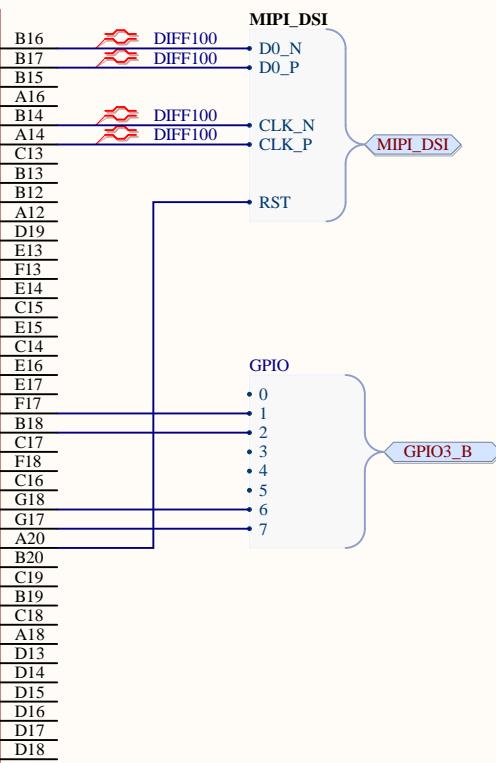
U1M

A

LVDS/MIPI_TX_D0N/LCDC_D11_M1
 LVDS/MIPI_TX_D0P/LCDC_D8_M1
 LVDS/MIPI_TX_D1N/LCDC_D1_M1
 LVDS/MIPI_TX_D1P/LCDC_D10_M1
 LVDS/MIPI_TX_CLKN/LCDC_D4_M1
 LVDS/MIPI_TX_CLKP/LCDC_D3_M1
 LVDS/MIPI_TX_D2N/LCDC_VSYNC_M1
 LVDS/MIPI_TX_D2P/LCDC_D5_M1
 LVDS/MIPI_TX_D3N/LCDC_HSYNC_M1
 LVDS/MIPI_TX_D3P/LCDC_DEN_M1
 LCDC_CLK GPIO3_A0_d
 UART5_RXD/I2S2_2CH_MCLK/LCDC_HSYNC_M0/GPIO3_A1_d
 UART5_TXD/I2S2_2CH_SCLK/LCDC_VSYNC_M0/GPIO3_A2_d
 UART5_CTS/I2S2_2CH_LRCK/CIF_D2_M1/LCDC_DEN_M0/GPIO3_A3_d
 LCDC_D0/GPIO3_A4_d
 UART5_RTS/I2S2_2CH_SDI/CIF_D3_M1/LCDC_D1_M0/GPIO3_A5_d
 LCDC_D2/GPIO3_A6_d
 I2S2_2CH_SDO/CIF_D4_M1/LCDC_D3_M0/GPIO3_A7_d
 I2S0_8CH_SD13/CIF_D5_M1/LCDC_D4_M0/GPIO3_B0_d
 SPI1_CS0/I2S0_8CH_SD12/CIF_D6_M1/LCDC_D5_M0/GPIO3_B1_d
 SPI1_CS1/LCDC_D6/GPIO3_B2_d
 I2S0_8CH_SD1/LCDC_D7/GPIO3_B3_d
 SPI1_MOSI/I2S0_8CH_SCLKRX/CIF_D7_M1/LCDC_D8_M0/GPIO3_B4_d
 I2S0_8CH_LRCKRX/LCDC_D9/GPIO3_B5_d
 SPI1_MISO/I2S0_8CH_SDO3/CIF_D8_M1/LCDC_D10_M0/GPIO3_B6_d
 SPI1_CLK/I2S0_8CH_SDO2/CIF_D9_M1/LCDC_D11_M0/GPIO3_B7_d
 I2S0_8CH_SD01/LCDC_D12/GPIO3_C0_d
 I2S0_8CH_MCLK/LCDC_D13/GPIO3_C1_d
 TDM_FSYNC/LCDC_D14/I2S0_8CH_LRCKTX/GPIO3_C2_d
 TDM_SCLK/I2S0_8CH_SCLKTX/LCDC_D15/GPIO3_C3_d
 TDM_SDO/I2S0_8CH_SDO0/LCDC_D16/GPIO3_C4_d
 TDM_SDI/I2S0_8CH_SD10/LCDC_D17/GPIO3_C5_d
 PDM_CLK0_M0/LCDC_D18/GPIO3_C6_d
 PDM_CLK1/LCDC_D19/GPIO3_C7_d
 PDM_SDII/CIF_CLKOUT_M1/LCDC_D20/GPIO3_D0_d
 ISP_PRELIGHT_TRIG/PDM_SD12/CIF_VSYNC_M1/LCDC_D21/GPIO3_D1_d
 ISP_FLASH_TRIGOUT/PDM_SD13/CIF_HREF_M1/LCDC_D22/GPIO3_D2_d
 ISP_FLASH_TRIGIN/PDM_SD10_M0/CIF_CLKIN_M1/LCDC_D23/GPIO3_D3_d

B

RK3326



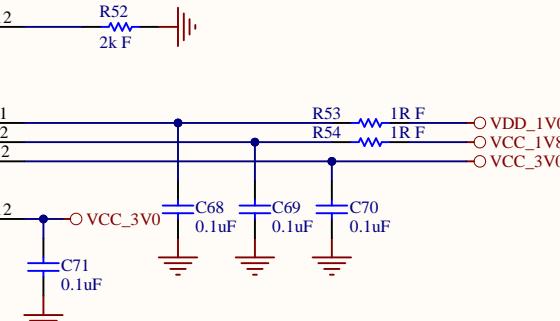
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LVDS_RBIAS

VCCIO4

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RK3326

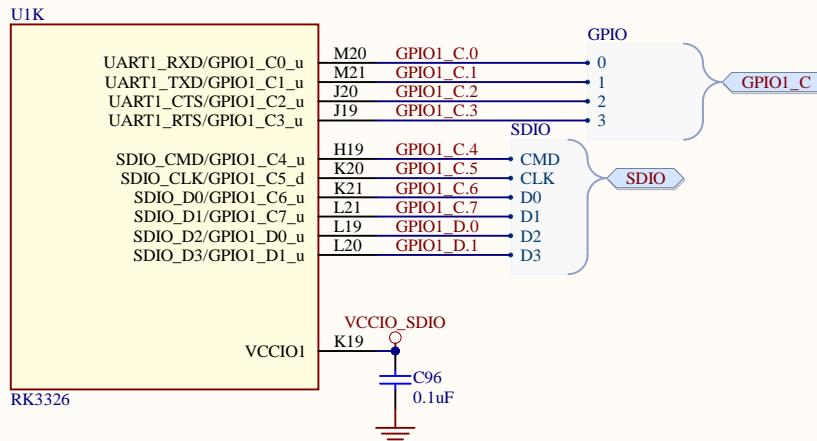
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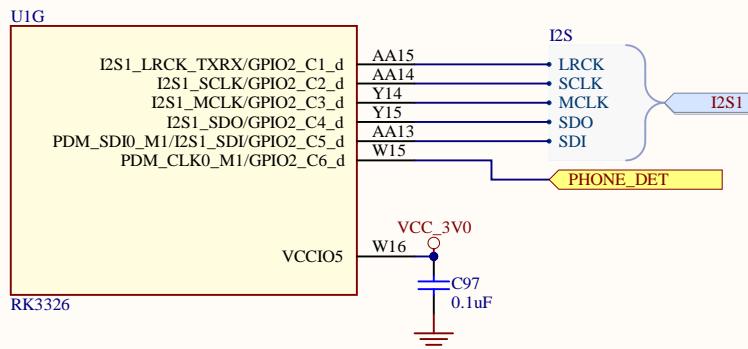
A



VCCIO_SDIO VCC_3V0

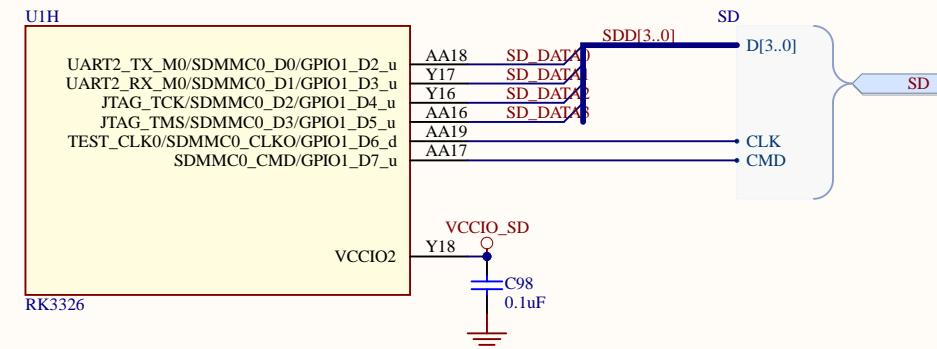
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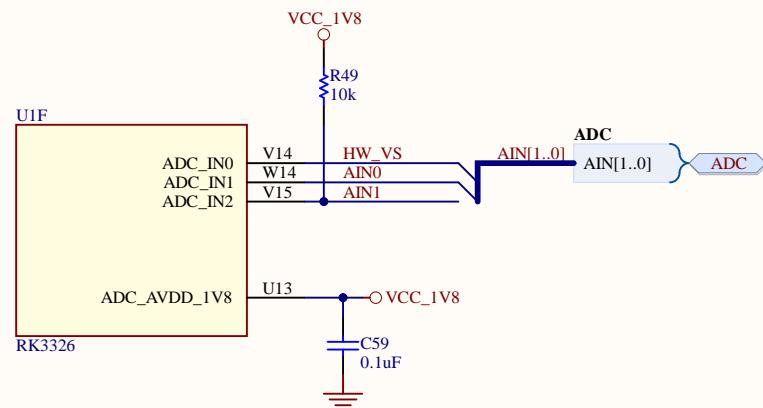
C

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Title RK3326 UART/SDIO/I2S/SDMMC			Hardkernel Co.,LTD.
Size: A4			475-1 Mananro Manangu, Anyang City Gyeonggi-do South Korea
Number:	8.	Revision:	1.0
Date:	2020-05-20	Time:	오후 6:37:57
Sheet 8 of 24			Designed by: *@hardkernel.com
File: RK3326_UART_SDIO_I2S_SDMMC.SchDoc			

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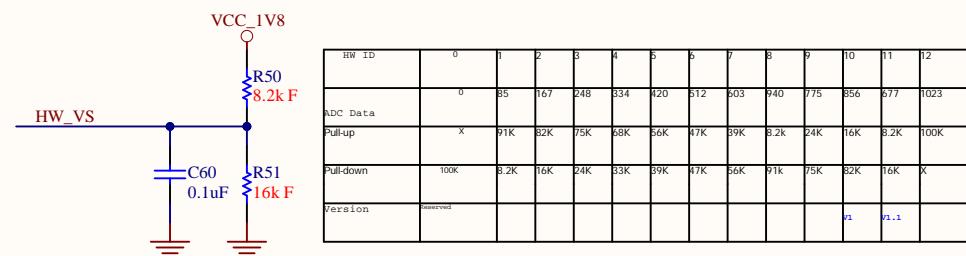
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Title **RK3326 SARADC**

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South Korea

ODROID

Size: **A4** Number: **9.** Revision: **0.3**

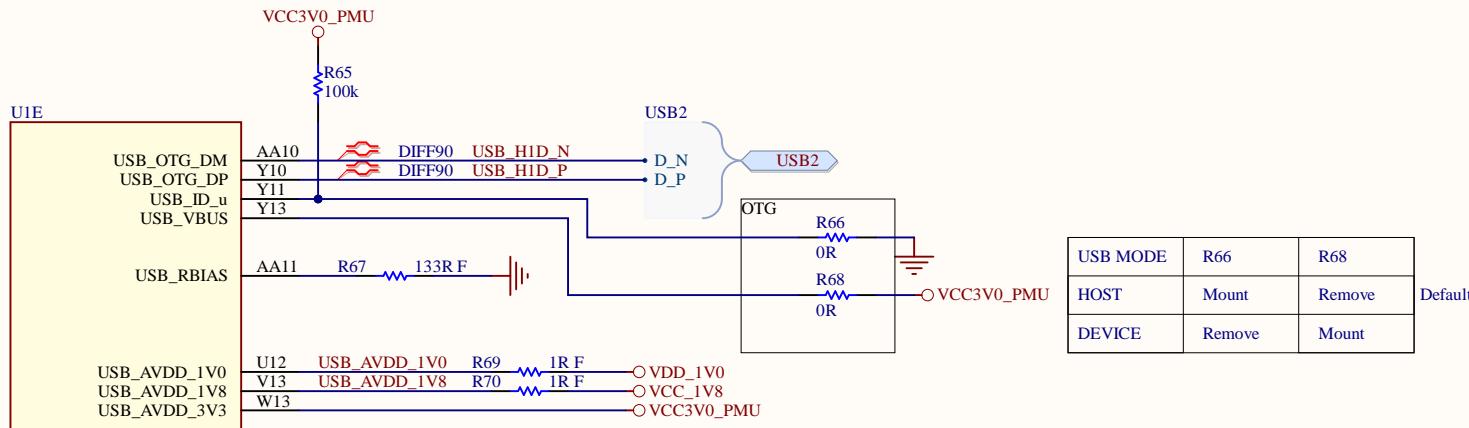
Date: 2020-05-20 Time: 오전 6:37:57 Sheet **9** of **24**

File: **RK3326_ADC.SchDoc**

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Title *RK3326 USB Controller*

Size: A4 Number: 10 Revision: 0.3

Date: 2020-05-20 Time: 오후 6:37:57 Sheet 10 of 24

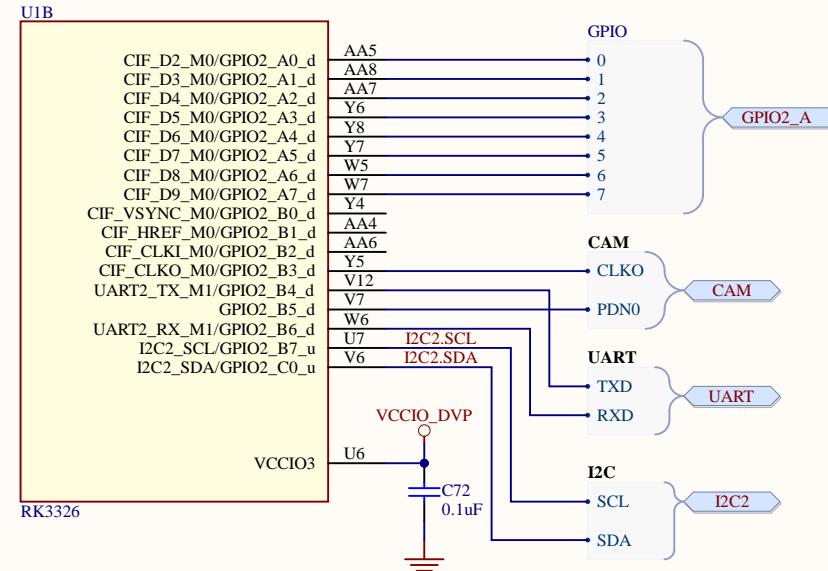
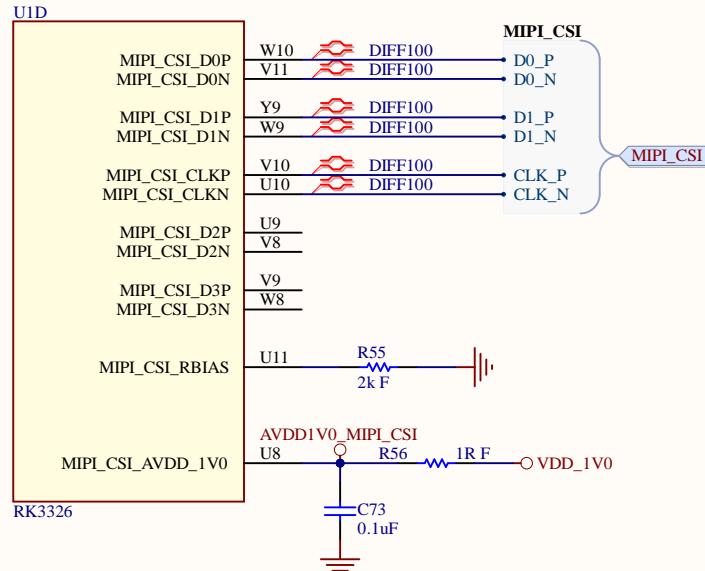
File: RK3326_USB.SchDoc

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RK3326 DVP Interface			<i>Hardkernel Co.,LTD. 475-1 Mananro Mananu, Anyang City Gyeonggi-do South Korea</i>	ODROID
Size: A4	Number: 11.	Revision: 0.3	Date: 2020-05-20 Time: 오후 6:37:57 Sheet 11 of 24	
File: RK3326_DVP.SchDoc		Designed by: *@hardkernel.com		

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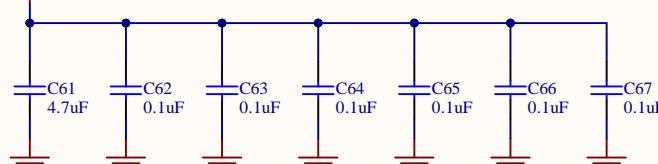
D

U1A

DDR0_D0	R4
DDR0_D1	R1
DDR0_D2	U3
DDR0_D3	Y2
DDR0_D4	N2
DDR0_D5	R3
DDR0_D6	R2
DDR0_D7	Y1
DDR0_D8	AA2
DDR0_D9	P3
DDR0_D10	Y3
DDR0_D11	M2
DDR0_D12	V4
DDR0_D13	M3
DDR0_D14	V3
DDR0_D15	N4
DDR0_D16	F4
DDR0_D17	G3
DDR0_D18	F3
DDR0_D19	K2
DDR0_D20	L2
DDR0_D21	M1
DDR0_D22	E2
DDR0_D23	L1
DDR0_D24	K3
DDR0_D25	J4
DDR0_D26	C2
DDR0_D27	D2
DDR0_D28	K4
DDR0_D29	J3
DDR0_D30	D1
DDR0_D31	E1
DDR0_DQS_P0	W2
DDR0_DQS_N0	W1
DDR0_DQS_P1	U2
DDR0_DQS_N1	U1
DDR0_DQS_P2	J2
DDR0_DQS_N2	J1
DDR0_DQS_P3	G2
DDR0_DQS_N3	G1
DDR0_DMO	T2
DDR0_DM1	U4
DDR0_DM2	G4
DDR0_DM3	M4
DDR0_DQS0_P	
DDR0_DQS0_N	
DDR_DQS1_P	
DDR_DQS1_N	
DDR_DQS2_P	
DDR_DQS2_N	
DDR_DQS3_P	
DDR_DQS3_N	
DDR_DM0	
DDR_DM1	
DDR_DM2	
DDR_DM3	

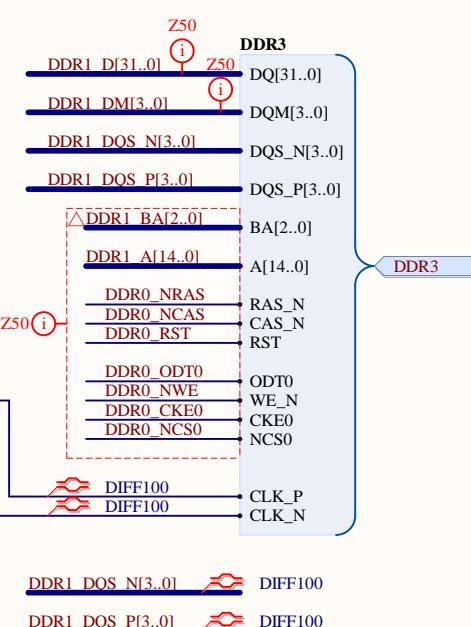
RK3326

VCC_DDR



DDR3_A0/DDR4_A10	C7	DDR0_A0
DDR3_A1/DDR4_A9	D10	DDR0_A1
DDR3_A2/DDR4_A4	D8	DDR0_A2
DDR3_A3/DDR4_A6	B5	DDR0_A3
DDR3_A4/DDR4_A5	A8	DDR0_A4
DDR3_A5/DDR4_A8	A4	DDR0_A5
DDR3_A6/DDR4_A7	B8	DDR0_A6
DDR3_A7/DDR4_A11	B4	DDR0_A7
DDR3_A8/DDR4_A13	C10	DDR0_A8
DDR3_A9/DDR4_A0	C6	DDR0_A9
DDR3_A10/DDR4_CS0N	A10	DDR0_A10
DDR3_A11/DDR4_A3	B7	DDR0_A11
DDR3_A12/DDR4_BA1	A9	DDR0_A12
DDR3_A13/DDR4_A2	D6	DDR0_A13
DDR3_A14/DDR4_A1	A7	DDR0_A14
DDR3_A15/DDR4_ODT0	C9	
DDR3_BA0/DDR4_BG0	A3	DDR0_BA0
DDR3_BA1/DDR4_CASN/DDR4_A15	B10	DDR0_BA1
DDR3_BA2/DDR4_BA0	A5	DDR0_BA2
DDR3_CS0N/DDR4_ACTN	B3	DDR0_NCS0
DDR3_CS1N/DDR4_CS1N	D11	
DDR3_CLKP/DDR4_CLKP	B2	DDR0_CLK_P
DDR3_CLKN/DDR4_CLKN	B1	DDR0_CLK_N
DDR3_ODT0/DDR4_WEN/DDR4_A14	D5	DDR0_ODT0
DDR3_ODT1/DDR4_ODT1	C11	
DDR3_RESETN/DDR4_RESETN	A2	DDR0_RST
DDR3_CASN/DDR4_A12	D9	DDR0_NCAS
DDR3_RASN/DDR4_CKE	C4	DDR0_NRAS
DDR3_CKE/DDR4_RASN/DDR4_A16	B11	DDR0_CKE0
DDR3_WEN/DDR4_BG1	C8	DDR0_NWE

DDRIOD_VDD_1	F8	VCC_DDR
DDRIOD_VDD_2	F9	
DDRIOD_VDD_3	F10	
DDRIOD_VDD_4	G9	
DDRIOD_VDD_5	G10	
DDRIOD_VDD_6	J6	
DDRIOD_VDD_7	J7	
DDRIOD_VDD_8	K6	
DDRIOD_VDD_9	K7	
DDRIOD_VDD_10	L6	
DDRIOD_VDD_11	L7	
DDRIOD_VDD_12	M6	
DDRIOD_VDD_13	M7	

Title **RK3326 DDR Controller**

Size: A4 Number: 12 Revision: 0.3

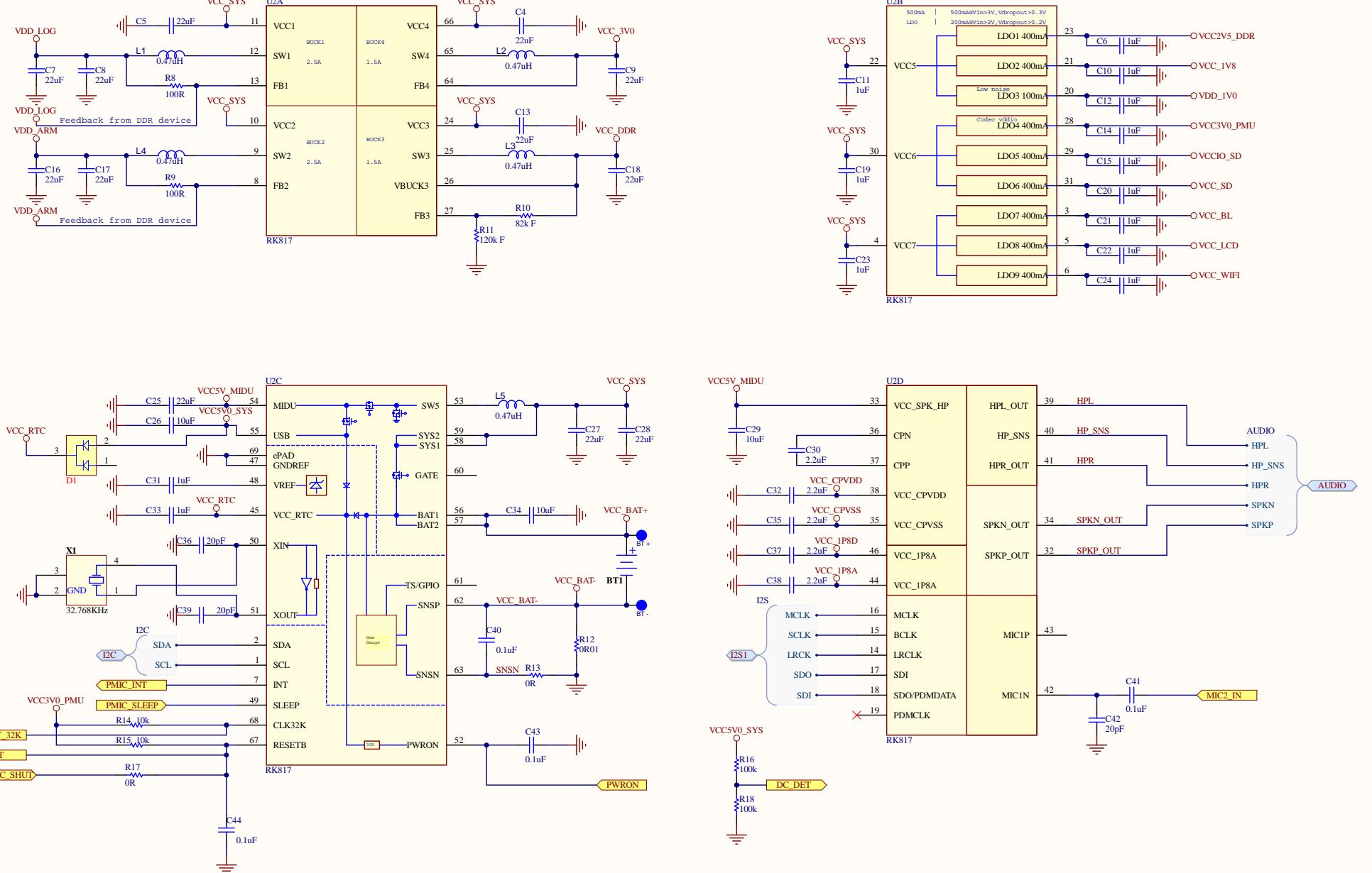
Date: 2020-05-20 Time: 오후 6:37:57 Sheet 12 of 24

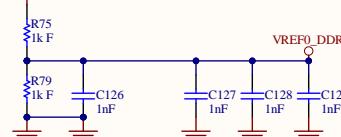
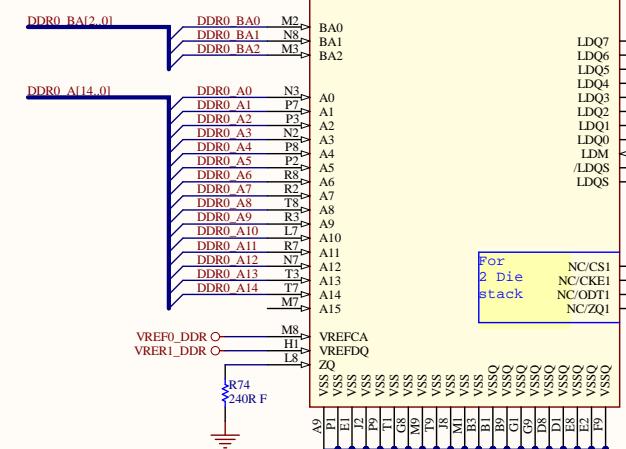
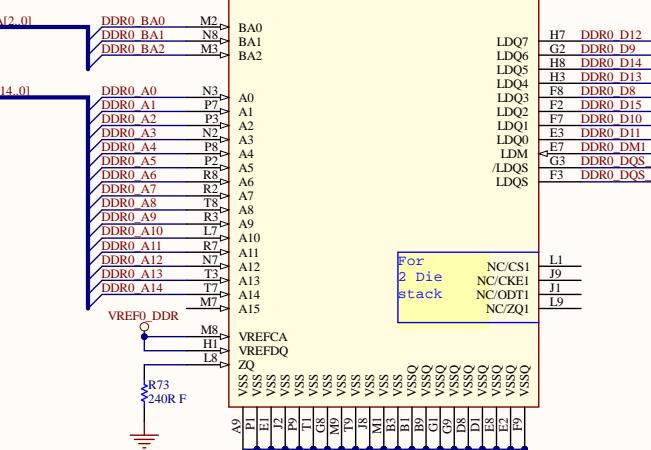
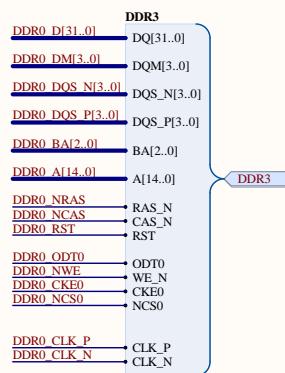
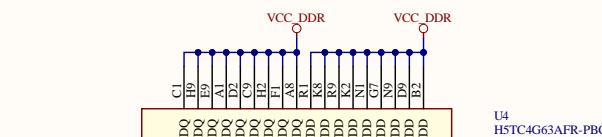
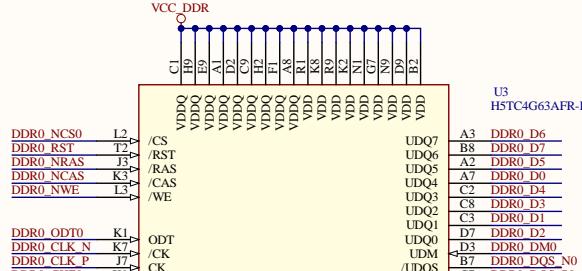
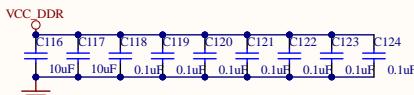
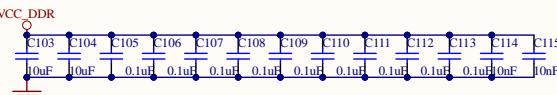
File: RK3326_DDR.SchDoc

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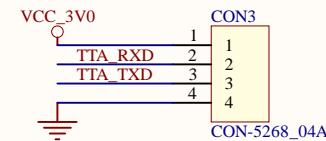
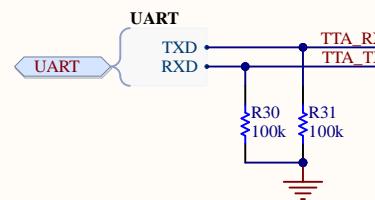
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Title UART Connector		
Size: A4	Number: 15.	Revision: 0.3
Date: 2020-05-20 Time: 오후 6:37:58 Sheet 15 of 24		
File: CON_UART.SchDoc		

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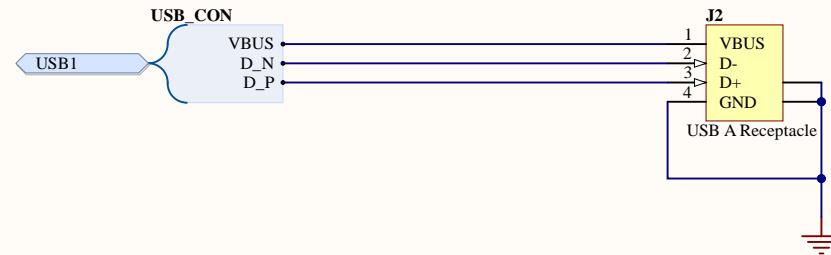
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Title *USB 2.0 Type A Connector*

Size: **A4** Number: **17.** Revision: **0.3**

Date: 2020-05-20 Time: 오후 6:37:58 Sheet 17 of 24

File: CON_USB_RA.SchDoc

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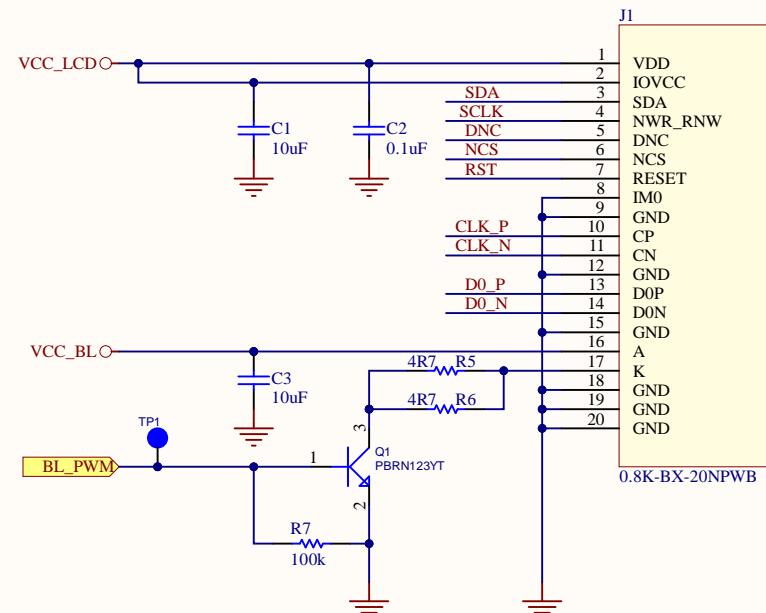
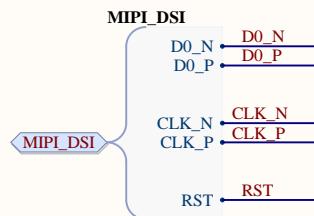
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Title **CON LCD KD35T133-A2**Size: **A4** Number: **17.** Revision: **1.0**

Date: 2020-05-20 Time: 오 6:37:58 Sheet 17 of 24

File: CON_LCD_KD35T133-A2_MIPI.SchDoc

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ODROID

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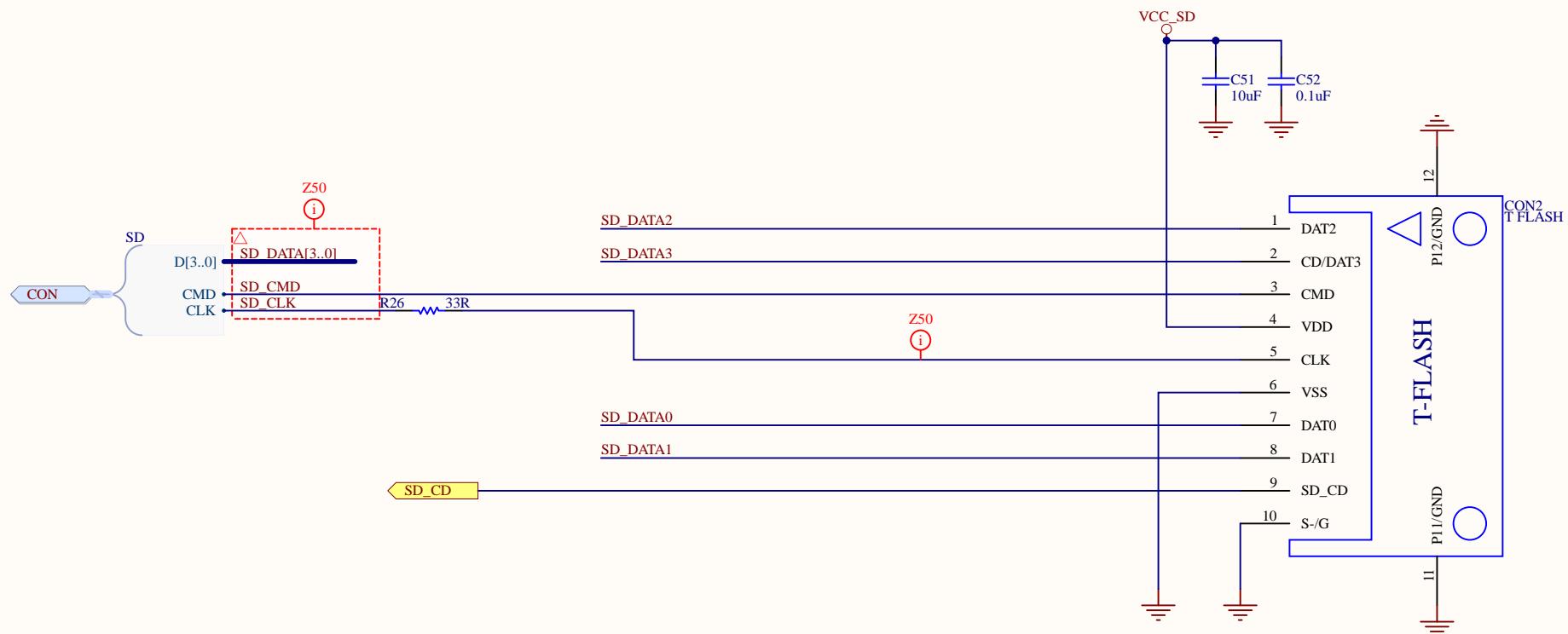
B

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Title **Micro SD Socket**

Size: A4	Number: 18.	Revision: 0.3
Date: 2020-05-20	Time: 오후 6:37:58	Sheet 18 of 24
File: CON_MICROSD_HD08-AL00335.SchDoc		Designed by: ruppi.kim@hardkernel.com

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ODROID

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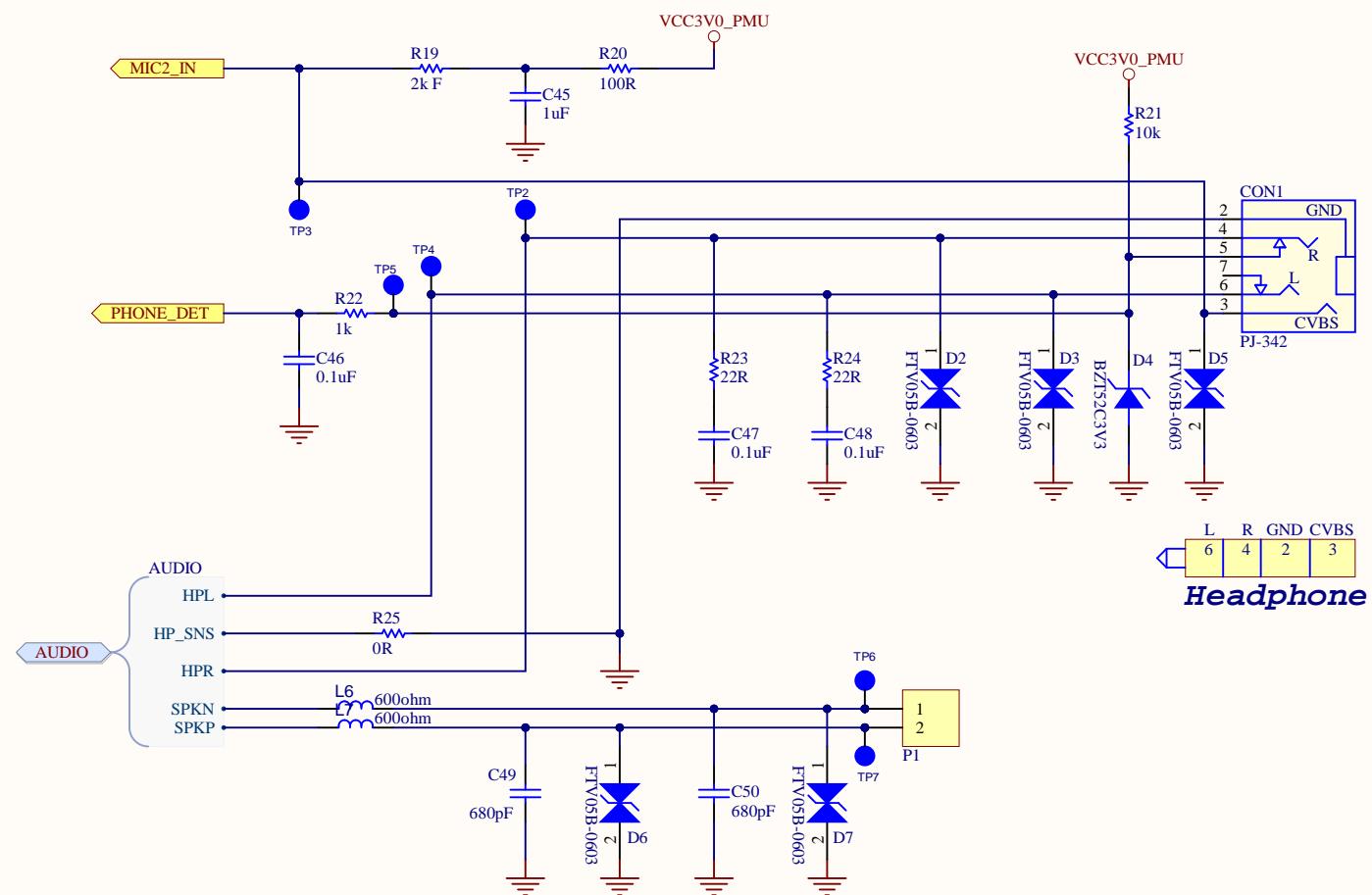
B

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Title *Audio Jack*

 Size: **A4** Number: **19.** Revision: **0.3**

Date: 2020-05-20 Time: 오후 6:37:58

File: CON_AUDIO.SchDoc

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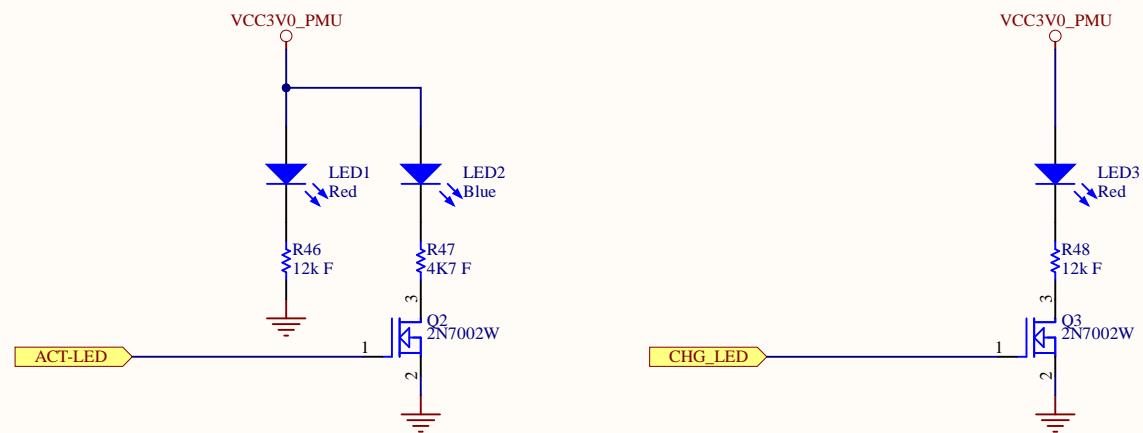
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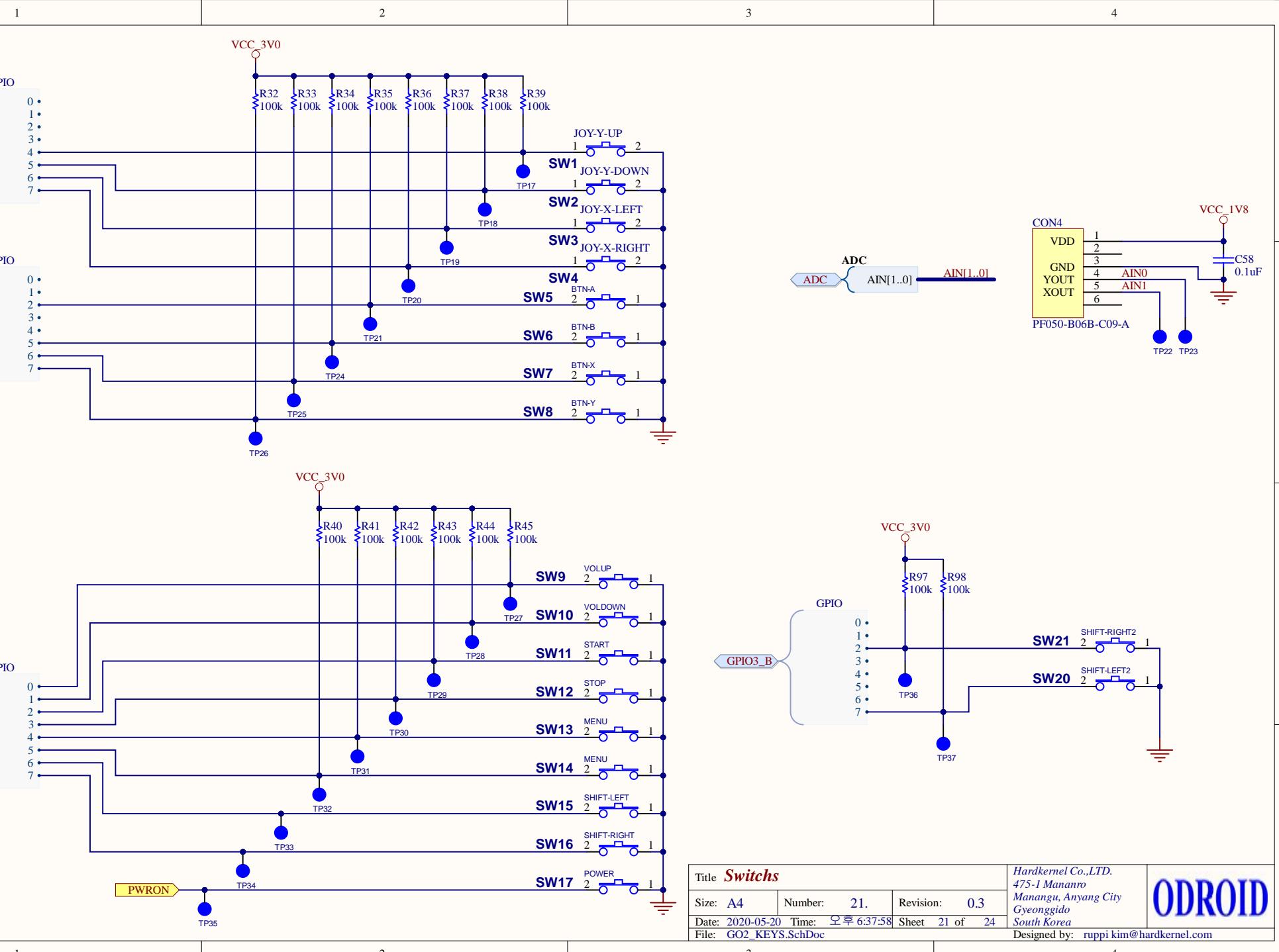
D

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Title *Indicator LEDs*

Size: A4	Number: 20.	Revision: 0.3	Hardkernel Co.,LTD. 475-1 Mananro Manangu, Anyang City Gyeonggi-do South Korea
Date: 2020-05-20	Time: 오후 6:37:58	Sheet 20 of 24	Designed by: ruppi.kim@hardkernel.com
File: GO2_LEDS.SchDoc			

ODROID

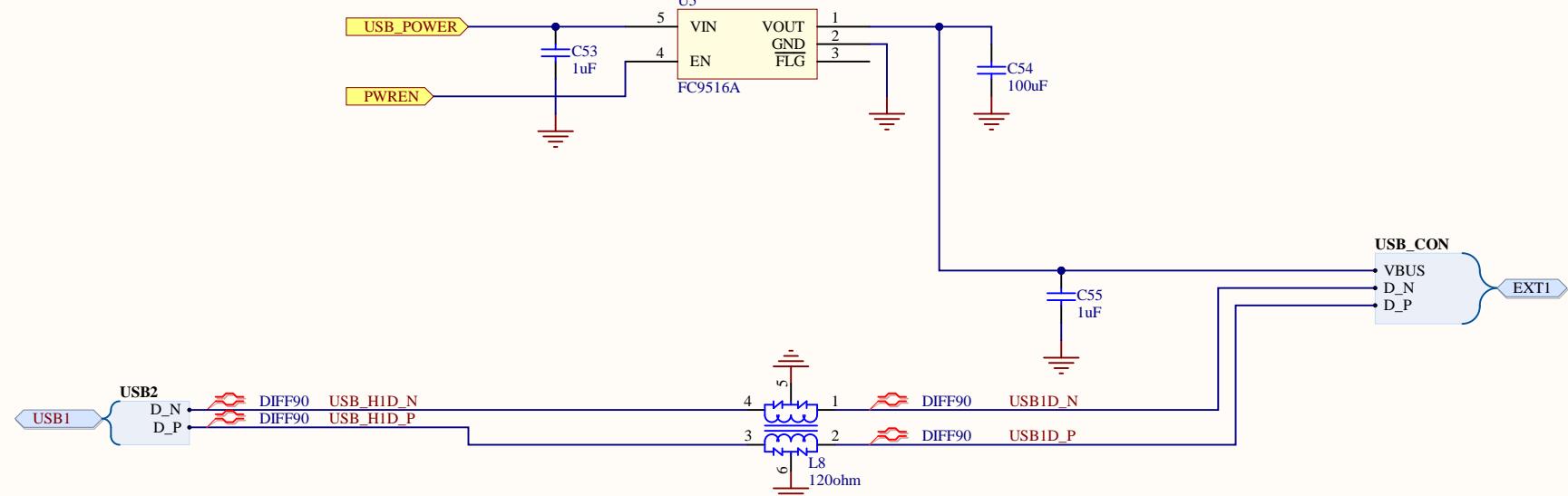


Title **Switches**

Size: A4	Number: 21.	Revision: 0.3	Hardkernel Co.,LTD. 475-1 Mananro Manangu, Anyang City Gyeonggi-do South Korea
Date: 2020-05-20	Time: 오후 6:37:58	Sheet 21 of 24	Designed by: ruppi.kim@hardkernel.com
File: GO2_KEYS.SchDoc			

ODROID

1 2 3 4



Title USB HOST PWR SW			<i>Hardkernel Co.,LTD. 1-605 Shinan Metrokhan 1115 Dalandong, Dongangu, Anyang Gyeonggi-do South Korea</i>
Size: A4	Number: 22.	Revision: 0.3	
Date: 2020-05-20	Time: 오전 6:37:58	Sheet 22 of 24	
File: USB_HOST_PWR_SW.SchDoc			Designed by: ruppi.kim@hardkernel.com

1 2 3 4

ODROID

A

A

B

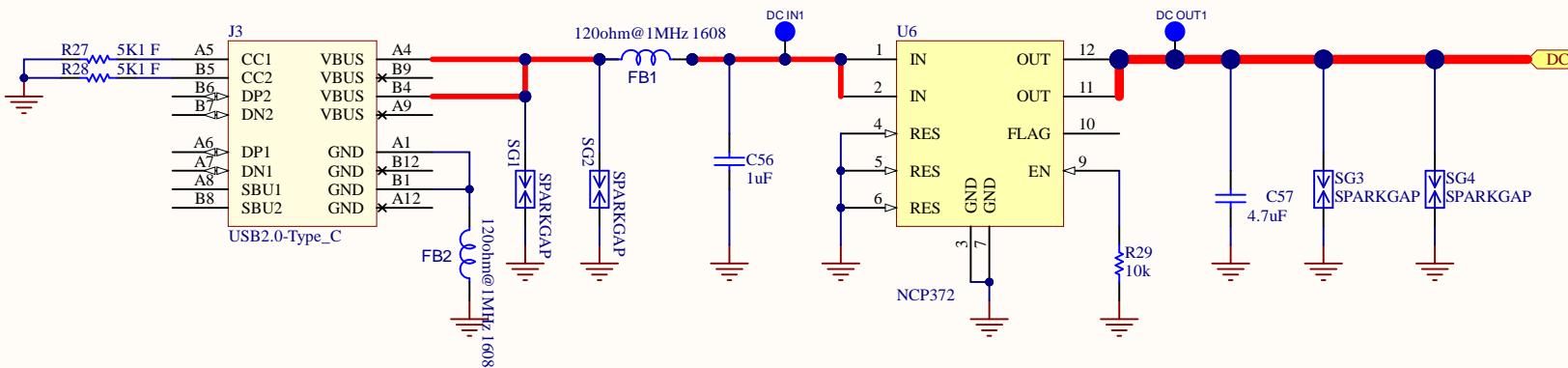
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C

C

D

D

Title **CON_DCJACK**Size: **A4**Number: **23.**Revision: **0.3**

Date: 2020-05-20 Time: 오후 6:37:58

Sheet **23** of **24**

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A

A

B

B

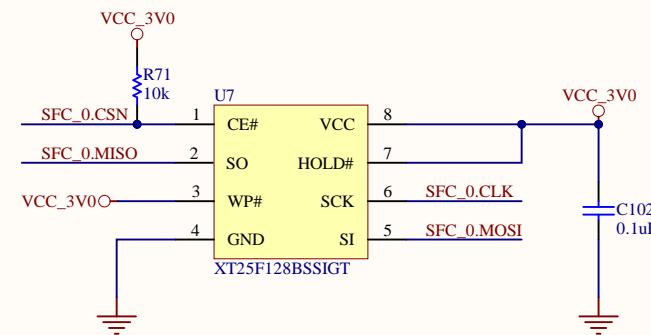
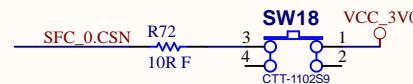
C

C

D

D

SFC_0

Title: ***SPI FLASH MEMORY***

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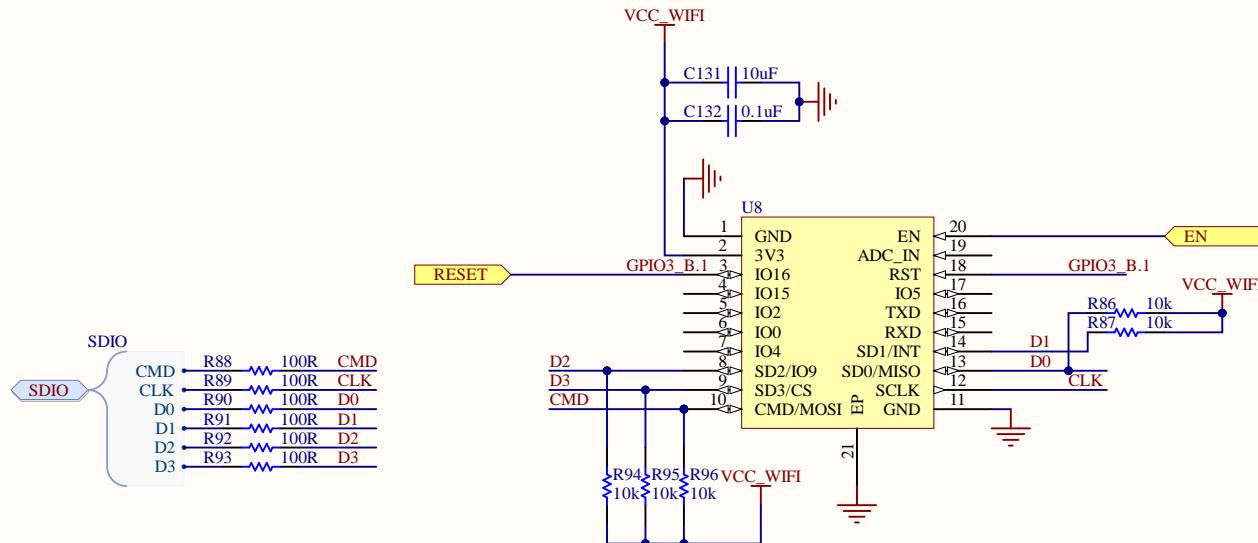
Size: **A4** Number: **24.** Revision: **0.3**

Date: 2020-05-20 Time: 오후 6:37:58 Sheet 24 of 24

File: SPI_FLASH.SchDoc

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Title **WIFI_ESP_WROOM_S2**Size: **A4** Number: **25.** Revision: **0.1**

Date: 2020-05-20 Time: 오후 6:37:58 Sheet 25 of 24

File: **WIFI_ESP_WROOM_S2.SchDoc**

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