Question 1 (5 points). What is the most significant difference between processes and threads?

Answer: the key point is whether they share the same memory space or not. (5 points)

If not directly come to this point, but somehow related (e.g., thread is more lightweight, context switch overhead, communication methods), then give 2-4 points

Question 2 (5 points). Briefly discuss the main advantages and disadvantages of using a Microkernel architecture as opposed to a Monolithic Kernel.

An advantage of using a Microkernel architecture is that it can be more secure and stables, as faults in one service cannot easily propagate to others. (2.5 points)

A disadvantage is the potential performance overhead (low efficiency) due to the need for more context switches and inter-process communication (IPC). (2.5 points)

Microkernl: small, secure and stable, low efficiency, some services in kernel mode others in user mode, better modularity and fault isolation

Monolithic kernel: efficient communication, larger, complex, faster, all the operating system services run in a single address space

Question 3 (5 points). Briefly explain how a system call differs from an API, concerning their interaction with the operating system.

A system call is typically a request directed at the operating system's kernel, while an API call may be a higher-level abstraction that encapsulates system calls, but also provides services by invoking code running in user space. (5 points)

Question 4 (6 points) Suppose we develop a program with a thread library using a one-toone thread model (i.e., Each user-level thread mapped to a kernel thread). Is it possible to support preemptive scheduling for the user-level threads using this thread library? Why?

Yes, (2 points)

This is because each user-level thread is paired with a distinct kernel thread. The operating system's kernel can independently manage these kernel threads, and therefore, it can preempt them based on its scheduling policies. (4 points)

## Question 5. (9 points)

(a) (4 points) Suppose a CPU scheduler favors processes that have utilized the least amount of CPU time in the recent past. Does this scheduler favor I/O-bound programs or CPUbound programs? Why?

It will favor I/O-bound programs (2 points)

Because I/O-bound programs have shorter CPU time (2 points)

Question 6 (10 points). Given a program as shown below. Suppose P1 is a process executing this program on a modern Linux machine.

Program	
void main(){	
pid_t pid;	
fork();	
fork();	_
pid = fork();	
if (pid == 0):{	
printf("Hello!\n"); //print on the screen	
else:{	
printf("Goodbye!\n"); //print on the screen	
printf("Finish!"); //print on the screen	
}	

(a) (5 points) Suppose all fork() are successful. How many processes are created in total, including P1 and all the processes recursively created by P1 (including, e.g., P1's child processes, P1's grandchild processes ...)? Why?

## 8. (2 points)

The first call to fork() results in two processes; each of those two processes then calls the second fork() which results in a total of four processes; the third call to fork() results in eight processes; (3 points)

(b) (5 points) How many times will the message "Hello!\n" be displayed? Why?

## 4. (2 points)

After second call to fork() but before third call to fork(), there are four processes. The third call to fork() results in eight processes, but only the child process (of which there are four) prints "Hello". "Hello!\n" is printed by the child processes created by the third fork() call.

(3 points)

(b) (5 points) With the above-mentioned scheduler, will CPU-bound programs suffer from starvation (i.e., have little chance for execution)? Why?

CPU-bound programs will not starve (2 points),

because I/O-bound programs often suspend themselves for I/O, so CPU-bound programs can execute (3 points)

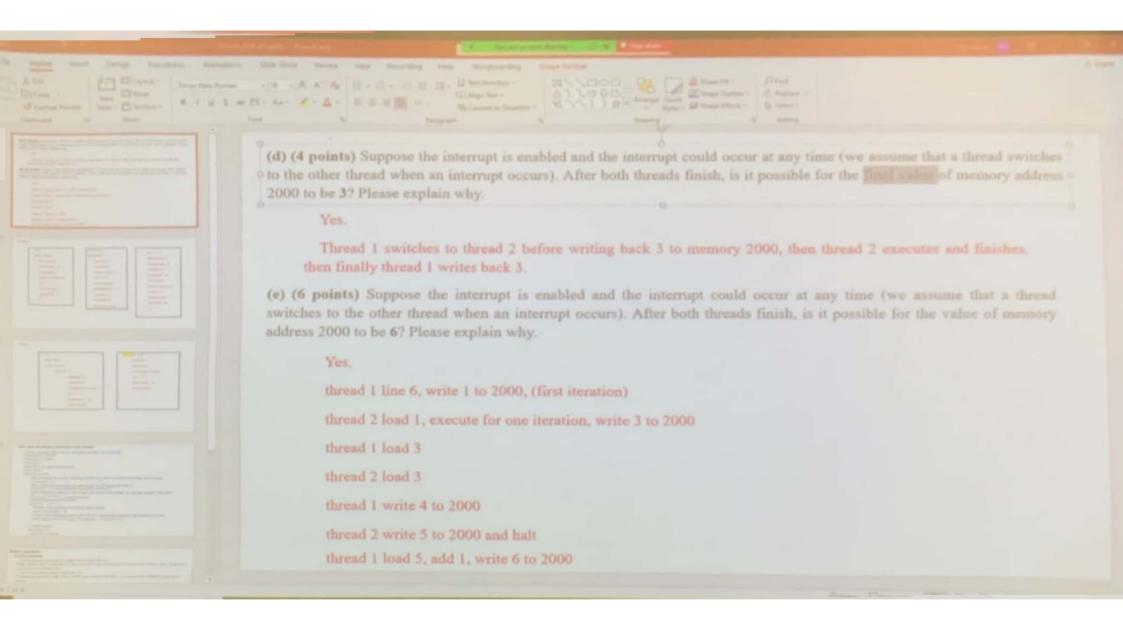
Question 7 (20 points) Consider the following two threads running concurrently on a single-processor machine. The value at the shared memory address 2000 is initialized to 0. Assume that Thread 1 will be scheduled to execute first.

Thread\_1

line	sode	comments	
		# the entry point of the thread	
	mov \$3, hax	if hav is initialized to 3	
	top	If a label	
	mov 2000, lidx	# get the value from the address to register lidx	
	add \$1, tidx	# increment the value in register lidx by 1	
	mov 8:04, 2000	# store the value back to the address	
	sub SI. Nex	# decrement the value in register hax by 1	
	test 50. Nax	# compare the value in register hax with 0	
	igt top	# jump to "top" if the result of the comparison is "greater"	
	halt	# stop runying this thread	

Thread 2

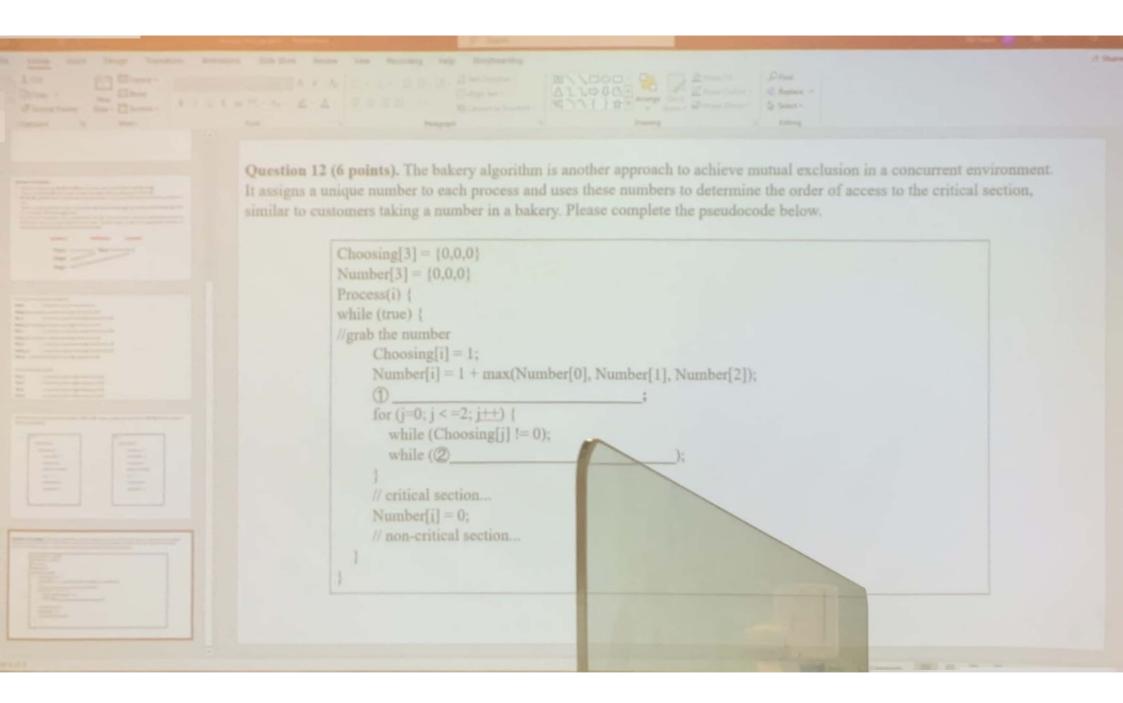
line	code	comments
		# the entry point of the thread
	mov \$5. lax	# Nax is initialized to 5
		# a label
	mov 2000, lids	# get the value from the address to register lids
	add \$2. kdr	# increment the value in register lids by 2
	mov %dx. 2000	# store the value back to the address
	tob \$1, kas	# decrement the value in register Nax by I
	test \$4. kdx	# compare the value in register lids with 4
	and out	# jump to "out" if lidx is greater than 4
	test 50 sax	
	jot top	# jump to "top" if hax is greater than 0
		is a label
		# stop running that there I

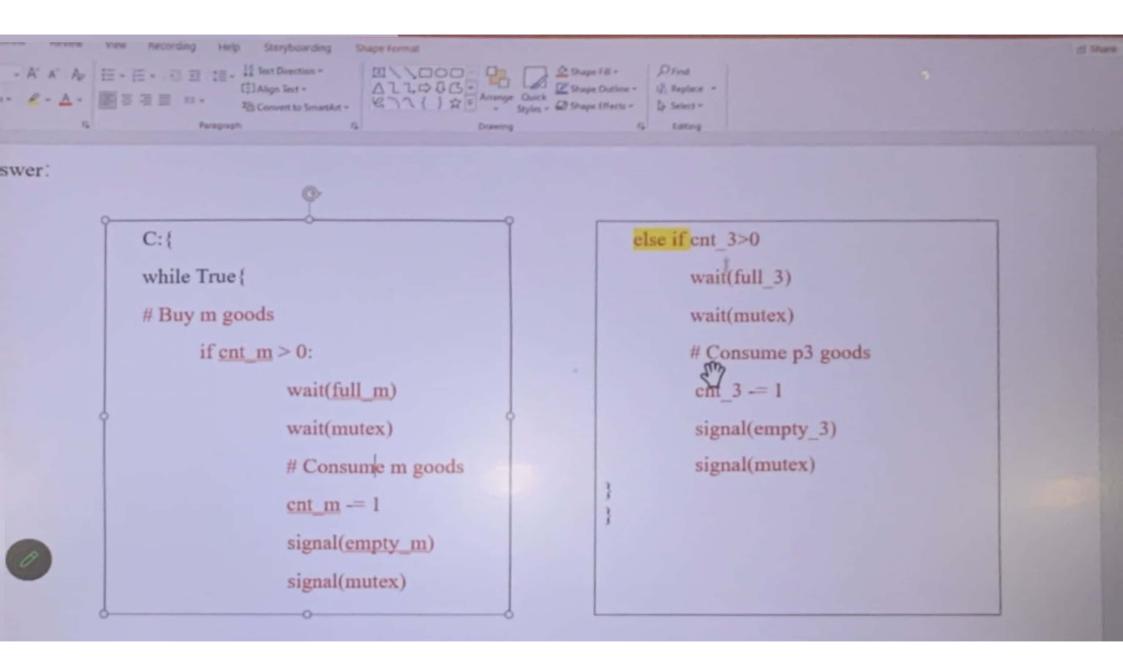


## Question 11 (10 points).

- There are three producers P1, P2, and P3 that can produce three types of products: p1, p2, and p3.
- There is a middleman M who consumes one p1 and one p2 to produce another type of product m.
- P1, P2, P3, and M all need to use the same production facility, so only one of them is allowed to produce products at a time
- There is a consumer C, which either consumes m or p3. When both m and p3 are available, the C consumes m. When
  m is unavailable, C consumes p3 instead.
- Each product is associated with a separate buffer of size N. When the buffer is full, the corresponding producer (or middleman) cannot put more products into the buffer. When the buffer is empty, the corresponding consumer (or middleman) cannot get that type of product from the buffer.

produce	middleman consumer
P1(p1)	$\longrightarrow$ M(m) $\longrightarrow$ C
P2(p2)	
P3(p3)	





The following gives the pseudocode samples of P1 and P2. Please complete the pseudocode of P3, M and C according to the above description.

```
P1: [
                                                           P2: {
                                                               while (True){
   while (True) [
                                                                     #produce p2
      #produce p1
                                                                     wait(empty_2)
         wait(empty_I)
          wait(mutex)
                                                                     wait(mutex)
                                                                     #add p2 to buffer
          #add pl to buffer
                                                                     cnt_2 += 1
          cnt_1 += 1
                                                                     signal(mutex)
          signal(mutex)
                                                                     signal(full_2)
          signal(full_1)
```

