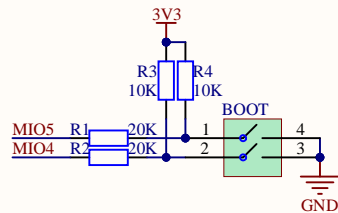
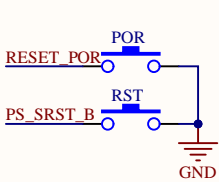
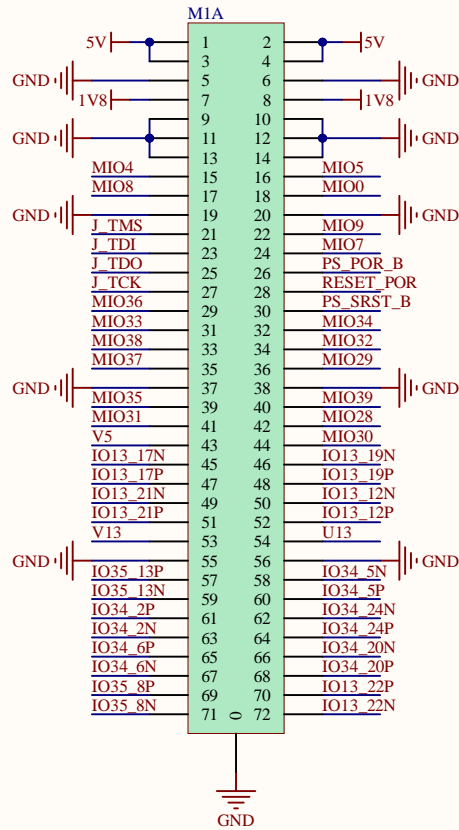


1

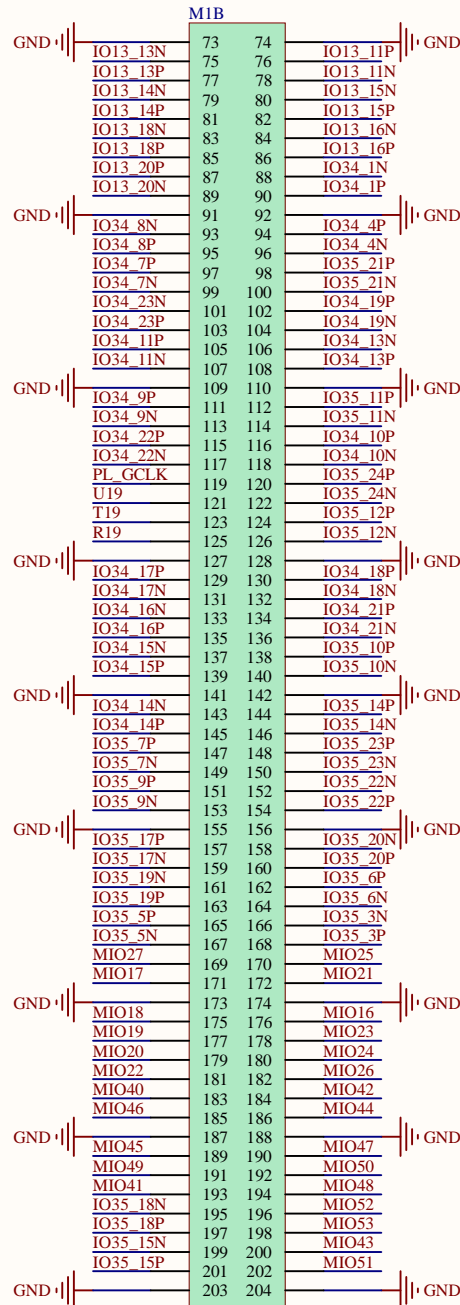
2

3

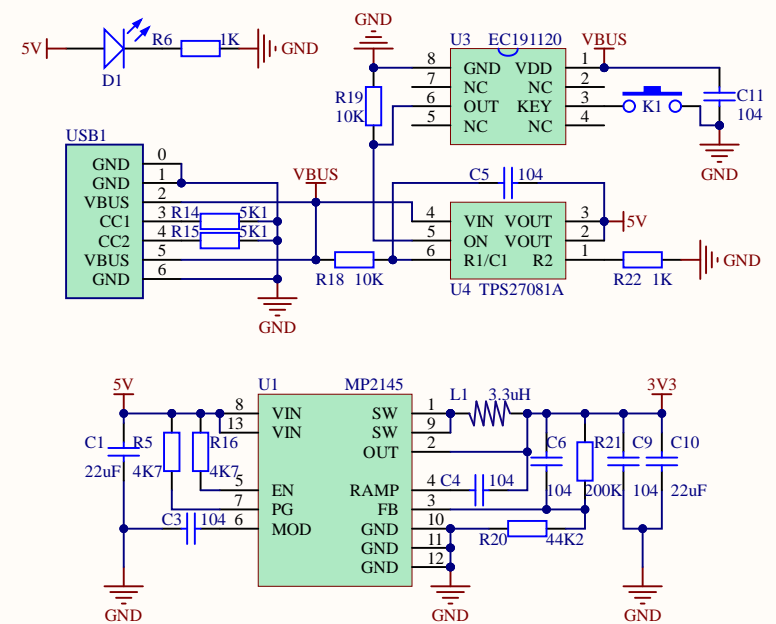
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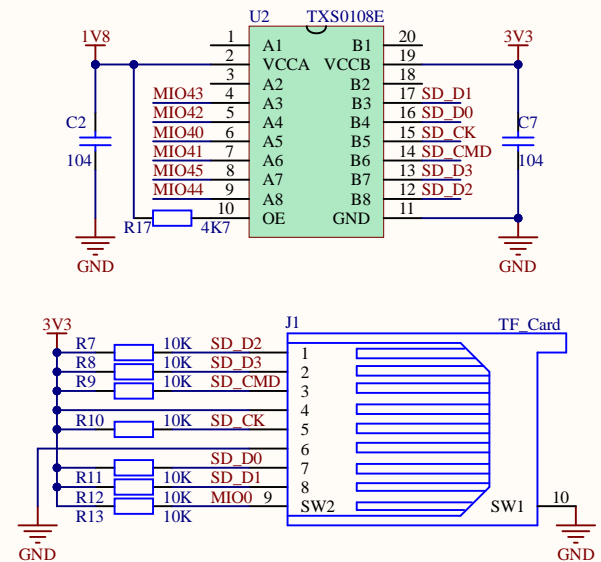
核心板接口



电源



SD卡



1

2

3

4

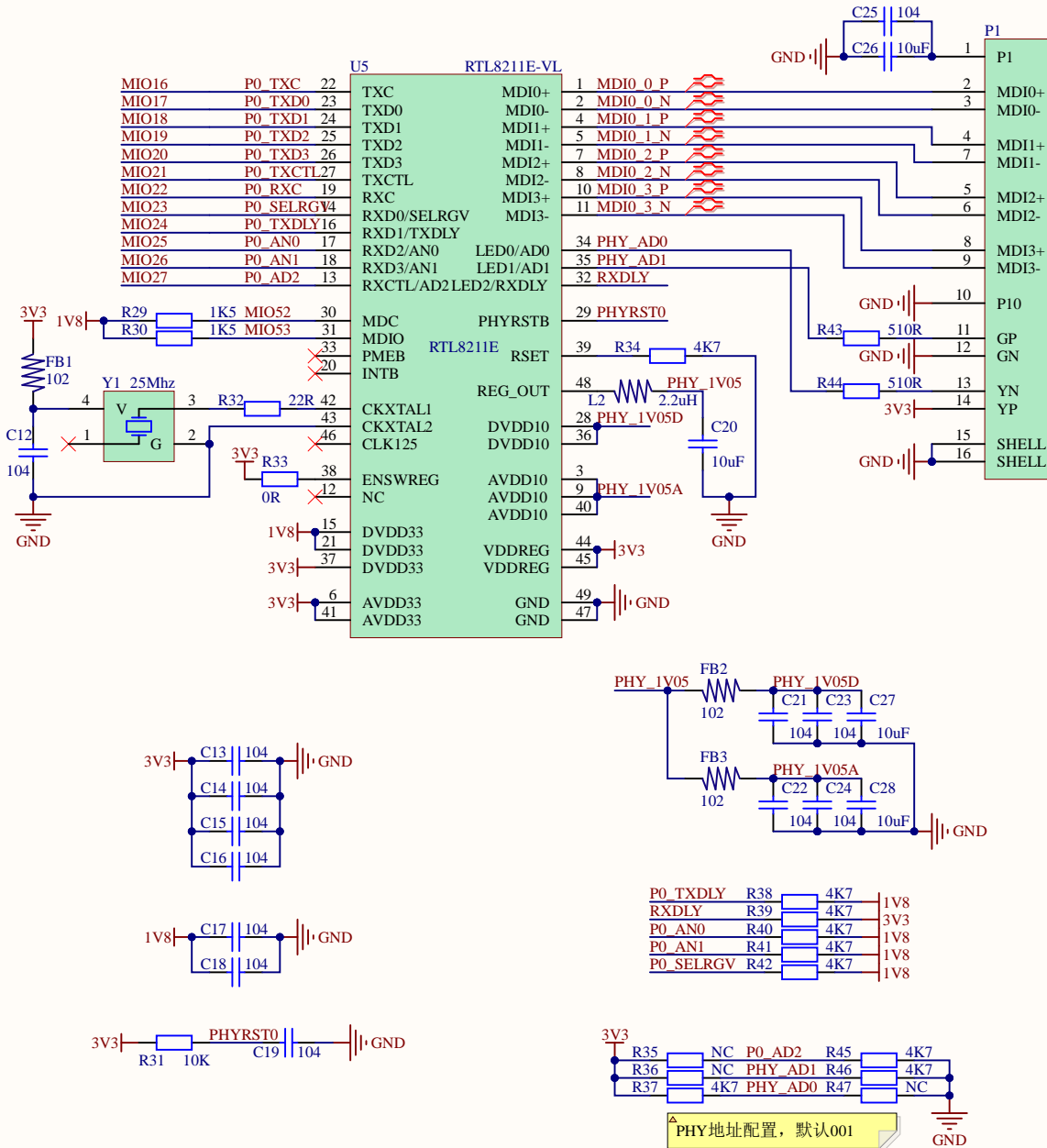
A

B

C

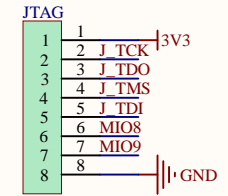
D

PS PHY



3

JTAG模块



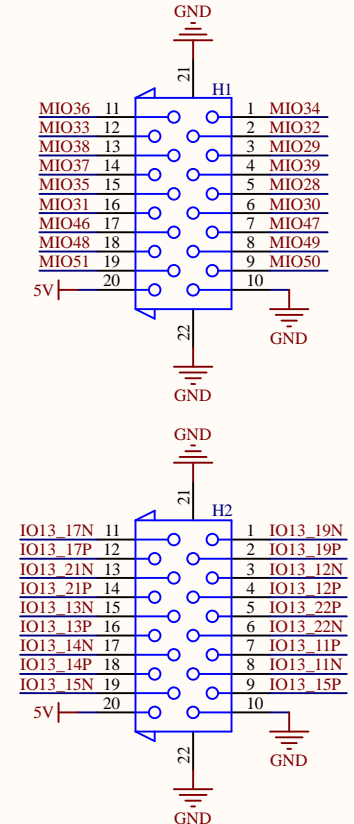
A

B

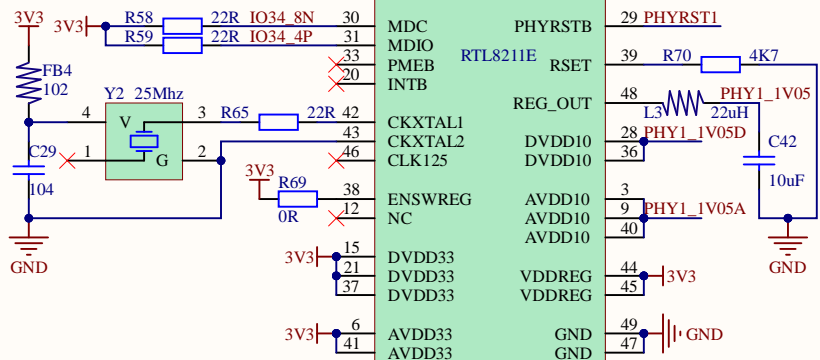
C

D

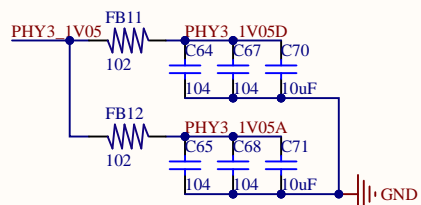
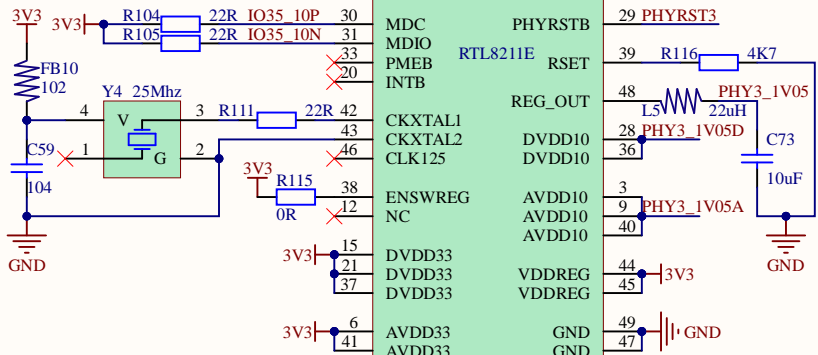
SCSI拓展接口



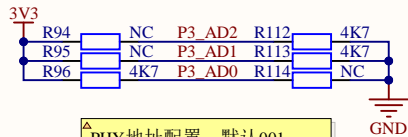
U6			RTL8211E-VB		
IO34_6P	P1_TXC	22	TXC	MDIO+	1 MDI0_0 P
IO34_20N	P1_TXD0	23	TXD0	MDIO-	2 MDI0_0 N
IO34_6N	P1_TXD1	24	TXD1	MDI1+	4 MDI1_1 P
IO34_20P	P1_TXD2	25	TXD2	MDI1-	5 MDI1_1 N
IO34_1N	P1_TXD3	26	TXD3	MDI2+	7 MDI2_2 P
IO34_1P	P1_TXCTL	27	TXCTL	MDI2-	8 MDI2_2 N
IO34_24P	P1_RXC	19	RXC	MDI3+	10 MDI2_3 P
IO34_5P	P1_SELRGV14		RXD0/SELRGV	MDI3-	11 MDI2_3 N
IO34_2P	P1_TXDLY	16	RXD1/TXDLY		
IO34_24N	P1_AN0	17	RXD2/AN0	LED0/AD0	34 P1_AD0 LED1_YN
IO34_2N	P1_AN1	18	RXD3/AN1	LED1/AD1	35 P1_AD1 LED2_GP
IO34_5N	P1_AD2	13	RXCTL/AD2LED2/RXDLY		32 P1_RXDLY



U8			RTL8211E-VB		
IO35_11P	P3_TXC	22	TXC	MDIO+	1 MDI3_0 P
IO35_11N	P3_TXD0	23	TXD0	MDIO-	2 MDI3_0 N
IO35_24P	P3_TXD1	24	TXD1	MDI1+	4 MDI3_1 P
IO35_24N	P3_TXD2	25	TXD2	MDI1-	5 MDI3_1 N
IO35_12P	P3_TXD3	26	TXD3	MDI2+	7 MDI3_2 P
IO35_12N	P3_TXCTL	27	TXD3	MDI2-	8 MDI3_2 N
IO35_21N	P3_RXC	19	TXCTL	MDI2-	10 MDI3_3 P
IO35_13N	P3_SELRGV14		RXC	MDI3+	11 MDI3_3 N
IO35_8P	P3_TXDLY	16	RXD0/SELRGV	MDI3-	
IO35_8N	P3_AN0	17	RXD1/TXDLY		
IO35_21P	P3_AN1	18	RXD2/AN0	LED0/AD0	34 P3_AD0_LED3_YN
IO35_13P	P3_AD2	13	RXD3/AN1	LED1/AD1	35 P3_AD1_LED3_GP
			RXCTL/AD2LED2/RXDLY		32 P3_RXDLY



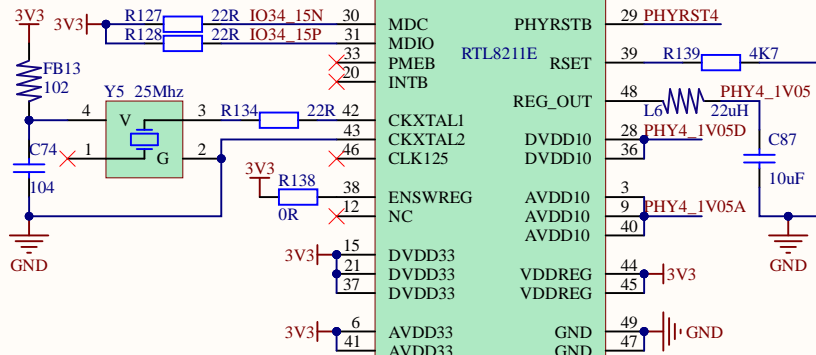
P3_TXDLY	R106	4K7	3V3
P3_RXDLY	R107	4K7	3V3
P3_AN0	R108	4K7	3V3
P3_AN1	R109	4K7	3V3
P3_SELRGV	R110	4K7	3V3



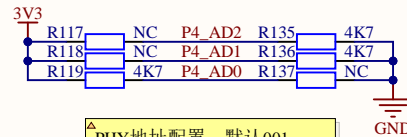
PHY地址配置, 默认001

总线2 A路

U9			RTL8211E-VB		
IO34_17N	P4_TXC	22	TXC	MDIO+	1 MDI4_0 P
IO34_18N	P4_TXD0	23	TXD0	MDIO-	2 MDI4_0 N
IO34_16N	P4_TXD1	24	TXD1	MDI1+	4 MDI4_1 P
IO34_21P	P4_TXD2	25	TXD2	MDI1-	5 MDI4_1 N
IO34_16P	P4_TXD3	26	TXD3	MDI2+	7 MDI4_2 P
IO34_21N	P4_TXCTL	27	TXD3	MDI2-	8 MDI4_2 N
IO34_18P	P4_RXC	19	TXCTL	MDI2-	10 MDI4_3 P
IO34_10P	P4_SELRGV14		RXC	MDI3+	11 MDI4_3 N
IO34_22N	P4_TXDLY	16	RXD0/SELRGV	MDI3-	
IO34_10N	P4_AN0	17	RXD1/TXDLY		
IO34_17P	P4_AN1	18	RXD2/AN0	LED0/AD0	34 P4_AD0_LED4_YN
IO34_22P	P4_AD2	13	RXD3/AN1	LED1/AD1	35 P4_AD1_LED4_GP
			RXCTL/AD2LED2/RXDLY		32 P4_RXDLY

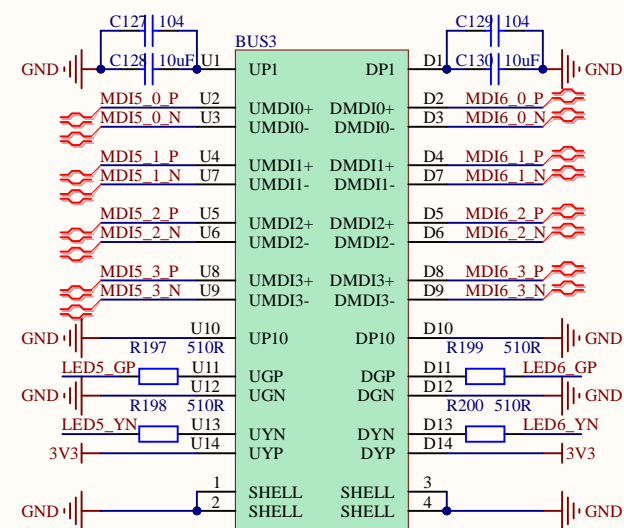


P4_TXDLY	R129	4K7	3V3
P4_RXDLY	R130	4K7	3V3
P4_AN0	R131	4K7	3V3
P4_AN1	R132	4K7	3V3
P4_SELRGV	R133	4K7	3V3

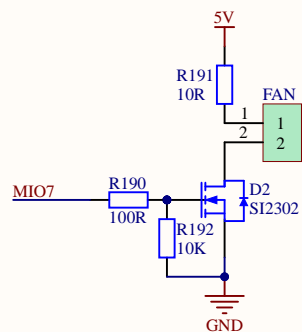


PHY地址配置, 默认001

总线2 B路



OLED模块



散热风扇