

ANALOG IO

Pin	ADC	DAC
ANAL0	1	1
ANAL1	2	2
ANAL2	3	3
ANAL3	4	4
ANAL4	5	5
ANAL5	6	6
ANAL6	7	7
ANAL7	8	8
ANAL8	9	9
ANAL9	10	10
ANAL10	11	11

NB: For best performance, signals should only be tested or output, not modules.

CLK/TRIGGER I/O

INTERNAL CLK/TRIG

LINK POWER

LINK B

LINK A

DES CORE POWER

ANALOG POWER

HPC FMC

Pin Counts
Total I/O pins: 114
L200 pins: 9 (FPGA)
55 pins: 104 (Desires)
50 pins: 104 (Desires)
50 pins: 104 (Desires)

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