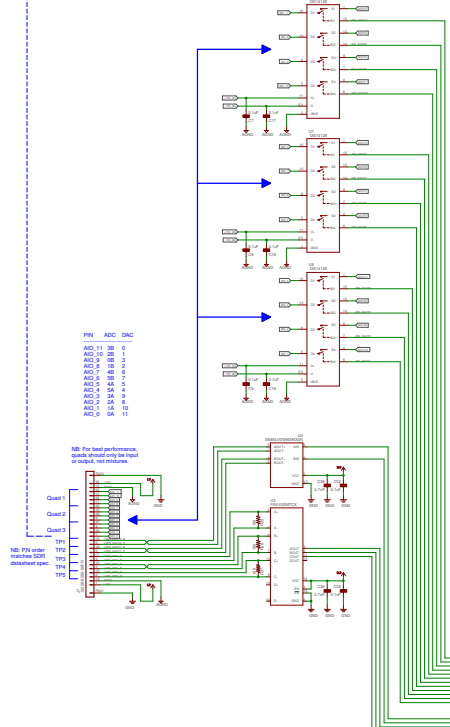
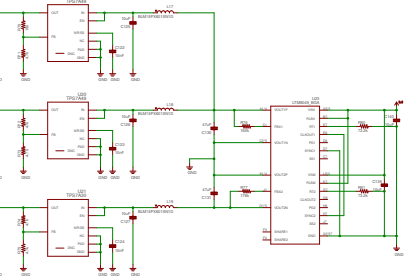


ANALOG IO



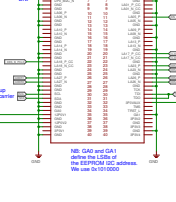
ANALOG POWER



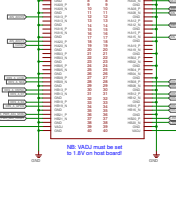
HPC FMC



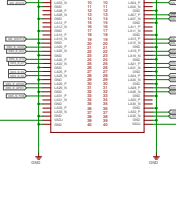
HPC FMC



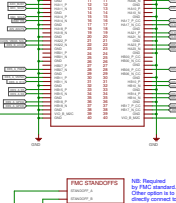
HPC FMC



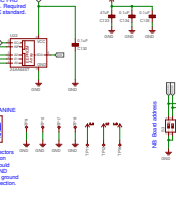
HPC FMC



HPC FMC



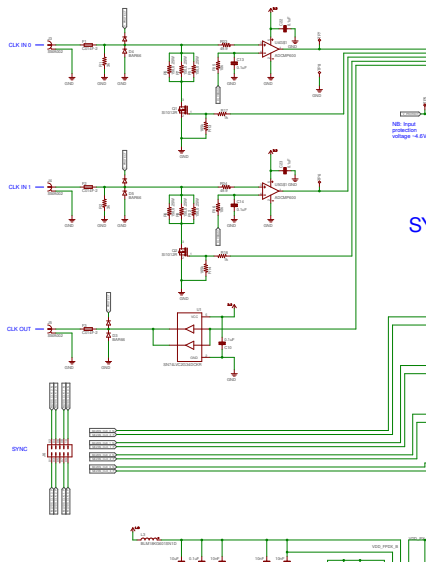
HPC FMC



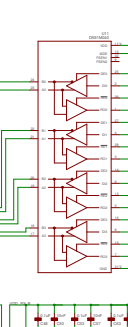
HPC FMC



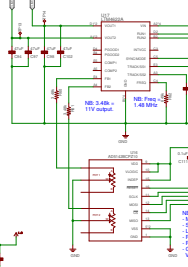
CLOCK IO



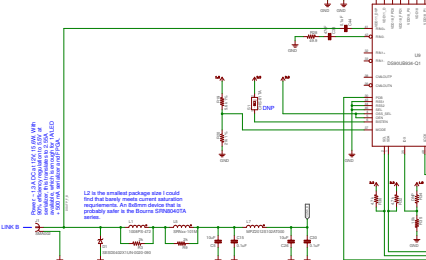
SYNC. TRIG.



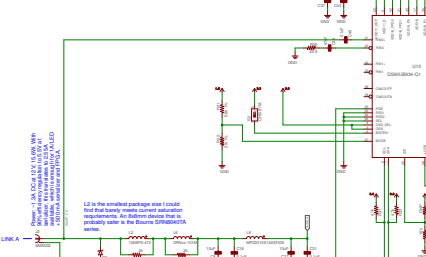
LINK POWER



LINK B



LINK A



DES. CORE POWER



NOTE: Worst case 100 mA max from both ADC and DAC. With 10V amplifiers, this is 100 mA max.

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