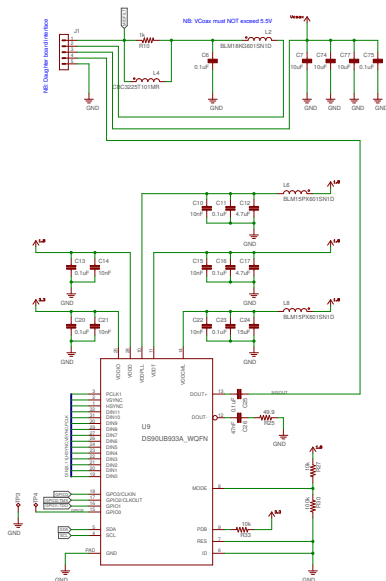
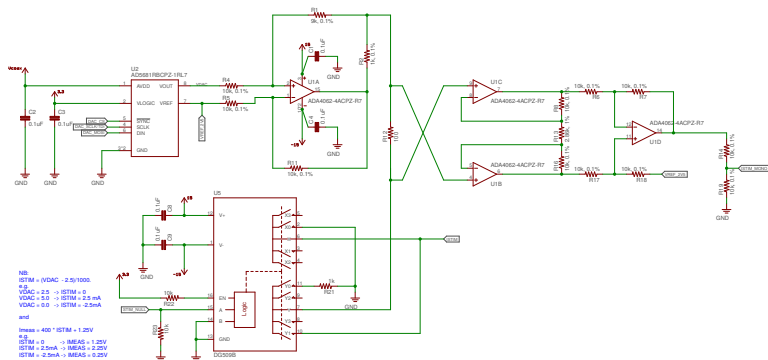


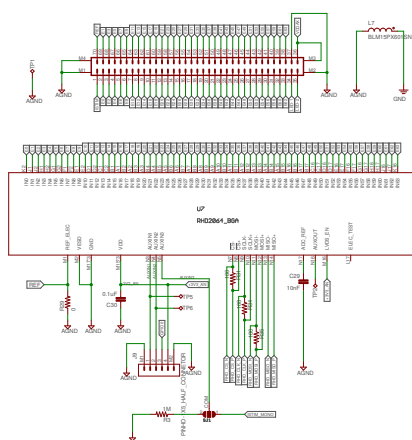
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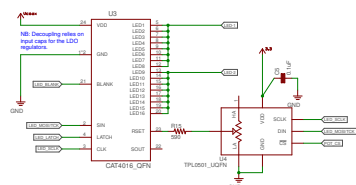
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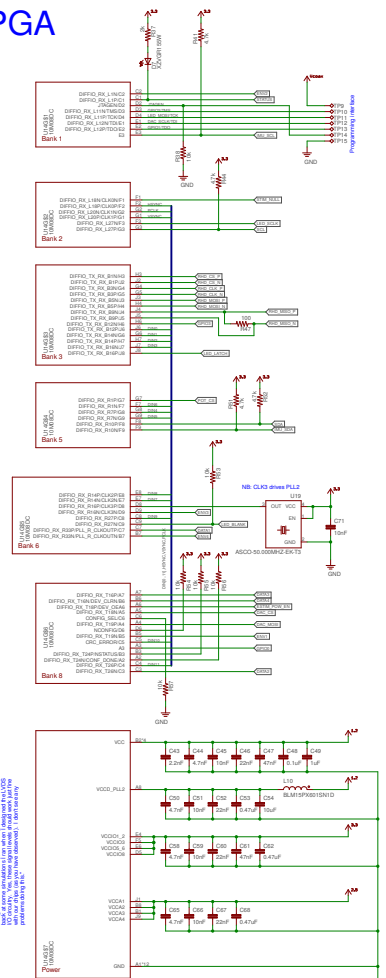
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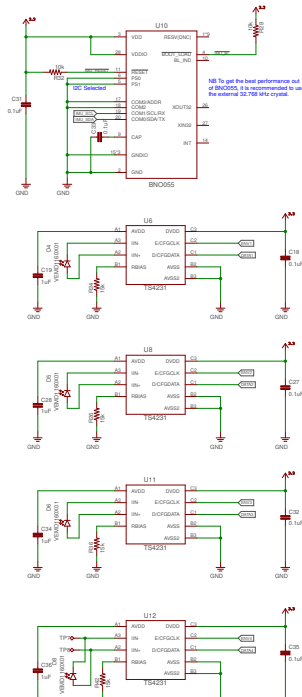
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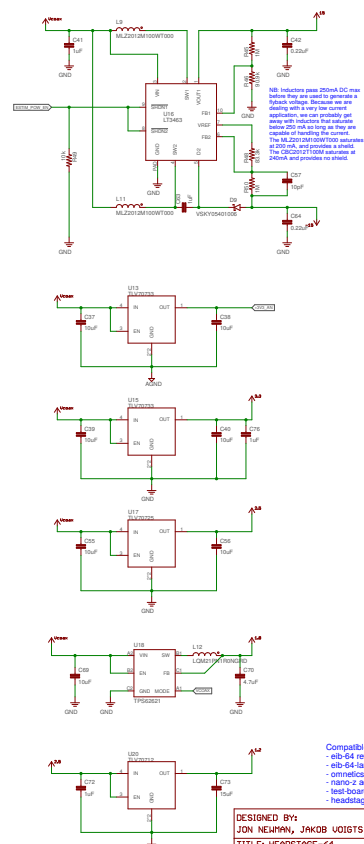
FPGA



TRACKING



POWER



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JON NEWMAN, JAKOB VOIGTS
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