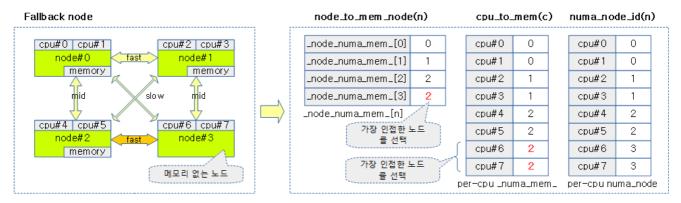
# **NUMA -2- (Fallback Node)**

<kernel v5.10>

In systems with the NUMA architecture, there is an API that can be used to use adjacent memory banks if there is no memory bank on a particular node.

# NUMA -2- (Fallback node)

When attempting to allocate memory on the current node, if memory does not exist on your node, the node chooses the nearest (fastest) node with memory as the fallback node.



(http://jake.dothome.co.kr/wp-content/uploads/2016/06/fallback-node-1b.png)

- node\_numa\_mem\_[3]: Node#3 has no memory, so it contains the nearest node id 2 that has memory.
- per-cpu \_numa\_mem\_: Node #67\ 7, which corresponds to CPU#3 and CPU#3, but has no memory, so it contains the nearest node ID 2 that has memory.
- per-cpu numa\_node: Node #6 corresponding to CPU#7 and CPU#3. (With or without memory)

### numa\_node\_id()

include/linux/topology.h

```
01
    #ifdef CONFIG_USE_PERCPU_NUMA_NODE_ID
02
    #ifndef numa_node_id
03
    /* Returns the number of the current Node */
    static inline int numa_node_id(void)
04
05
            return raw_cpu_read(numa_node);
06
07
08
    #endif
09
    #else
    /* Returns the number of the current Node. */
10
    #ifndef numa_node_id
```

```
12  static inline int numa_node_id(void)
13  {
14          return cpu_to_node(raw_smp_processor_id());
15  }
16  #endif
17  #endif
```

Returns the node ID to which the CPU currently belongs.

- If you use the CONFIG\_USE\_PERCPU\_NUMA\_NODE\_ID kernel option, you can get the numa node ID by reading the numa\_node value in a per-cpu way, and if you don't use this option, you can get the node ID from the current CPU number.
- It's not uncommon to use NUMA on a 32-bit arm, but if you do, using the per-cpu approach allows for faster processing because the node's calculations use TPIDRPRW registers instead of memory.
- Architectures such as x86, PowerPC, and IA64 are also ready for quick implementation.

## numa\_mem\_id()

include/linux/topology.h

```
01 #ifdef CONFIG_HAVE_MEMORYLESS_NODES
02
   #ifndef numa_mem_id
   /* Returns the number of the nearest Node with memory */
03
   static inline int numa_mem_id(void)
04
05
06
            return raw_cpu_read(_numa_mem_);
07
08
   #endif
09
   #else
10
   #ifndef numa_mem_id
   /* Returns the number of the nearest Node with memory */
12
   static inline int numa_mem_id(void)
13
14
            return numa_node_id();
15
   #endif
16
17 #endif
```

Returns the node ID to which the CPU currently belongs, and if this node has no memory, it knows the ID of the nearest node in memory.

- If you use the CONFIG\_HAVE\_MEMORYLESS\_NODES kernel option, it returns the node ID contained in the per-cpu \_numa\_mem\_, which returns the ID of the nearest node with memory on the current node if the node to which the CPU belongs does not have memory, and if you don't use the kernel option, it simply returns the node ID to which the current CPU belongs.
- Consultation:
  - Enable memoryless node on x86 platforms (https://lwn.net/Articles/605155/) | LWN.net
  - Enable memoryless node support for x86 (https://lwn.net/Articles/654702/) | LWN.net

#### node\_to\_mem\_node()

include/linux/topology.h

```
01 #ifdef CONFIG_HAVE_MEMORYLESS_NODES
```

```
#ifndef node_to_mem_node
02
   static inline int node_to_mem_node(int node)
03
04
05
            return _node_numa_mem_[node];
06
07
   #endif
80
   #else
09
   #ifndef node_to_mem_node
   static inline int node_to_mem_node(int node)
10
11
12
            return node;
13
14
   #endif
15 #endif
```

If the specified node has memory, it returns the specified node ID as is, and if there is no memory, it knows the nearest node ID with memory.

• If you use the CONFIG\_HAVE\_MEMORYLESS\_NODES kernel option, it returns an array of \_node\_numa\_mem\_[] values. The \_node\_numa\_mem\_[] array contains the same value as the nodeid used as an array argument, but in the case of a node with no memory, it contains the nearest node id with memory.

# **Related Global Variables**

## per-cpu numa\_node variable

mm/page\_alloc.c

```
#ifdef CONFIG_USE_PERCPU_NUMA_NODE_ID
DEFINE_PER_CPU(int, numa_node);
#endif
```

It contains the node ID to which each CPU belongs.

#### \_node\_numa\_mem Variables

mm/page\_alloc.c

```
#ifdef CONFIG_HAVE_MEMORYLESS_NODES
int _node_numa_mem_[MAX_NUMNODES];
#endif
```

It contains the node ID corresponding to each node in the array, but if that node is a node with no memory, it contains the ID of the adjacent node with memory as the alternate node.

### per-cpu \_numa\_mem\_ variables

mm/page\_alloc.c

```
#ifdef CONFIG_HAVE_MEMORYLESS_NODES
/*

* N.B., Do NOT reference the '_numa_mem_' per cpu variable directly.

* It will not be defined when CONFIG_HAVE_MEMORYLESS_NODES is not defined.
```

It contains the node ID to which the CPU belongs, but if the node is a node with no memory, it contains the ID of a neighboring node with memory as a replacement node.

# consultation

- NUMA -1- (ARM64 initialization) (http://jake.dothome.co.kr/numa-1/) | Qc
- NUMA -2- (Fallback Node) | Sentence C Current post
- NODE Bitmap (API) (http://jake.dothome.co.kr/node-api) | Qc

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✓ Slub Memory Allocator -3- (Create Cache) (http://jake.dothome.co.kr/slub-cache-create/)

Slub Memory Allocator -12- (debugging slub) > (http://jake.dothome.co.kr/slub-debug/)

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