SPEC

Spec No.	TQ3C-8EAF0-E1TAC07-00
Date	July 28, 2016

# TYPE: T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)

< 10.4 inch SVGA transmissive color TFT with LED backlight

and constant current circuit for LED backlight>

#### **CONTENTS**

- 1. Application
- 2. Construction and outline
- 3. Mechanical specifications
- 4. Absolute maximum ratings
- 5. Electrical characteristics
- 6. Optical characteristics
- 7. Interface signals
- 8. Input timing characteristics
- 9. Lot number identification
- 10. Warranty
- 11. Precautions for use
- 12. Reliability test data
- 13. Outline drawing



#### KYOCERA DISPLAY CORPORATION

This specification is subject to change without notice.

Consult Kyocera display before ordering.

l	Original	Designed by:	Engineering dept. Confirmed by: QA			QA dept.
	Issue Date	Prepared	Checked	Approved	Checked	Approved
	July 28, 2016	M. Koyama	Y. Yomazaki	G Matsumoto	O. Sato	I. Hamars

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Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN	-
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### Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

#### Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN	_
	(TCG104SVLQJPNN-AN40)	

#### Revision record

Date		Designo	esigned by : Engineering dept. Confirmed by : QA			: QA dept.	
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Spec	No.	Part No.	Page
TQ	3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	1

### 1. Application

This document defines the specification of T-55787GD104J-LW-AHN. (TCG104SVLQJPNN-AN40) (RoHS Compliant)

#### 2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Anti-Glare treatment

Interface : LVDS

Additional circuit : Timing controller, Power supply (3.3V input)

: with Constant current circuit for LED Backlight(12V input)

#### 3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	230(W) x 180.2(H) x 10.5(D)	mm
Active area	211.2(W) × 158.4(H) (26.4cm/10.4 inch(Diagonal))	mm
Dot format	800×(R,G,B)(W)×600(H)	dot
Dot pitch	0.088(W)×0.264(H)	mm
Base color 2)	Normally Black	-
Mass	475	g

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.



#### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

	Item	Symbol	Min.	Max.	Unit
Supply voltag	ge(+3.3V)	$V_{ m DD}$	-0.3	4.0	V
Supply voltage(+12V)		$V_{\rm IN}$	-0.3	14.0	V
	RxINi+, RxINi- 1) 2)	$V_{I1}$	-0.3	2.8	V
Input signal	CK IN+, CK IN- 2)	$V_{I2}$	-0.3	2.8	V
voltage	SELLVDS	$V_{I3}$	-0.3	V <sub>DD</sub> +0.5	V
	BLBRT, BLEN	$V_{I4}$	-0.3	$V_{\mathrm{IN}}$	V

- 1) i=0,1,2,3
- 2)  $V_{DD}$  must be supplied correctly within the range described in 5-1.

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature (Ambient)	1)	Top(Ambient)	-30	80	$^{\circ}\mathrm{C}$
Operating temperature (Panel)	2)	Top(Panel)	-30	80	$^{\circ}\mathrm{C}$
Storage temperature	3)	$T_{ m STO}$	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	4)	Нор	10	5)	%RH
Storage humidity	4)	$H_{\mathrm{STO}}$	10	5)	%RH
Vibration		-	6)	6)	-
Shock		-	7)	7)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Panel surface temperature (all the surface)
- 3) Temp. =  $-30^{\circ}$ C < 48h , Temp. =  $80^{\circ}$ C < 168h

Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

- (Please refer to "Precautions for Use" for details.)
- 4) Non-condensing
- 5) Temp. ≤ 40°C, 85%RH Max.

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

6)

Frequency	$10{\sim}55\mathrm{Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total)

**EIAJ ED-2531** 

7) Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

EIAJ ED-2531



Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	3

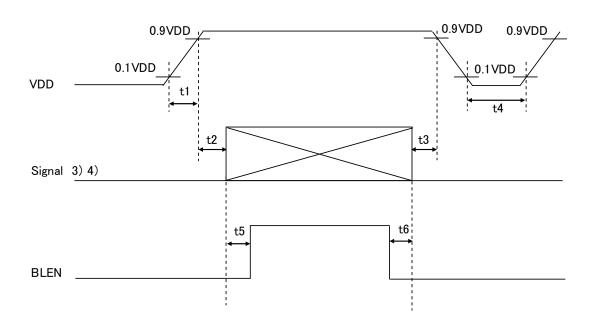
### 5. Electrical characteristics

### 5-1. LCD

Temp. =  $-30 \sim 80$ °C

						remp. –	30 00 0
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage	1)	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption		${ m I}_{ m DD}$	2)		250	300	mA
Permissive input ripple volt	age	$V_{\mathrm{RP}}$	V <sub>DD</sub> =3.3V	-	-	100	mVp-p
T 1 1,	0)	$V_{\mathrm{IL}}$	"Low" level	0	-	0.8	V
Input signal voltage	3)	$V_{\mathrm{IH}}$	"High" level	2.0	-	$V_{ m DD}$	V
T , 1 1	9)	Iol	V <sub>13</sub> =0V	-10	-	10	μΑ
Input leak current	3)	Іон	V <sub>13</sub> =3.3V	-	-	400	μΑ
LVDS Input voltage	4)	$V_{\rm L}$	-	0	-	1.9	V
Differential input voltage	4)	$V_{\mathrm{ID}}$	-	250	350	450	mV
Differential input	4) 5)	$V_{\mathrm{TL}}$	"Low" level	V <sub>CM</sub> -100	-	-	mV
threshold voltage	4) 5)	$V_{\mathrm{TH}}$	"High" level	-	-	V <sub>CM</sub> +100	mV
Terminator		$R_1$	-	-	100	-	Ω
		t1	-	0.1	-	10	ms
		t2	-	0	-	-	ms
177	1)	t3	-	0	-	-	ms
V <sub>DD</sub> -turn-on conditions	1)	t4	-	1.0	-	-	s
		t5	-	200		-	ms
		t6	-	200	-	-	ms

### 1) V<sub>DD</sub>-turn-on conditions

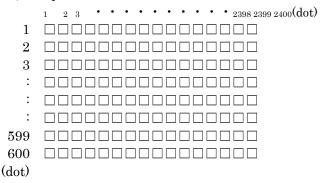




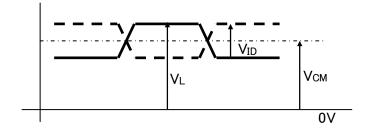
Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	4

2) Display pattern:

$$V_{\rm DD}$$
 = 3.3V, Temp. = 25°C



- 3) Input signal: SELLVDS
- 4) Input signal: RxIN3+, RxIN3-, RxIN2+, RxIN2-, RxIN1+, RxIN1-, RxIN0+, RxIN0-CK IN+, CK IN-



5)  $V_{CM}$ : LVDS Common mode voltage ( $V_{CM}$ =1.25V)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN	5
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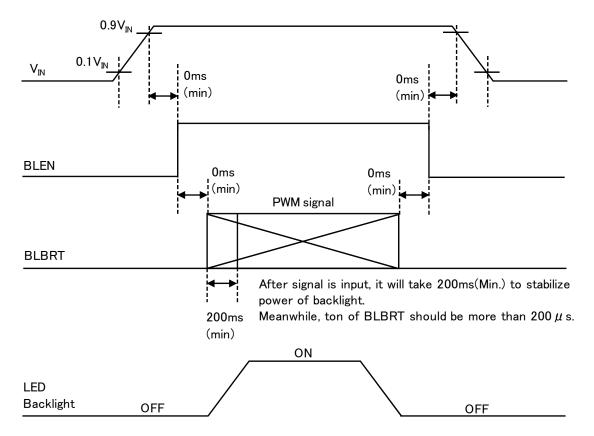
# $5\mbox{-}2.$ Constant current circuit for LED Backlight

Temp. =  $-30 \sim 80$ °C

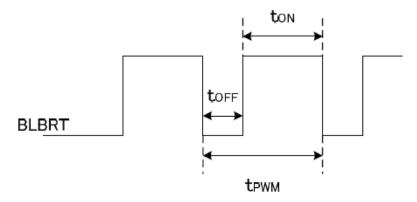
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	$V_{\rm IN}$	-	10.8	12.0	13.2	V
Current consumption	$I_{\rm IN}$	2)	-	(570)	(740)	mA
Permissive input ripple voltage	$V_{\mathrm{RP\_BL}}$	V <sub>IN</sub> =12.0V	-	-	100	mVp-p
DI DDM I t	V <sub>IL_BLBRT</sub>	"Low" level	0	-	0.8	V
BLBRT Input signal voltage	V <sub>IH_BLBRT</sub>	"High" level	2.3	-	$V_{\rm IN}$	V
BLBRT Input pull-down resistance	Rin_blbrt	-	(50)	(150)	(250)	kΩ
DI EN I ' l lt	V <sub>IL_BLEN</sub>	"Low" level	0	-	0.8	V
BLEN Input signal voltage	V <sub>IH_BLEN</sub>	"High" level	2.3	-	$V_{\rm IN}$	V
BLEN Input pull-down resistance	RIN_BLEN	-	(50)	(150)	(250)	kΩ
PWM Frequency 3)	fрwм	-	200	-	10k	Hz
		f <sub>PWM</sub> =200Hz	1	-	100	%
P WM Duty ratio 3)	$\mathrm{D}_{\mathrm{PWM}}$	f <sub>PWM</sub> =2kHz	10	-	100	%
		f <sub>PWM</sub> =10kHz	50	-	100	%
Operating life time 4), 5)	Т	Temp.=25°C	-	(70,000)	-	h



1) V<sub>IN</sub>-turn-on conditions



- 2)  $V_{IN} = 12V$ , Temp. = 25°C,  $D_{PWM} = 100\%$
- 3) PWM Timing Diagram



ton, toff  $\geq 50 \,\mu$  s.

In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

- 4) When brightness decrease 50% of minimum brightness.

  The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 5) Life time is estimated data. (Condition: IF=100mA, Ta=25°C in chamber).



Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN	7
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# 6. Optical characteristics

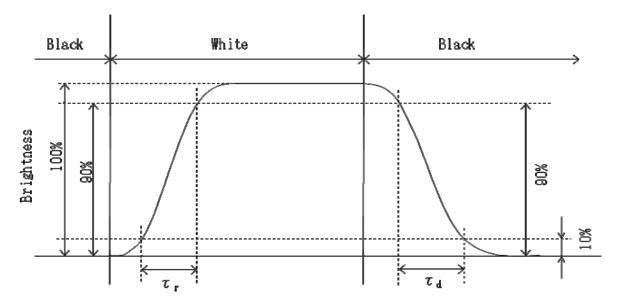
Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

			,			,	
Item		Symbol	Condition	Min.	Typ.	Max.	Unit
D 4:	Rise	Τr	$\theta = \phi = 0$ °	-	18	-	ms
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	12	-	ms
		$\theta$ upper		-	85	-	1
77 1		$\theta$ lower	CR≧10	-	85	-	deg.
Viewing angle	range	ф сегт	CR≦10	-	85	-	1
				-	85	-	deg.
Contrast ratio	Contrast ratio		$\theta = \phi = 0^{\circ}$	500	750	-	-
Brightness		L	IF=70mA/Line	_	1200	-	cd/m²
	Green	У		0.550	0.600	0.650	
	Green	X	$\theta = \phi = 0^{\circ}$	0.300	0.350	0.400	
	Green	У	$\theta = \phi = 0^{\circ}$	0.285	0.335	0.385	
Chromaticity	Blue	X	$\theta - \phi - 0$	0.520	0.570	0.620	
coordinates	Blue	У	0 - 1 -09	0.100	0.150	0.200	-
	White	X	$\theta = \phi = 0^{\circ}$	0.070	0.120	0.170	
	White	X	$\theta = \phi = 0^{\circ}$	0.270	0.320	0.370	
	White	У	$\sigma - \phi - 0$	0.295	0.345	0.395	

#### 6-1. Definition of contrast ratio

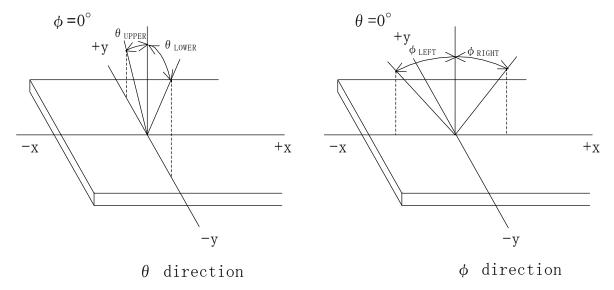
CR(Contrast ratio) = Brightness with all pixels "White"
Brightness with all pixels "Black"

## 6-2. Definition of response time

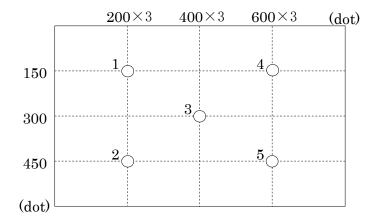




#### 6-3. Definition of viewing angle



#### 6-4. Brightness measuring points



- 1) Rating is defined as the white brightness at center of display screen(3).
- 2) 5 minutes after LED is turned on. (Ambient Temp.= $25^{\circ}$ C)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	9

# 7. Interface signals

#### 7-1. Interface signals

No.	Symbol	Description	Note
1	GND	GND	
2	SELLVDS	Mode select signal(LVDS Data mapping)	
3	GND	GND	
4	GND	GND	
5	RxIN3+	LVDS receiver signal CH3(+)	LVDS
6	RxIN3-	LVDS receiver signal CH3(-)	LVDS
7	GND	GND	
8	CK IN+	LVDS receiver signal CK(+)	LVDS
9	CK IN-	LVDS receiver signal CK(-)	LVDS
10	GND	GND	
11	RxIN2+	LVDS receiver signal CH2(+)	LVDS
12	RxIN2-	LVDS receiver signal CH2(-)	LVDS
13	GND	GND	
14	RxIN1+	LVDS receiver signal CH1(+)	LVDS
15	RxIN1-	LVDS receiver signal CH1(-)	LVDS
16	GND	GND	
17	RxIN0+	LVDS receiver signal CH0(+)	LVDS
18	RxIN0-	LVDS receiver signal CH0(-)	LVDS
19	GND	GND	
20	GND	GND	
21	$V_{\mathrm{DD}}$	+3.3V power supply	
22	$V_{\mathrm{DD}}$	+3.3V power supply	
23	GND	GND	
24	BLBRT	PWM signal(Brightness adjustment)	
25	BLEN	ON/OFF terminal voltage	
26	GND	GND	
27	$V_{\rm IN}$	+12V power supply	
28	$V_{\rm IN}$	+12V power supply	
29	$V_{\rm IN}$	+12V power supply	
30	GND	GND	

LCD connector : FI-X30SSLA-HF (JAE) Matching connector : FI-X30HL (JAE)

> : FI-X30HL-T (JAE) : FI-X30C2L-NPB (JAE) : FI-X30C2L-T-NPB (JAE)

LVDS receiver : Embedded in ASIC

 $Matching\ LVDS\ transmitter\quad :\quad THC63LVDM83R (THine\ Electronics)\ or\ compatible$ 



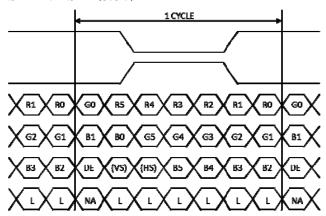
Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	10

# 7-2. Data mapping(6bit RGB input)

# 1) Location of SELLVDS (THC63LVDM83R(THine Electronics) or compatible)

	mitter	2Pin SE	CLLVDS
Pin No.	Data	= L(GND) or OPEN	= H(3.3V)
51	TA0	_	R0(LSB)
52	TA1	_	R1
54	TA2	_	R2
55	TA3	_	R3
56	TA4	_	R4
3	TA5	_	R5(MSB)
4	TA6	_	G0(LSB)
6	TB0	_	G1
7	TB1	_	G2
11	TB2	_	G3
12	TB3	_	G4
14	TB4	_	G5(MSB)
15	TB5	_	B0(LSB)
19	TB6	_	B1
20	TC0	_	B2
22	TC1	_	В3
23	TC2	_	B4
24	TC3	_	B5(MSB)
27	TC4	_	(HS)
28	TC5	_	(VS)
30	TC6	_	DE
50	TD0	_	GND
2	TD1	_	GND
8	TD2	_	GND
10	TD3	_	GND
16	TD4	_	GND
18	TD5	_	GND
25	TD6	_	GND

#### SELLVDS=H(3.3V)



DE: DATA ENABLE

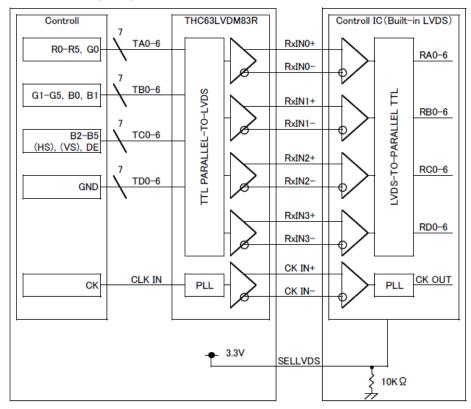
 $\begin{array}{l} HS: H_{SYNC} \\ VS: V_{SYNC} \end{array}$ 



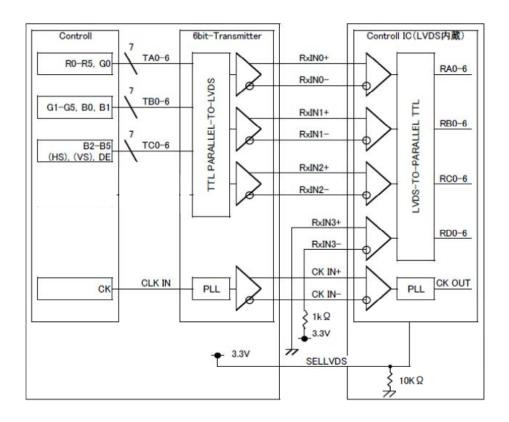
Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	11

#### 2) Block Diagram

#### SELLVDS=H(3.3V)



When using "6-bit Transmitter", please connect the unused channel of the control IC receiver as described in the diagram below.





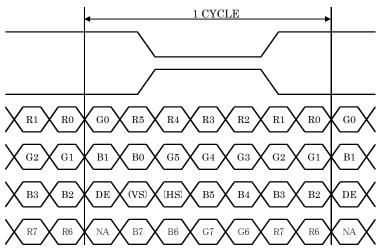
Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	12

# 7-3. Data mapping(8bit RGB input)

# 1) Location of SELLVDS (THC63LVDM83R(THine Electronics) or compatible)

Trans	mitter	2Pin S	ELLVDS
Pin No.	Data	= L(GND) or OPEN	= H(3.3V)
51	TA0	R0(LSB)	R2
52	TA1	R1	R3
54	TA2	R2	R4
55	TA3	R3	R5
56	TA4	R4	R6
3	TA5	R5	R7(MSB)
4	TA6	G0(LSB)	G2
6	TB0	G1	G3
7	TB1	G2	G4
11	TB2	G3	G5
12	TB3	G4	G6
14	TB4	G5	G7(MSB)
15	TB5	B0(LSB)	B2
19	TB6	B1	В3
20	TC0	B2	B4
22	TC1	В3	B5
23	TC2	B4	В6
24	TC3	B5	B7(MSB)
27	TC4	(HS)	(HS)
28	TC5	(VS)	(VS)
30	TC6	DE	DE
50	TD0	R6	R0(LSB)
2	TD1	R7(MSB)	R1
8	TD2	G6	G0(LSB)
10	TD3	G7(MSB)	G1
16	TD4	В6	B0(LSB)
18	TD5	B7(MSB)	B1
25	TD6	(NA)	(NA)

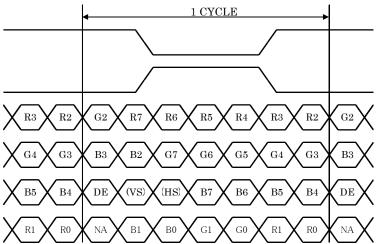
### SELLVDS=L(GND) or OPEN



DE : DATA ENABLE

 $HS: H_{SYNC}$   $VS: V_{SYNC}$ 



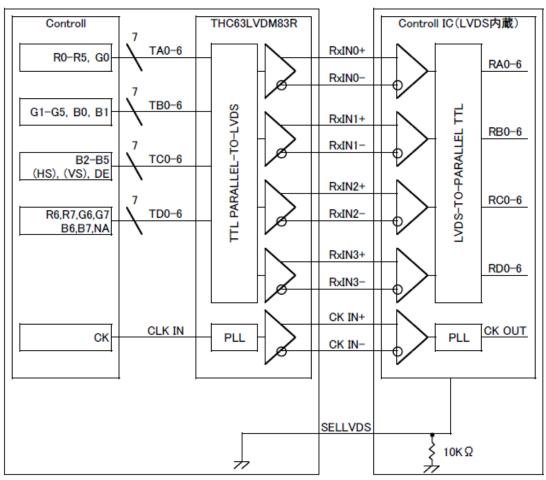


DE: DATA ENABLE

 $HS: H_{SYNC}$  $VS:V_{SYNC} \\$ 

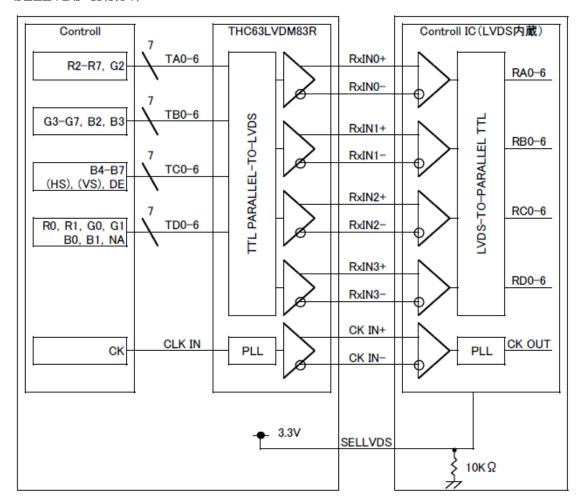
#### 2) Block Diagram

#### SELLVDS=L(GND) or OPEN





#### SELLVDS=H(3.3V)





Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	15

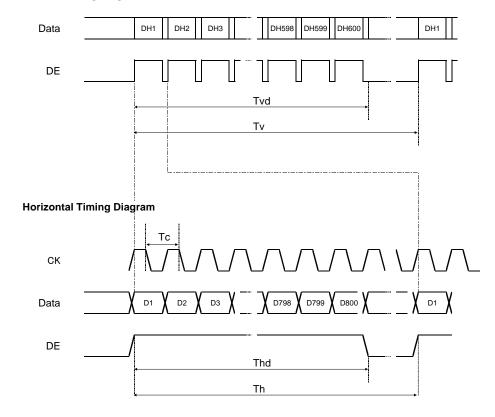
### 8. Input timing characteristics

#### 8-1. Timing characteristics

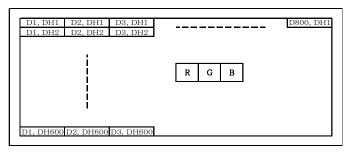
	Symbol	Min.	Typ.	Max.	Unit	Note	
Clock (CK)	Frequency	1/Tc	30	40	48	MHz	
II ID I		Th	860	1056	1395	Тс	
	Horizontal Period	111	24.0	26.4	-	$\mu$ s	1)
Enable signal (DE)	Horizontal display period	Thd		800		Тс	
Vertical Period  Vertical display period		Tv	610	628	1024	Th	
		Tvd		600		Th	
Refresh rate		fv	50	60	70	Hz	2)

- 1) Please set a clock frequency, a vertical dormant period, and the horizontal dormant period so that the Horizontal Period should not reach less than Min. value.
- 2) If the refresh rate reach less than Min. value, the deterioration of the display quality, flicker etc., may occur. (fv=1/Tv)

#### **Vertical Timing Diagram**



8-2. Input Data Signals and Display position on the screen

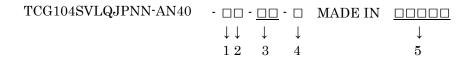




Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	16

#### 9. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.



No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2016	2017	2018	2019	2020	2021
Code	6	7	8	9	0	1
Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6
Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

#### 10. Warranty

#### 10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 10-2. Production warranty

Kyocera display warrants its LCD's for a period of 12 months from the ship date. Kyocera display shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera display's responsibility.



 
 Spec No.
 Part No.
 Page

 TQ3C-8EAF0-E1TAC07-00
 T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)
 17

#### 11. Precautions for use

#### 11-1. Installation of the LCD

- 1) Please ground either of the mounting (screw) holes of LCD, in order to stabilize brightness and display quality.
- 2) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 3) The LCD shall be installed so that there is no pressure on the LSI chips.
- 4) Since this product is wide viewing product, occurrence level of in-plane unevenness by the external stress is different compared to current normal viewing product. So there is a possibility that in-plane unevenness will be occurred by over twist, strain giving by attaching to LCD, and over pressure to touch panel. Please be careful of stress when designing the housing.
- 5) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

#### 11-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 11-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Please select the best display pattern based on your evaluation because flicker, lines or nonuniformity or unevenness can be visible depending on display patterns.

#### 11-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 11-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera display LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1TAC07-00	T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)	18

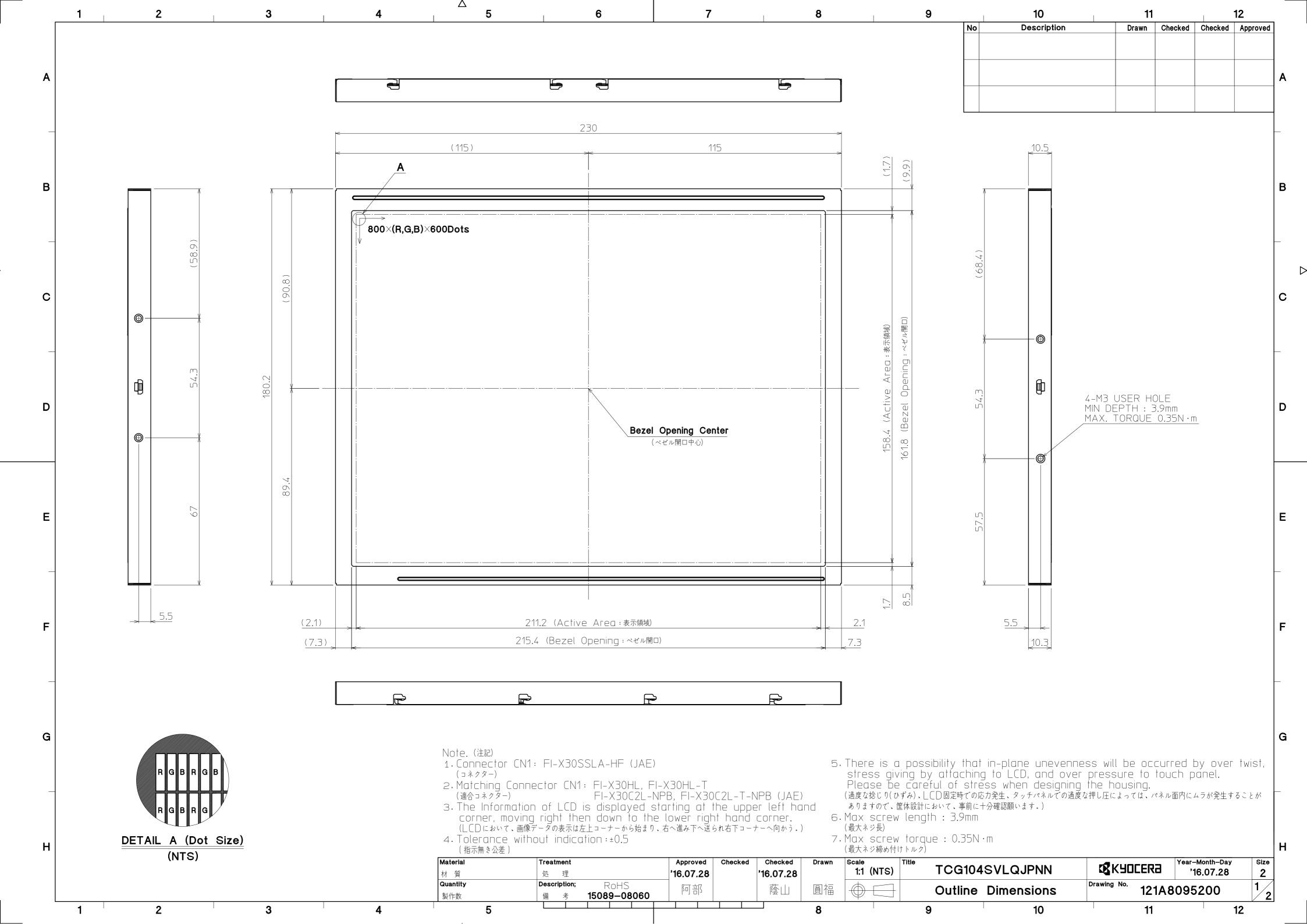
# 12. Reliability test data

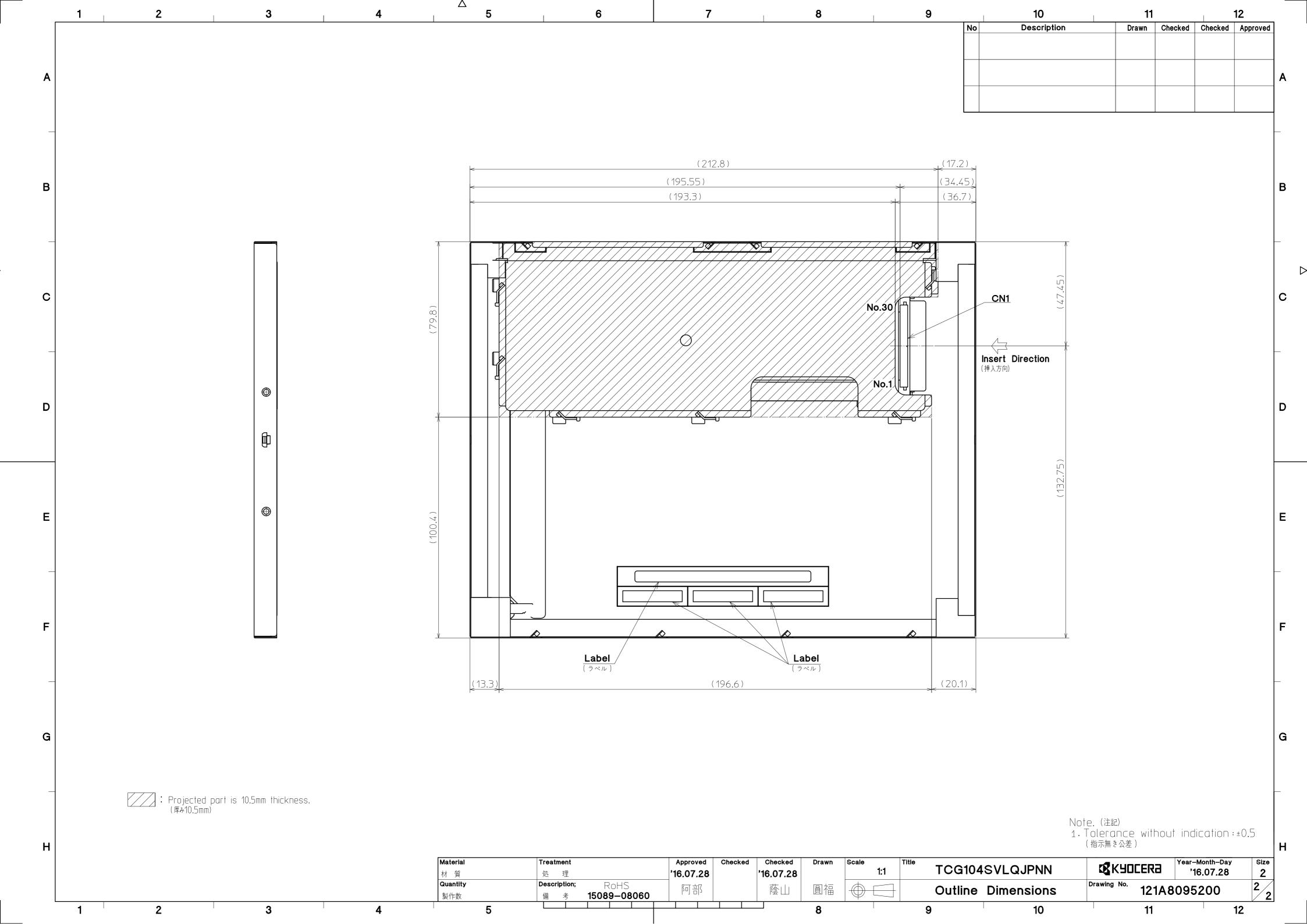
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	(240h)	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	(240h)	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	(240h)	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	(10cycles)	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	80°C	(500h)	Display function Display quality Current consumption	: No defect : No defect : No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

  The reliability test is conducted only to examine the LCD's capability.







Spec No.	TQ3C-8EAF0-E2TAC07-00
Date	July 28, 2016

# KYOCERA INSPECTION STANDARD

# TYPE: T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40)

#### KYOCERA DISPLAY CORPORATION

Original	Designed by:	Engineering de	ept.	Confirmed by	: QA dept.
Issue Date	Prepared	Checked	Approved	Checked	Approved
July 28, 2016	M. Koyama	Y. Yomazaki	G Matsumoto	O. Sato	1. Hamars



Spec No.
TQ3C-8EAF0-E2TAC07-00

Part No. T-55787GD104J-LW-AHN (TCG104SVLQJPNN-AN40) Page

#### Revision record

	Dete	Designed by : Engineering dept. Confirmed by : QA d			: QA dept.		
	Date	Prepa		Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Description	ons	



Spec No.	Part No.	Page
TQ3C-8EAF0-E2TAC07-00	T-55787GD104J-LW-AHN	1
1 400 02211 0 22111001 00	(TCG104SVLQJPNN-AN40)	1

# Visuals specification 1) Note

1) Note	1							
			Note					
General	reviewe 2. This ins	d by Kyocera, and an addit	t defined within this inspection standard shall be tional standard shall be determined by mutual consent. e image quality shall be applied to any defect within the					
		active area and shall not be applicable to outside of the area.						
		on conditions	: 500 Lux min.					
	Lumina	ince ion distance	300 Lux min.					
	Temper		: 25 ± 5℃					
	Direction		Directly above					
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the					
inspection			LCD, even when all "Black" data sent to the screen.					
item			Inspection tool: 5% Transparency neutral density filter.					
			Count dot: If the dot is visible through the filter.					
			Don't count dot: If the dot is not visible through the					
			filter.  RGBRGBRGB  There is an electrode in the middle of the dot and one dot is shown in the left drawing.  RGBRGBRGB  RGBRGBRGB  Adot drawing>					
		Black dot defect	The dot is constantly "off" when power applied to the					
			LCD, even when all "White" data sent to the screen.					
			Similar size compared to bright dot.					
		White dot	Pixel works electrically, however, circular/foreign particle makes dot appear to be "on" even when all					
		(Circular/foreign						
		particle)	"Black" data is sent to the screen.					
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.					
			R G B R G B R G B R G B R G B R G B R G B R G B R G B					
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non					
	inspection	Foreign particle	operating.					
		(Polarizer, Cell, Backlight)						
		Appearance inspection	Does not satisfy the value at the spec.					
	Others	CFL wires	Damaged to the CFL wires, connector, pin, functional failure or appearance failure.					
	Definition	Definition of cir	rcle size Definition of linear size					
	of size	d = (a + b)	2)/2					



Spec No.	Part No.	Page
TQ3C-8EAF0-E2TAC07-00	T-55787GD104J-LW-AHN	9
1400 0221100, 00	(TCG104SVLQJPNN-AN40)	2

# 2) Standard

Defect Type			Limit	
Visual defect	Scratch	$0.01 \text{ mm} < W \leq 0.05 \text{ mm}$	N ≤ 4	
		$L \leq 10 \text{ mm}$		
		0.01 mm < W	N = 0	
		10mm < L		
		0.05 mm < W	N = 0	
	Dent	$0.2 \text{ mm} < \Phi \leq 0.4 \text{ mm}$	N ≦ 4	
		0.4 mm < Φ	N = 0	
	Black spot bubble	$0.2 \text{ mm} < \Phi \leq 0.4 \text{ mm}$	$N \leq 5$	
		0.4 mm < Φ	N = 0	
	Lint	$L \leq 3 \text{ mm}$	N ≦ 4	
		$W \leq 0.1 \text{ mm}$		
		$3~\mathrm{mm}~<~\mathrm{L}$	N = 0	
		$W \leq 0.1 \text{ mm}$		
		0.1 mm < W	According to Black spot	
Electrical defect	Bright dot	$N \leq 5$		
	Dark dot	N ≤ 5		
	Total dot	N ≦ 8		
	Two adjacent dot			
	Bright dot	$\leq 2 \text{ pairs}$		
	Dark dot	$\leq 2 \text{ pairs}$		
	Three or more	Not allowed		
	adjacent dot			
	Line defect	Not allowed		

