LCD Module Technical Specification

First Edition Sep. 1, 2005

Final Revision Mar. 23, 2017

Type No. C-51505NFQJ-LB-AJN

Customer : **STANDARD**

Customer's Product No: -----

KYOCERA Display Corporation

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Revision History Rev. Date Page Comment 1 May 9, 2008 18~20 Revise of Precautions Relating Product Handling and Warranty. 2 Apr. 13. 2012 Changing company's name 3 Mar. 24.2017 7 Luminance of Backlight Surface Min. 38 ⇒ Min. 30 C-51505NFQJ-LB-AJN Rev.3 **KYOCERA** Display Corporation Page 2/20

1. General Specifications

Operating Temp. : min. -20°C ~ max. 70°C

Storage Temp. : min. -20°C ~ max. 70°C

Display Format : 20 characters \times 2 lines

Display Fonts : 5×8 dots (1 character)

Viewing Area : 83.0 (W) \times 18.6 (H) mm

Outline Dimensions : 116.0 (W) imes 37.0 (H) imes (15.6) (D) mm

Weight : 42.9g max.

LCD Type : NTD-21333

(STN / Color-mode / Transmissive)

Viewing Angle : 6:00

Data Transfer : 8-bit parallel data transfer

Backlight : LED Backlight / Blue

Additional Spec : Vivid Color Display Specification

(High Performance Color Film is Used)

Drawings : Dimensional Outline C-51505 AH base

RoHS regulation : To our best knowledge, this product satisfies material

requirement of RoHS regulation.

Our company is doing the best efforts to obtain the equivalent certificate from our suppliers.

2. Electrical Specifications

2.1. Absolute Maximum Ratings

Vss=0V

Parameter	Symbol	Conditions	Min.	Max.	Units
Supply Voltage	Vcc-Vss	-	-0.3	7.0	V
(Logic)					
Supply Voltage	Vcc-Vee	-	-0.3	13.5	V
(LCD Drive)					
Input Voltage	Vı	-	-0.3	Vcc+0.3	V

2.2.DC Characteristics

Ta=25°C, Vcc=5V, Vss=Vee=0V

		1	1	1	I	I
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Supply Voltage	Vcc-Vss	-	4.5	-	5.5	V
(Logic)						
Supply Voltage	Vcc-Vee		Shown in 3	.1		V
(LCD Drive)						
High Level	ViH	-	2.2	-	Vcc	V
Input Voltage						
Low Level	VIL	-	-0.3	-	0.8	V
Input Voltage						
High Level	Vон	Iон=-0.25mA	2.4	-	-	V
Output Voltage						
Low Level	Vol	IoL=1.2mA	-	-	0.4	٧
Output Voltage						
Supply Current	lcc	Vcc-Vss=5.3V	-	2.0	3.0	mA

All voltage values are referenced to Vss =0V

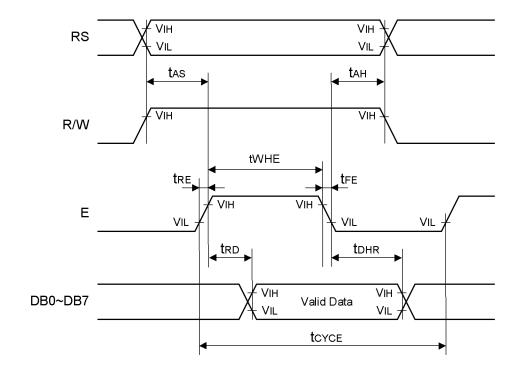
 V_1 to V_5 must maintain $V_{DD} \ge V_1 \ge V_2 \ge V_3 \ge V_4 \ge V_5$.

2.3.AC Characteristic

Read Operation

Vcc=5.0V±10%

Parameter	Symbol	Min.	Max.	Units
Enable Cycle Time	tcyce	500	-	ns
Enable "H" Level Pulse Width	twhe	300	-	ns
Enable Rise/Fall Time	t _{RE} , t _{FE}	-	25	ns
RS,R/W Setup Time (8-bit operation mode) (4-bit operation mode)	t _{AS}	60 100	-	ns
RS,R/W Address Hold Time	t _{AH}	10	-	ns
Read Data Output Delay	t _{RD}	-	190	ns
Read Data Hold Time	t _{DHR}	20	-	ns

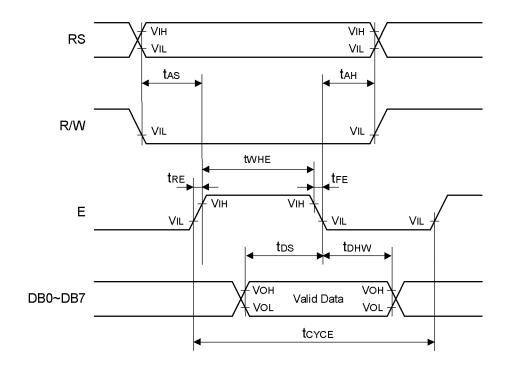


Read Operation Timing

Write Operation

Vcc=5.0V±10%

Parameter	Symbol	Min.	Max.	Units
Enable Cycle Time	tcyce	500	-	ns
Enable "H" Level Pulse Width	twhe	300	-	ns
Enable Rise/Fall Time	t _{RE,} t _{FE}	-	25	ns
RS,R/W Setup Time (8-bit operation mode) (4-bit operation mode)	t _{AS}	60 100	-	ns
RS,R/W Address Hold Time	t _{AH}	10	-	ns
Data Output Delay	t _{DS}	-	-	ns
Data Hold Time	t _{DHR}	10	-	ns



Write Operation Timing

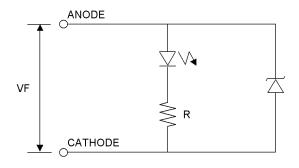
2.4.Lighting Specifications

2.4.1.Absolute Maximum Ratings

Ta=25°C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Forward Voltage	VF	Note 1	-	-	6.0	<
LED Power Dissipation	P□	-	-	-	0.125	mW
Reverse Voltage	VR	-	-	-	5.0	V

Note 1: VF is defined as the voltage between ANODE and CATHODE as shown below.



2.4.2. Operating Characteristics

Ta=25°C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Foward Current	lF	V⊧=5.0V	-	15	18	mA
Luminance of	L	V _F =5.0V	30	-	-	cd/m ²
Backlight Surface						

3. Optical Specifications

3.1.LCD Driving Voltage

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Recommended		Ta= -20°C	-	ı	5.5	V
LCD Driving Voltage	Vcc-Vee	Ta=25°C	4.9	5.3	5.5	V
Note 1		Ta=70°C	4.7	-	ı	V

Note 1 : Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

3.2. Optical Characteristics

Ta=25°C, 1/16 Duty, 1/5 Bias, Vop=5.5V (Note 4), θ = 0°C, ϕ =-°

Parameter		Symbol	Conditions	Min.	Тур.	Max.	Units
Contrast Ratio Note 1		CR	θ= 0°C , φ=-°	-	80	-	
Viewing An	gle			Shown i	n 3.3		
Response	Rise Note 2	Ton	-	-	45	90	ms
Time	Decay Note 3	Toff	-	-	200	300	ms

Note 1 :Contrast ratio is definded as follows. (CR = Lon / Loff)

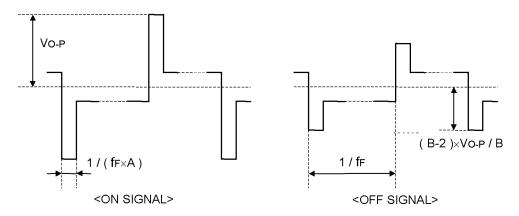
Lon: Luminance of the ON segments

Loff: Luminance of the OFF segments

Measuring Spot:0.9mm

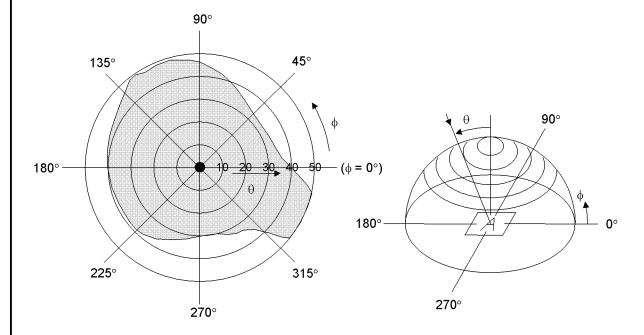
- Note 2 :The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.
- Note 3 :The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.
- Note 4 : Definition of Driving Voltage Vod

Assuming that the typical driving waveforms shown below are applied to the LCD Panel at 1/A Duty - 1/B Bias (A: Duty Number, B: Bias Number). Driving voltage VoD is definded as the voltage VoD when the contrast ratio (CR=LoN / LOFF) is at its maximum.

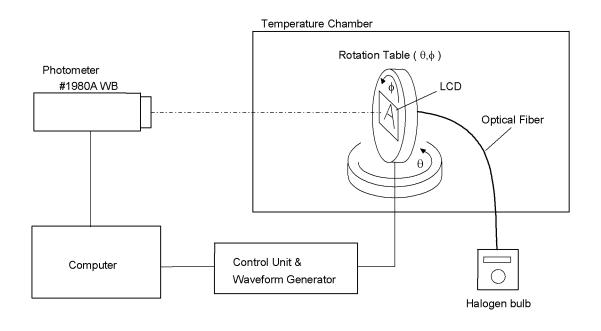


3.3. Definition of Viewing Angle and Optimum Viewing Area

- *Point shows the point where contrast ratio is measured. : θ = 0°, ϕ = -°
- *Driving condition: 1/16 Duty, 1/5 Bias, VoD=5.5V, fF=84.3Hz



3.4.System Block Diagram



4.<u>I/O Terminal</u>

4.1.Pin Assignment

<u>CN1</u>

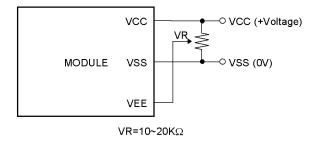
No.	Symbol	Function
1	LED (A)	LED Anode Terminal
2	LED (K)	LED Cathode Terminal
3	Vss	Power Supply (0V, GND)
4	Vcc	Power Supply for Logic
5	VEE	Power Supply for LCD Drive
6	RS	Register Select Signal
7	R/W	Read/Write Select Signal H : Read L : Write
8	E	Enable Signal (No pull-up Resister)
9	DB0	Data Bus Line / Non-connection at 4-bit operation
10	DB1	Data Bus Line / Non-connection at 4-bit operation
11	DB2	Data Bus Line / Non-connection at 4-bit operation
12	DB3	Data Bus Line / Non-connection at 4-bit operation
13	DB4	Data Bus Line
14	DB5	Data Bus Line
15	DB6	Data Bus Line
16	DB7	Data Bus Line

<u>CN2</u>

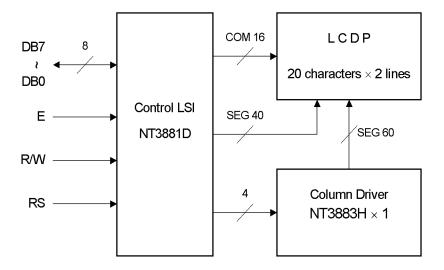
No.	Symbol	Function
1	LED (A)	LED Anode Terminal
2	LED (K)	LED Cathode Terminal

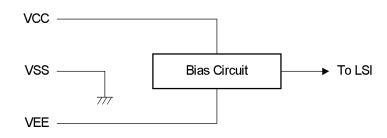
4.2. Example of Power Supply

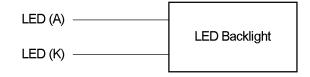
It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.



4.3.Block Diagram







5.Test

No abnormal function and appearance are found after the following tests.

Conditions: Unless otherwise specified, tests will be conducted under the following condition.

Temperature: 20±5°C Humidity: 65±5%RH

tests will be not conducted under functioning state.

No.	Parameter	Conditions	Notes
1	High Temperature Operating	70°C±2°C, 96hrs (operation state)	
2	Low Temperature Operating	-20°C±2°C, 96hrs (operation state)	1
3	High Temperature Storage	70°C±2°C, 96hrs	2
4	Low Temperature Storage	-20°C±2°C, 96hrs	1,2
5	Damp Proof Test	40°C±2°C,90~95%RH, 96hrs	1,2
6	Vibration Test	Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X, Y, Z each 15 minutes	3
7	Shock Test	To be measured after dropping from 60cm high the concrete surface in packing state. Dropping method corner dropping A corner : once Edge dropping B,C,D edge : once Face dropping E,F,G face : once	

Note 1: No dew condensation to be observed.

Note 2 :The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after removed from the test chamber.

Note 3: Vibration test will be conducted to the product itself without putting it in a container.

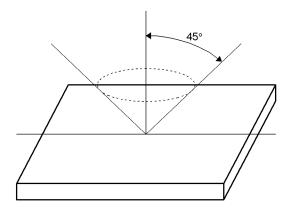
6. Appearance Standards

6.1.Inspection conditions

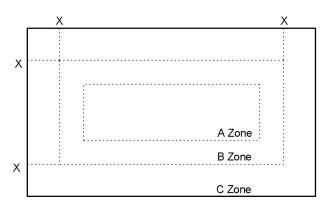
The LCD shall be inspected under 40W white fluorescent light.

The distance between the eyes and the sample shall be more than 30cm.

All directions for inspecting the sample should be within 45° against perpendicular line.



6.2. Definition of applicable Zones



X: Maximum Seal Line

A Zone : Active display area

B Zone : Out of active display area ~ Maximum seal line

C Zone: Rest parts

A Zone + B Zone = Validity viewing area

6.3.Standards(E rank, LED)

D = (Long + Short) / 2 *: Disregard Units: mm

$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	units : mm) / 2 * : Disregard Unit										
Dimension Acceptable Num D ≤ 0.10 * 0.10 < D ≤ 0.15 1 pc / dot(only segme 2 pcs / cell or less (2) Pattern Shift A B A - B ≤ 0.15 (3) Breakage or Chips / Deformation 1. Segment Type Dimension Acceptable Num A ≤ 0.10 * B ≤ 0.10 * 2. Dot Type Dimension Acceptable Number A≤0.10 * (Should not be connected to be connected		ia										
$D \le 0.10 \qquad *$ $0.10 < D \le 0.15 \qquad 1 \text{ pc / dot(only segmen)} 2 \text{ pcs / cell or less}$ (2) Pattern Shift $A B A-B \le 0.15$ $A B A-B \le 0.15$ (3) Breakage or Chips / Deformation $A \le 0.10 *$ $A \le 0.10 *$ $B \le 0.10 *$ $A \le 0.10 *$ $B \le 0.10 *$ $A \le 0.10 $					(1) Pin Hole	1 The Shape of Dot (1) Pin H						
(2) Pattern Shift A B A - B ≤ 0.15 (3) Breakage or Chips / Deformation A 1.Segment Type Dimension Acceptable Num A ≤ 0.10 * B ≤ 0.10 * 2.Dot Type Dimension Acceptable Number Acceptabl	Number	Acceptable Numbe	Dimension		//							
(2) Pattern Shift A B A - B ≤ 0.15 A B A - B ≤ 0.15 A B A - B ≤ 0.15 A B A - B ≤ 0.15 Dimension Acceptable Number A ≤ 0.10 * B ≤ 0.10 * Chould not be connected to be connec		*	D ≤ 0.10									
(2) Pattern Shift A B A - B ≤ 0.15 (3) Breakage or Chips / Deformation 1. Segment Type Dimension Acceptable Number A ≤ 0.10 * B ≤ 0.10 * (Should not be connected to a con	gment)or less	1 pc / dot(only segment)	0.40 . D < 0.45									
(3) Breakage or Chips / Deformation 1. Segment Type Dimension Acceptable Num A ≤ 0.10 * B ≤ 0.10 * 2. Dot Type Dimension Acceptable Number Acceptable	;	2 pcs / cell or less	J. 13	0.10 < D ≤ 0	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\							
(3) Breakage or Chips / Deformation 1. Segment Type Dimension Acceptable Num A ≤ 0.10 * B ≤ 0.10 * 2. Dot Type Dimension Acceptable Number A≤0.10 * (Should not be connected to 1 pc / dot(only segment) or 0.10 < A≤0.15 2 pcs / cell or less (Should not be connected to B ≤ 0.10 * 3. Defective type extends over multiple number D≤0.10 * 1 pc / dot(only segment) or 2 pcs / cell or less 2 pcs / cell												
(3) Breakage or Chips / Deformation 1. Segment Type Dimension Acceptable Num A ≤ 0.10 * B ≤ 0.10 * 2. Dot Type Dimension Acceptable Number A≤0.10 * (Should not be connected to 1 pc / dot(only segment) or 0.10 < A≤0.15 2 pcs / cell or less (Should not be connected to B ≤ 0.10 * 3. Defective type extends over multiple number D≤0.10 * 1 pc / dot(only segment) or 2 pcs / cell or less 2 pcs / cell				hift	(2) Pattern Sh							
(3) Breakage or Chips / Deformation 1. Segment Type Dimension Acceptable Num A ≤ 0.10 * B ≤ 0.10 * 2. Dot Type Dimension Acceptable Number Asolution Asolut			≤ 0.15	АВ								
1. Segment Type Dimension A ≤ 0.10 B ≤ 0.10 2. Dot Type Dimension Acceptable Number A ≤ 0.10 (Should not be connected to 1 pc / dot(only segment) or 1 pc / cell or less (Should not be connected to B ≤ 0.10 3. Defective type extends over multiple number A ≤ 0.10 Dimension Acceptable Number A ≤ 0.10 A ≤ 0.10 A ≤ 0.10 The proof of the proof												
Dimension Acceptable Number $A \le 0.10$ * $B \le 0.10$ * 2.Dot Type Dimension Acceptable Number $A \le 0.10$ * (Should not be connected to 1 pc / dot(only segment) or 10 pc / dot(only segmen		n	(3) Breakage or Chips / Deformation									
$A \le 0.10$ $B \le 0.10$ $A \le 0.10$			уре	A								
$A \le 0.10 \qquad * \\ B \le 0.10 \qquad * \\ A \le 0.10 \qquad * \\ A \le 0.10 \qquad * \\ A \le 0.10 \qquad A \\ A \le $	Number	Acceptable Numbe	on									
$B \leq 0.10 \qquad *$ $2. \text{Dot Type}$ $Dimension \qquad Acceptable Number \\ A \leq 0.10 \qquad *$ $(Should not be connected to the second of the s$		*	0	A ≤ 0.10								
2.Dot Type Dimension $A \le 0.10$ * (Should not be connected to 1 pc / dot(only segment)or 1 2 pcs / cell or less (Should not be connected to B ≤ 0.10 3.Defective type extends over multiple number $A \le 0.10$ Dimension $A \le 0.10$ * 3.Defective type extends over multiple number $A \le 0.10$ $A \le 0.$		*	0	B ≤ 0.10								
Dimension Acceptable Number $A \le 0.10$ * (Should not be connected to 1 pc / dot(only segment) or 1 2 pcs / cell or less (Should not be connected to $B \le 0.10$ * 3.Defective type extends over multiple number $A \le 0.10$ $A $				2 Dot Type	B							
A ≤ 0.10 * (Should not be connected to 1 pc / dot(only segment) or 1 on 1 pc / dot(only segment) or 1 pc / dot	nher	Acceptable Number										
(Should not be connected to 1 pc / dot(only segment) or 1 pc / dot(only segment) or 1 pc / cell or less (Should not be connected to $B \le 0.10$ * 3.Defective type extends over multiple number $Dimension$ Acceptable Number $D \le 0.10$ * 1 pc / dot(only segment) or 1 pc / dot(only segment) or 1 pc / cell or less					A							
$0.10 < A \le 0.15$ 2 pcs / cell or less (Should not be connected to B ≤ 0.10 * 3.Defective type extends over multiple number Dimension Acceptable Number D ≥ 0.10 * 1 pc / dot(only segment) or 1	d to next dot)	ld not be connected to ne										
(Should not be connected to $B \le 0.10$ * 3. Defective type extends over multiple number $D = 0.10$ $D = 0.15$ $D = 0$	or less	dot(only segment)or less	1 pc /									
3. Defective type extends over multiple number $\begin{array}{c ccccccccccccccccccccccccccccccccccc$		/ cell or less	2 pcs	0.10 <a≤0.15< td=""><td>→_B ←</td><td></td><td></td></a≤0.15<>	→ _B ←							
3.Defective type extends over multiple number Dimension Acceptable Number D≤0.10 * 1 pc / dot(only segment)or 1 2 pcs / cell or less	d to next dot)	ld not be connected to next do										
Dimension Acceptable Number D≤0.10 * 1 pc / dot(only segment)or l 2 pcs / cell or less		*		B ≤ 0.10								
D≤0.10 * 1 pc / dot(only segment)or 2 pcs / cell or less	mbers of dots	s over multiple numbers	extend	Defective type	3.0							
1 pc / dot(only segment)or location 2 pcs / cell or less	nber	Acceptable Number		Dimension								
'→		*		D≤0.10	* \							
	or less	dot(only segment)or less	1 pc /									
			2 pcs		'→							
(Individual dot must secure	ıre 1/2 area		; -	0.10 <d≤0.15< td=""><td></td><td></td><td></td></d≤0.15<>								
or more)			1									
		,	5, 111									

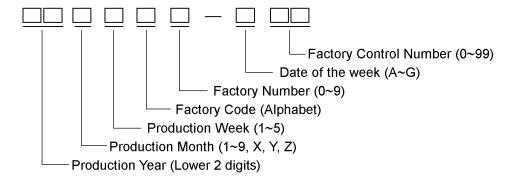
No.	Parameter	D = (Long + Short) / 2 * : Disregard Units : r Criteria					
2	Black and	(1) Round Sha	пре				
	White Spots,		Zone	Acceptable Number			
	Foreign Substances	Dimension		A B		С	
		D ≤ 0.10		*	*	*	
		0.10<	D ≤ 0.20	6	6	*	
		0.20<	D ≤ 0.30	2	2	*	
		Individual dot must secure 1/2 area or more.					
		(2) Line Shape)				
			Zone	Acceptable Number			
		Length	Nidth	Α	В	С	
		L ≤2.0	W≤0.03	*	*	*	
		L ≤2.0	0.03 <w≤0.05< td=""><td>4</td><td>4</td><td>*</td></w≤0.05<>	4	4	*	
		*	0.05 <w< td=""><td colspan="3">In the same way (1) *</td></w<>	In the same way (1) *			
3					efects")		
	Color Variation Air Bubbles	Not to be cons	picuous defects.				
	+	Not to be cons	picuous defects.		ptable Numl	ber	
	Air Bubbles	Not to be cons	-			ber C	
	Air Bubbles (between glass	Dimension	-	Acce	ptable Numl		
	Air Bubbles (between glass	Dimension	Zone	Acce	ptable Numl	С	
	Air Bubbles (between glass	Dimension 0.15<	Zone D ≤ 0.15	Acce A *	ptable Numl B *	C *	
	Air Bubbles (between glass	Dimension 0.15< 0.30<	Zone D ≤ 0.15 D ≤ 0.30	Acce A *	ptable Numl B *	* *	
	Air Bubbles (between glass	Dimension 0.15< 0.30< 0.50<	Zone $D \le 0.15$ $D \le 0.30$ $D \le 0.50$	Acce A * 2	ptable Numl B * 3	* * *	
	Air Bubbles (between glass	0.15< 0.30< 0.50< No more tha	Zone $D \le 0.15$ $D \le 0.30$ $D \le 0.50$ $D \le 1.00$	Acce A * 2 1 0	eptable Numl B * 3 2	* * *	
	Air Bubbles (between glass	Dimension 0.15< 0.30< 0.50< No more that (Refer to "Co	Zone $D \le 0.15$ $D \le 0.30$ $D \le 0.50$ $D \le 1.00$ n 3pcs as total.	Acce A * 2 1 0 Substance De	eptable Numl B * 3 2	* * *	
4	Air Bubbles (between glass & polarizer)	Dimension 0.15< 0.30< 0.50< No more that (Refer to "Co	Zone D ≤ 0.15 D ≤ 0.30 D ≤ 0.50 D ≤ 1.00 n 3pcs as total. complex Foreign S	Acce A * 2 1 0 Substance De	eptable Numl B * 3 2 1 efects")	* * *	
5	Air Bubbles (between glass & polarizer) Polarizer Scratches	Dimension 0.15< 0.30< 0.50< No more that (Refer to "Co" Not to be considered in the stains are not defective.	Zone $D \le 0.15$ $D \le 0.30$ $D \le 0.50$ $D \le 1.00$ $n 3pcs as total.$ $omplex Foreign Sepicuous defects.$	Acce A * 2 1 0 Substance De	ptable Numl B * 3 2 1 efects")	C * * * * *	
5 6	Air Bubbles (between glass & polarizer) Polarizer Scratches Polarizer Dirts	Dimension 0.15< 0.30< 0.50< No more that (Refer to "Co") Not to be constituted in the stains are not defective. Black spots, line	Zone $D \le 0.15$ $D \le 0.30$ $D \le 0.50$ $D \le 1.00$ $n 3pcs as total.$ $omplex Foreign Sepicuous defects.$ $e removed easily$	Acce A * 2 1 0 Substance De	eptable Numl B * 3 2 1 efects") surface, the	C * * * * *	
5 6	Air Bubbles (between glass & polarizer) Polarizer Scratches Polarizer Dirts Complex Foreign	Dimension 0.15< 0.30< 0.50< No more that (Refer to "Co") Not to be constituted in the stains are not defective. Black spots, line	Zone D ≤ 0.15 D ≤ 0.30 D ≤ 0.50 D ≤ 1.00 n 3pcs as total. complex Foreign Service removed easily the shaped foreign ter should be 7pc.	Acce A * 2 1 0 Substance De	eptable Numl B * 3 2 1 efects") surface, the	C * * * * *	
5 6	Air Bubbles (between glass & polarizer) Polarizer Scratches Polarizer Dirts Complex Foreign Substance Defects	Dimension 0.15< 0.30< 0.50< No more that (Refer to "Co") Not to be constituted in the stains are not defective. Black spots, linglass & polarize	Zone D ≤ 0.15 D ≤ 0.30 D ≤ 0.50 D ≤ 1.00 n 3pcs as total. complex Foreign Service removed easily the shaped foreign ter should be 7pc.	Acce A * 2 1 0 Substance De	eptable Numl B * 3 2 1 efects") surface, the	C * * * * module	

D =	(Long + Short)/2	* : Disregard	Units: mm

No.	Parameter	Criteria							
9	Chipped Glass	(1) Other th	(1) Other than electrode pad areas and corner areas						
	"A" si	"A" size:0.82or more			"A" size:Less than 0.82				
	X	Υ	Z	Х	Υ	Z			
	*	≤0.4	*	*	≤0.3	3 *			
		≤2.5	≤0.7	=t	≤2.0	≤0.5	5 =t		
		≤3.5	≤1.0	≤1/2t	≤3.0	≤0.5	5 ≤1/2t		
		≤5.5	≤1.5	≤1/3t	≤4.5	≤1.2	2 ≤1/3t		
		X	X Y Y			"A" size " 0.82or more Les			
						0	X+Y ≤3.0		
		2.Other	than electi	rode pad Are	eas				
					"A" siz	ze	"A" size		
					0.82or	more	Less than 0.82		
				//	X+,	Y	X+Y		
			\forall		≤3.	0	≤2.0		
		The total number of chipped glass defects shall be up to 5							
							e of the defect not acceptabl		

7. Code System of Production Lot

The production lot of module is specified as follows.



8. Type Number

The type number of module is specified as follows.

C-51505NFQJ-LB-AJN

9. Applying Precautions

Please contact us when questions and/or new problems not specified in this specificationsarise.

10. Precautions Relating Product Handling

The Following precautions will guide you in handling our product correctly.

- 1) Liquid crystal display devices
- 1. The liquid crystal display panel used in the liquid crystal display module is made of plate glass. Avoid any strong mechanical shock. Should the glass break handle it with care. The polarizer adhering to the surface of the LCD is made of a soft material. Guard against scratching it.
- 2) Care of the liquid crystal display module against static electricity discharge.
 - 1. When working with the module, be sure to ground your body and any electrical equipment you may be using. We strongly recommend the use of anti static mats (made of rubber), to protect worktables against the hazards of electrical shock.
 - 2. Avoid the use of work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
 - 3. Slowly and carefully remove the protective film from the LCD module, since this operation can generate static electricity.
- 3) When the LCD module must be stored for long periods of time:
 - 1. Protect the modules from high temperature and humidity.

Conditions: Temperature: 0°C~40°C

Humidity: Less than 60%RH

No dew condensation to be observed.

- 2. Keep the modules out of direct sunlight or direct exposure to ultraviolet rays.
- 3. Protect the modules from excessive external forces.
- 4) Use the module with a power supply that is equipped with an overcurrent protector circuit, since the module is not provided with this protective feature.
- 5) Do not ingest the LCD fluid itself should it leak out of a damaged LCD module. Should hands or clothing come in contact with LCD fluid, wash immediately with soap.
- 6) Conductivity is not guaranteed for models that use metal holders where solder connections between the metal holder and the PCB are not used. Please contact us to discuss appropriate ways to assure conductivity.
- 7) For models which use CFL:
 - 1. High voltage of 1000V or greater is applied to the CFL cable connector area. Care should be taken not to touch connection areas to avoid burns.
 - 2.Protect CFL cables from rubbing against the unit and thus causing the wire jacket to become worn.
 - 3. The use of CFLs for extended periods of time at low temperatures will significantly shorten their service life.
 - 4. After storing the product (or LCD) under low temperature and/or in dark atmosphere for a long period of time, CCFL may take longer time to reach its specified brightness.
- 8) For models which use touch panels:
 - 1.Do not stack up modules since they can be damaged by components on neighboring modules.
 - 2.Do not place heavy objects on top of the product. This could cause glass breakage.

- 9) For models which use COG, TAB, or COF:
 - 1. The mechanical strength of the product is low since the IC chip faces out unprotected from the rear. Be sure to protect the rear of the IC chip from external forces.
 - 2. Given the fact that the rear of the IC chip is left exposed, in order to protect the unit from electrical damage, avoid installation configurations in which the rear of the IC chip runs the risk of making any electrical contact.
- 10) Models which use flexible cable, heat seal, or TAB:
 - 1. In order to maintain reliability, do not touch or hold by the connector area.
 - 2. Avoid any bending, pulling, or other excessive force, which can result in broken connections.
- 11)In case of buffer material such as cushion / gasket is assembled into LCD module, it may have an adverse effect on connecting parts (LCD panel-TCP / HEAT SEAL / FPC / etc., PCB-TCP / HEAT SEAL / FPC etc., TCP-HEAT SEAL, TCP-FPC, HEAT SEAL-FPC, etc.,) depending on its materials. Please check and evaluate these materials carefully before use.
- 12)In case of acrylic plate is attached to front side of LCD panel, cloudiness (very small cracks) can occur on acrylic plate, being influenced by some components generated from polarizer film. Please check and evaluate those acrylic materials carefully before use.

11. Warranty

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery pecifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1. We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- 2. We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 3. We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 4. When the product is in CFL models, CFL service life and brightness will vary according to the performance of the inverter used, leaks, etc. We cannot accept responsibility for product performance, reliability, or defect, which may arise.
- 5. We cannot accept responsibility for intellectual property of a third party, which may arise through the application of our product to your assembly with exception to those issues relating directly to the structure or method of manufacturing of our product.
- 6. We will not be held responsible for any quality issue(s) after two years and beyond from its production date indicated on the lot number (please refer to "Code System of Production Lot" indicated earlier in this specification).