SPEC

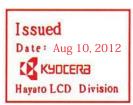
Spec No.	TQ3C-8EAF0-E1YAA89-00
Date	August 9, 2012

TYPE: TCG070WVLPEAFA-AA20

< 7.0 inch WVGA transmissive color TFT with LED backlight and constant current circuit for LED backlight and touch panel>

CONTENTS

- 1. Application
- 2. Construction and outline
- 3. Mechanical specifications
- 4. Absolute maximum ratings
- 5. Electrical characteristics
- 6. Optical characteristics
- 7. Interface signals
- 8. Input timing characteristics
- 9. Design guidance for analog touch panel
- 10. Lot number identification
- 11. Warranty
- 12. Precautions for use
- 13. Reliability test data
- 14. Outline drawing



KYOCERA CORPORATION LCD DIVISION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by: Engineering dept.			Confirmed by: QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
August 9, 2012	X. Mori	Y. Yamazaki	M.Fyitani	I Hamars	To , Suf

Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	-

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	-

Revision record

Date				Confirmed by : QA dept.			
	Date	Prepa	ared	Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Description	ons	



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	1

1. Application

This document defines the specification of TCG070WVLPEAFA-AA20. (RoHS Compliant)

2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Anti-Glare treatment

Interface : LVDS

Additional circuit : Timing controller, Power supply (3.3V input)

With Constant current circuit for LED Backlight(12V input)

Touch panel : Analog type, Non-Glare treatment

3. Mechanical specifications

3-1. LCD

Item	Specification	Unit
Outline dimensions 1)	165(W)×(104.4)(H)×10.4(D)	mm
Active area	152.4(W)×91.44(H) (17.8cm/7.0 inch(Diagonal))	mm
Dot format	800×(R,G,B)(W)×480(H)	dot
Dot pitch	0.0635(W)×0.1905(H)	mm
Base color 2)	Normally White	-
Mass	(260)	g

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

3-2. Touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	-
Actuation Force	0.05 ~ 0.8	N
Transmittance	Typ. 80	%
Surface hardness	Pencil hardness 2H or more according	-
Anti newton's ring treatment	None	-



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	2

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage(+3.3V)	V_{DD}	-0.3	4.0	V
Supply voltage(+12V)	$V_{\rm IN}$	-0.3	14.0	V
	RxINi+, RxINi- 1)	V_{I1}	-0.3	2.8	V
Input signal	CK IN+, CK IN-	V_{I2}	-0.3	2.8	V
voltage 2)	SELLVDS, BITSEL, SC	V_{I3}	-0.3	$ m V_{DD}$ +0.5	V
	BLBRT, BLEN	V_{I4}	-0.3	$V_{\rm IN}$	V
Supply voltage for touch panel		V_{TP}	0	6.0	V
Input current or	f touch panel	${ m I}_{ m TP}$	0	0.5	mA

- 1) i=0,1,2,3
- 2) V_{DD} must be supplied correctly within the range described in 5-1.

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	T_{OP}	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	2)	T_{STO}	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	3)	Нор	10	4)	%RH
Storage humidity	3)	${ m H}_{ m STO}$	10	4)	%RH
Vibration	•	-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. = -30°C < 48h , Temp. = 80°C < 168h Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp. 40°C, 85%RH Max.
 Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

6) Acceleration: 490 m/s², Pulse width: 11 ms 3 times in each direction: ±X, ±Y, ±Z EIAJ ED-2531



5)

Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	3

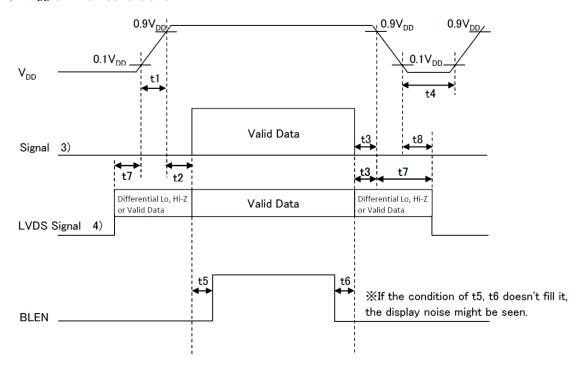
5. Electrical characteristics

5-1. LCD

Temp. = $-20 \sim 70$ °C

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Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)		$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption		${ m I}_{ m DD}$	2)	•	200	260	mA
Permissive input ripple voltag	ge	V_{RP}	V _{DD} =3.3V	-	-	100	mVp-p
T t ' 1 1t	9)	V_{IL}	"Low" level	0	1	0.8	V
Input signal voltage	3)	V_{IH}	"High" level	2.0	-	$V_{ m DD}$	V
I amount and be assumed.		I_{OL}	V ₁₃ =0V	-10	-	10	μA
Input reek current		I_{OH}	V _{I3} =3.3V	-	-	400	μA
LVDS Input voltage	4)	$V_{ m L}$	-	0	-	1.9	V
Differential input voltage	4)	V_{ID}	-	250	350	450	mV
Differential input	4) 5)	V_{TL}	"Low" level	V _{CM} -100	-	-	mV
threshold voltage	4) 5)	V_{TH}	"High" level	-	-	V _{CM} +100	mV
Terminator		R_1	-	-	100	-	
		t1	-	0.1	-	10	ms
		t2	-	0	-	-	ms
		t3	-	0	-	-	ms
77	1)	t4	-	1.0	-	-	s
V _{DD} -turn-on conditions	1)	t5	-	200	-	-	ms
		t6	-	200	-	-	ms
		t7	-	0	-	10	s
		t8	-	0	-	-	ms

1) V_{DD} -turn-on conditions

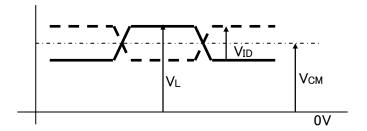




Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	4

2) Display pattern:

- 3) Input signal: SELLVDS, BITSEL, SC
- 4) Input signal : RxIN3+, RxIN3-, RxIN2+, RxIN2-, RxIN1+, RxIN1-, RxIN0+, RxIN0-CK IN+, CK IN-



- 5) V_{CM}: LVDS Common mode voltage (V_{CM}=1.25V)
- 6) Please power on LVDS transmitter at the same time as VDD, or LVDS transmitter should be powered on first.



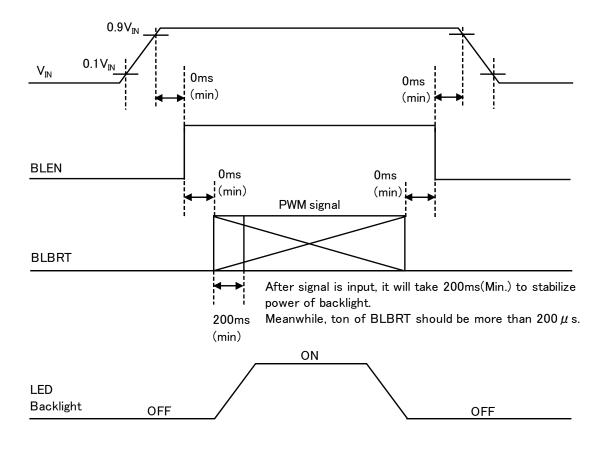
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	5

5-2. Constant current circuit for LED Backlight

Temp. = $-20 \sim 70$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	$V_{\rm IN}$	-	10.8	12.0	13.2	V
Current consumption	${ m I_{IN}}$	2)	-	165	280	mA
Permissive input ripple voltage	$V_{\mathrm{RP_BL}}$	V _{IN} =12.0V	-	1	100	mVp-p
DI DDT Input signal veltage	V _{IL_BLBRT}	"Low" level	0	1	0.8	V
BLBRT Input signal voltage	V _{IH_BLBRT}	"High" level	2.3	-	V_{IN}	V
BLBRT Input pull-down resistance	R _{IN_BLBRT}	-	100	300	500	k
DI EN Legest signed politicas	V _{IL_BLEN}	"Low" level	0	-	0.8	V
BLEN Input signal voltage	V _{IH_BLEN}	"High" level	2.3	-	V_{IN}	V
BLEN Input pull-down resistance	R _{IN_BLEN}	-	100	300	500	k
PWM Frequency 3)	$\mathbf{f}_{\mathrm{PWM}}$	-	200	-	10k	$_{ m Hz}$
		f_{PWM} =200Hz	1	-	100	%
P WM Duty ratio 3)	$\mathrm{D}_{\mathrm{PWM}}$	f _{PWM} =2kHz	10	-	100	%
		f _{PWM} =10kHz	50	-	100	%
Operating life time 4), 5)	Т	Temp.=25°C	-	70,000	-	h

1) V_{IN}-turn-on conditions

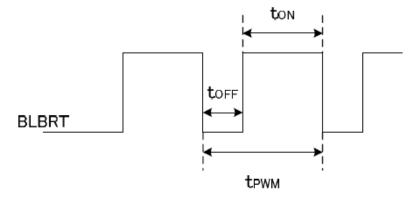


2) $V_{IN} = 12V$, Temp. = 25 , $D_{PWM} = 100\%$



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	6

3) PWM Timing Diagram



 $t_{\rm ON}$, $t_{\rm OFF}$ $50\,\mu\,s.$

In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

- 4) When brightness decrease 50% of minimum brightness.

 The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 5) Life time is estimated data. (Condition: IF=60mA, Ta=25 in chamber).

5-3. Touch panel

touch paner	
Item	Specification
Supply voltage for touch panel	$5.0\mathrm{V}$
m · 1 · · ·	$xL \sim xR : 274 \sim 640\Omega$
Terminal resistance	yU ~ yL : 183Ω ~ 428Ω
Linearity	less than ±2.0%
Insulation resistance	$100 \mathrm{M}\Omega$ or more at DC25V



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	7

6. Optical characteristics

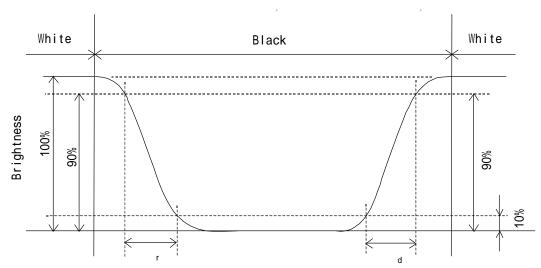
Measuring spot = 6.0mm, Temp. = 25°C

					0 1		1
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D .:	Rise	Τr	= =0°	-	5	-	ms
Response time	Down	τd	= =0°	-	25	-	ms
		UPPER		-	60	-	deg.
Viewing angle View direction	_	LOWER	CR 10	-	80	-	
: 12 o'clo		LEFT	CR 10	-	80	-	1
(Gray in	version)	ф кібнт		-	80	-	deg.
Contrast ratio		CR	= =0°	700	1000	-	-
Brightness		L	IF=60mA/Line	190	280	-	cd/m ²
	Red	x	= =0°	0.550	0.600	0.650	
		У		0.300	0.350	0.400	
	C	X	00	0.270	0.320	0.370	
Chromaticity	Green	У	= =0°	0.500	0.550	0.600	
coordinates	DI	X	= =0°	0.100	0.150	0.200	-
	Blue	У		0.070	0.120	0.170	
	VV 71- 14 -	X	= =0°	0.240	0.290	0.340	
	White	У		0.255	0.305	0.355	

6-1. Definition of contrast ratio

 $\begin{tabular}{ll} CR(Contrast\ ratio) &= & \hline & Brightness\ with\ all\ pixels\ "White" \\ \hline & Brightness\ with\ all\ pixels\ "Black" \\ \hline \end{tabular}$

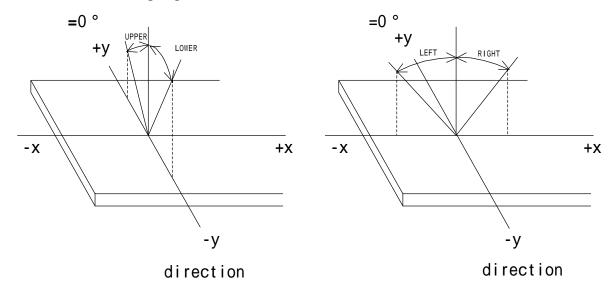
6-2. Definition of response time



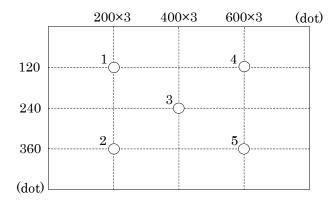


Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA	A89-00 TCG070WVLPEAFA-AA20	8

6-3. Definition of viewing angle



6-4. Brightness measuring points



- 1) Rating is defined as the white brightness at center of display screen(3).
- 2) 5 minutes after LED is turned on. (Ambient Temp.=25)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	9

7. Interface signals

7-1. Interface signals

No.	Symbol	Description	Note
1	BITSEL	Bit data select signal(GND or Open: 8bit mode, High: 6bit mode)	
2	SELLVDS	Mode select signal(LVDS Data mapping)	
3	GND	GND	
4	GND	GND	
5	RxIN3+	LVDS receiver signal CH3(+)	LVDS
6	RxIN3-	LVDS receiver signal CH3(-)	LVDS
7	GND	GND	
8	CK IN+	LVDS receiver signal CK(+)	LVDS
9	CK IN-	LVDS receiver signal CK(-)	LVDS
10	GND	GND	
11	RxIN2+	LVDS receiver signal CH2(+)	LVDS
12	RxIN2-	LVDS receiver signal CH2(-)	LVDS
13	GND	GND	
14	RxIN1+	LVDS receiver signal CH1(+)	LVDS
15	RxIN1-	LVDS receiver signal CH1(-)	LVDS
16	GND	GND	
17	RxIN0+	LVDS receiver signal CH0(+)	LVDS
18	RxIN0-	LVDS receiver signal CH0(-)	LVDS
19	GND	GND	
20	GND	GND	
21	$V_{ m DD}$	+3.3V power supply	
22	$V_{ m DD}$	+3.3V power supply	
23	SC	Scan direction control(High or Open: Normal、GND: Reverse)	1)
24	BLBRT	PWM signal(Brightness adjustment)	
25	BLEN	ON/OFF terminal voltage	
26	NC	NC	
27	Vin	+12V power supply	
28	Vin	+12V power supply	
29	GNDB	GND (Backlight)	
30	GNDB	GND (Backlight)	

LCD connector : MDF76GW-30S-1H(55) (HIROSE)
Matching connector : MDF76-30P-1C (HIROSE)

LVDS receiver : Embedded in ASIC

 $Matching\ LVDS\ transmitter\quad :\quad THC63LVDM83R(THine\ Electronics)\ or\ compatible$

1) Scanning

SC: High or Open SC: GND







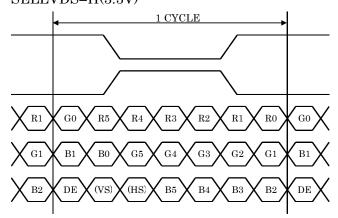
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	10

7-2. Data mapping (6bit input / 8bit mode)

1) Location of BITSEL, SELLVDS (THC63LVDM83R(THine Electronics) or compatible)

L/ Location C	,, DII () DID, 1	SELLADO (TITOGOLADIMOGIA(TITILI	c Licetroffics, of compatible,
Transmitter		1Pin BITSEL = "L" or OPEN	1Pin BITSEL = "L" or OPEN
Pin No.	Data	2Pin SELLVDS = "L" or OPEN	2Pin SELLVDS = "H"
51	TA0	-	R0(LSB)
52	TA1	-	R1
54	TA2	-	R2
55	TA3	-	R3
56	TA4	-	R4
3	TA5	-	R5(MSB)
4	TA6	-	G0(LSB)
6	TB0	-	G1
7	TB1	-	G2
11	TB2	-	G3
12	TB3	-	G4
14	TB4	-	G5(MSB)
15	TB5	-	B0(LSB)
19	TB6	-	B1
20	TC0	-	B2
22	TC1	-	B3
23	TC2	-	B4
24	TC3	-	B5(MSB)
27	TC4	-	(HS)
28	TC5	-	(VS)
30	TC6	-	DE
50	TD0	-	GND
2	TD1	-	GND
8	TD2	-	GND
10	TD3	-	GND
16	TD4	-	GND
18	TD5	-	GND
25	TD6	-	GND

BITSEL=L(GND) or OPEN SELLVDS=H(3.3V)



DE: DATA ENABLE

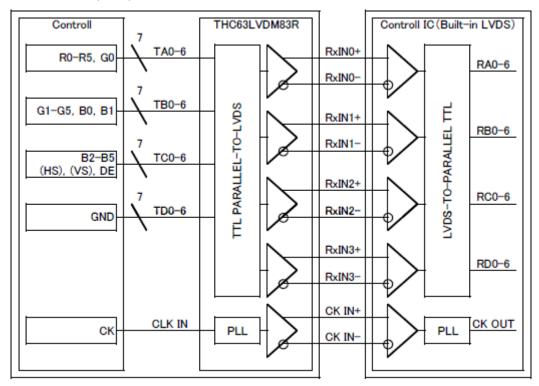
 $\begin{array}{c} HS:H_{SYNC} \\ VS:V_{SYNC} \end{array}$



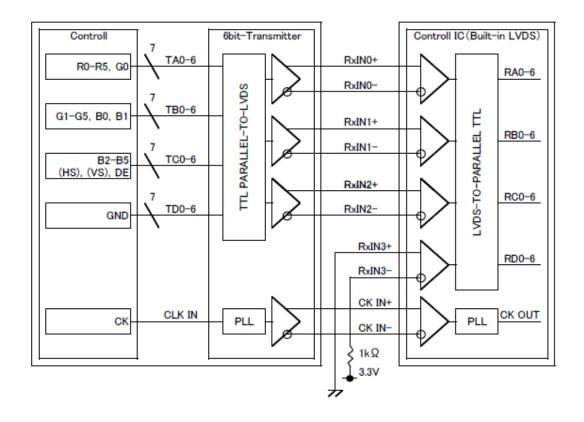
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	11

2) Block Diagram

BITSEL=L(GND) or OPEN SELLVDS=H(3.3V)



When using "6-bit Transmitter", please connect the unused channel of the control IC receiver as described in the diagram below.





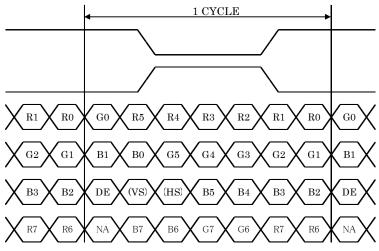
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	12

7-3. Data mapping (8bit input / 8bit mode)

1) Location of BITSEL, SELLVDS (THC63LVDM83R(THine Electronics) or compatible)

Transmitter		1Pin BITSEL = "L" or OPEN	1Pin BITSEL = "L" or OPEN
Pin No.	Data	2Pin SELLVDS = "L" or OPEN	2Pin SELLVDS = "H"
51	TA0	R0(LSB)	R2
52	TA1	R1	R3
54	TA2	R2	R4
55	TA3	R3	R5
56	TA4	R4	R6
3	TA5	R5	R7(MSB)
4	TA6	G0(LSB)	G2
6	TB0	G1	G3
7	TB1	G2	G4
11	TB2	G3	G5
12	TB3	G4	G6
14	TB4	G5	G7(MSB)
15	TB5	B0(LSB)	B2
19	TB6	B1	В3
20	TC0	B2	B4
22	TC1	B3	B5
23	TC2	B4	B6
24	TC3	B5	B7(MSB)
27	TC4	(HS)	(HS)
28	TC5	(VS)	(VS)
30	TC6	DE	DE
50	TD0	R6	R0(LSB)
2	TD1	R7(MSB)	R1
8	TD2	G6	G0(LSB)
10	TD3	G7(MSB)	G1
16	TD4	B6	B0(LSB)
18	TD5	B7(MSB)	B1
25	TD6	(NA)	(NA)

BITSEL=L(GND) or OPEN SELLVDS=L(GND) or OPEN

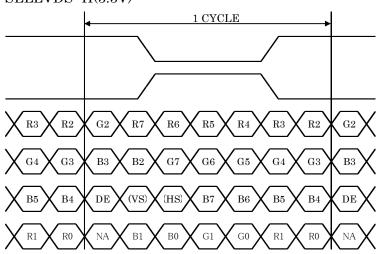


DE: DATA ENABLE

HS: Hsync VS: Vsync



BITSEL=L(GND) or OPEN SELLVDS=H(3.3V)

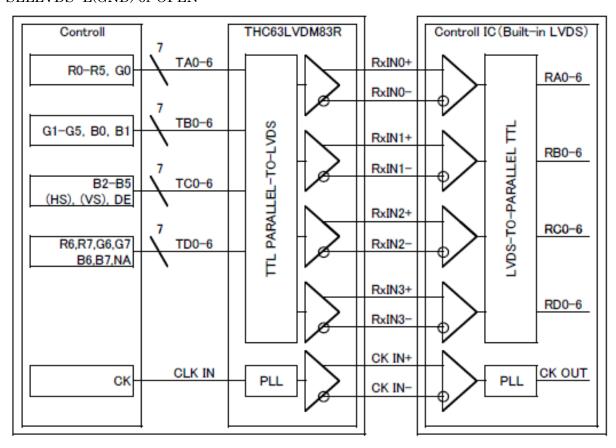


DE: DATA ENABLE

 $HS: H_{SYNC}$ $VS: V_{SYNC}$

2) Block Diagram

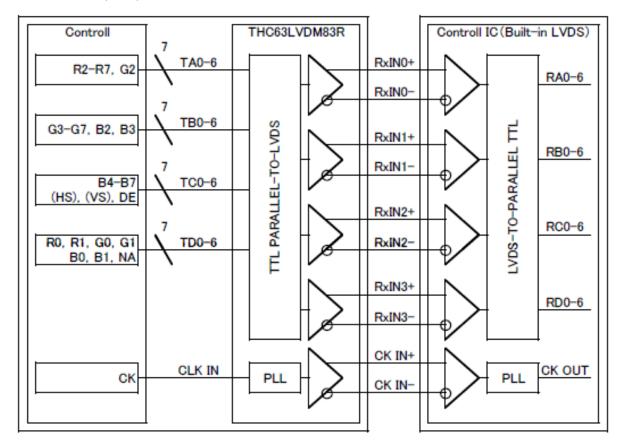
BITSEL=L(GND) or OPEN SELLVDS=L(GND) or OPEN





Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	14

BITSEL=L(GND) or OPEN SELLVDS=H(3.3V)





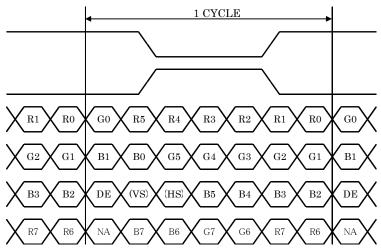
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	15

7-4. Data mapping (6bit input / 6bit mode)

1) Location of BITSEL, SELLVDS (THC63LVDM83R(THine Electronics) or compatible)

Trans	Transmitter 1Pin BITSEL = "H"		1Pin BITSEL = "H"
Pin No.	Data	2Pin SELLVDS = "L" or OPEN	2Pin SELLVDS = "H"
44	TA0	R0(LSB)	-
45	TA1	R1	-
47	TA2	R2	-
48	TA3	R3	-
1	TA4	R4	-
3	TA5	R5(MSB)	-
4	TA6	G0(LSB)	-
6	TB0	G1	-
7	TB1	G2	-
9	TB2	G3	-
10	TB3	G4	-
12	TB4	G5(MSB)	-
13	TB5	B0(LSB)	-
15	TB6	B1	-
16	TC0	B2	-
18	TC1	В3	-
19	TC2	B4	-
20	TC3	B5(MSB)	-
22	TC4	(HS)	-
23	TC5	(VS)	-
25	TC6	DE	-

BITSEL=H(3.3V) SELLVDS=L(GND) or OPEN



DE: DATA ENABLE

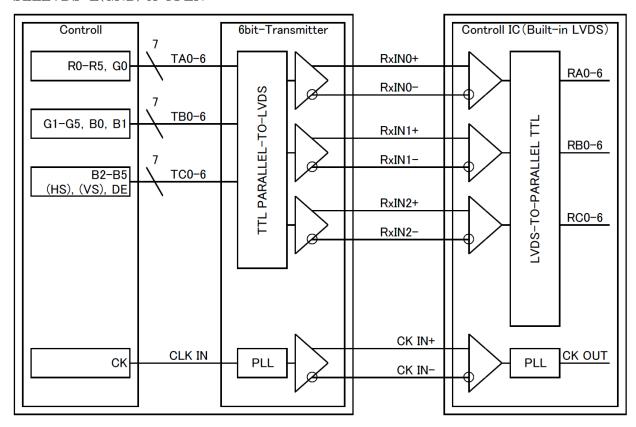
 $HS: H_{SYNC}$ $VS: V_{SYNC}$



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	16

2) Block Diagram

BITSEL=H(3.3V) SELLVDS=L(GND) or OPEN



7-5. Touch panel

No.	Symbol	Description
1	xR	x-Right terminal
2	yL	y-Lower terminal
3	xL	x-Left terminal
4	уU	y-Upper terminal

Touch panel side connector : 1mm pitch

Recommended matching connector : 08 6262 004 940 846+ (KYOCERA Connector Products)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	17

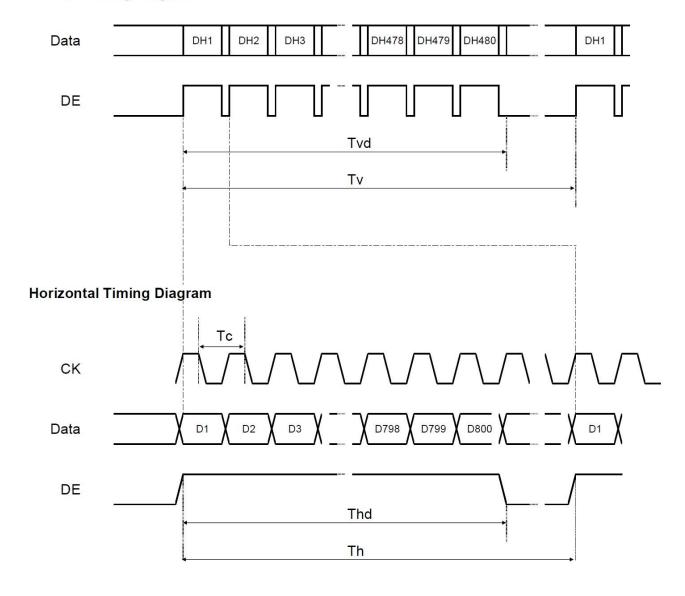
8. Input timing characteristics

8-1. Timing characteristics

	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock (CK)	Frequency	1/Tc	(29.88)	(33.2)	(36.52)	MHz	
	Horizontal Period	Th	(1024)	(1056)	(1088)	Тс	
			-	(31.8)	-	μs	1)
Enable signal (DE)	Horizontal display period	Thd		800		Тс	
(DL)	Vertical Period	Tv	(487)	(525)	(550)	Th	
	Vertical display period	Tvd		480		Th	
Refresh rate		fv	50	60	70	Hz	2)

- 1) Please set a clock frequency, a vertical dormant period, and the horizontal dormant period so that the Horizontal Period should not reach less than Min. value.
- 2) If the refresh rate reach less than Min. value, the deterioration of the display quality, flicker etc., may occur.(fv=1/Tv)

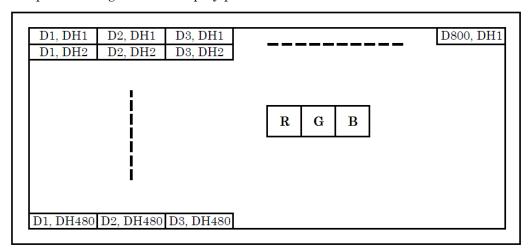
Vertical Timing Diagram





Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	18

8-2. Input Data Signals and Display position on the screen



9. Design guidance for analog touch panel

- 9-1. Electrical (In customer's design, please remember the following considerations.)
 - 1) Do not use the current regulated circuit.
 - 2) Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maximum ratings" for details.)
 - 3) Analog touch panel can not sense two points touching separately.
 - 4) A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the touch panel position data.
 - 5) Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

9-2. Software

- 1) Do the "User Calibration".
- 2) "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3) When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.
- 9-3. Mounting on display and housing bezel
 - 1) Do not use an adhesive tape to bond it on the front of touch panel and hang it to the housing bezel.
 - 2) Never expand the touch panel top layer (PET-film) like a balloon by internal air pressure. The life of the touch panel will be extremely short.
 - 3) If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur. This will cause sometimes a short circuit.
 - 4) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	19

10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Ī	Year	2012	2013	2014	2015	2016	2017
Ī	Code	2	3	4	5	6	7

Ī	Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
	Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	20

12. Precautions for use

12-1. Installation of the LCD

- 1) The LCD shall be installed so that there is no pressure on the LSI chips.
- 2) The LCD shall be installed flat, without twisting or bending.
- Please design the housing window so that its edges are between the active area and the effective area of the LCD screen.

12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

12-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Please select the best display pattern based on your evaluation because flicker, lines or nonuniformity or unevenness can be visible depending on display patterns.

12-4. Storage

- The LCD shall be stored within the temperature and humidity limits specified.
 Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAA89-00	TCG070WVLPEAFA-AA20	21

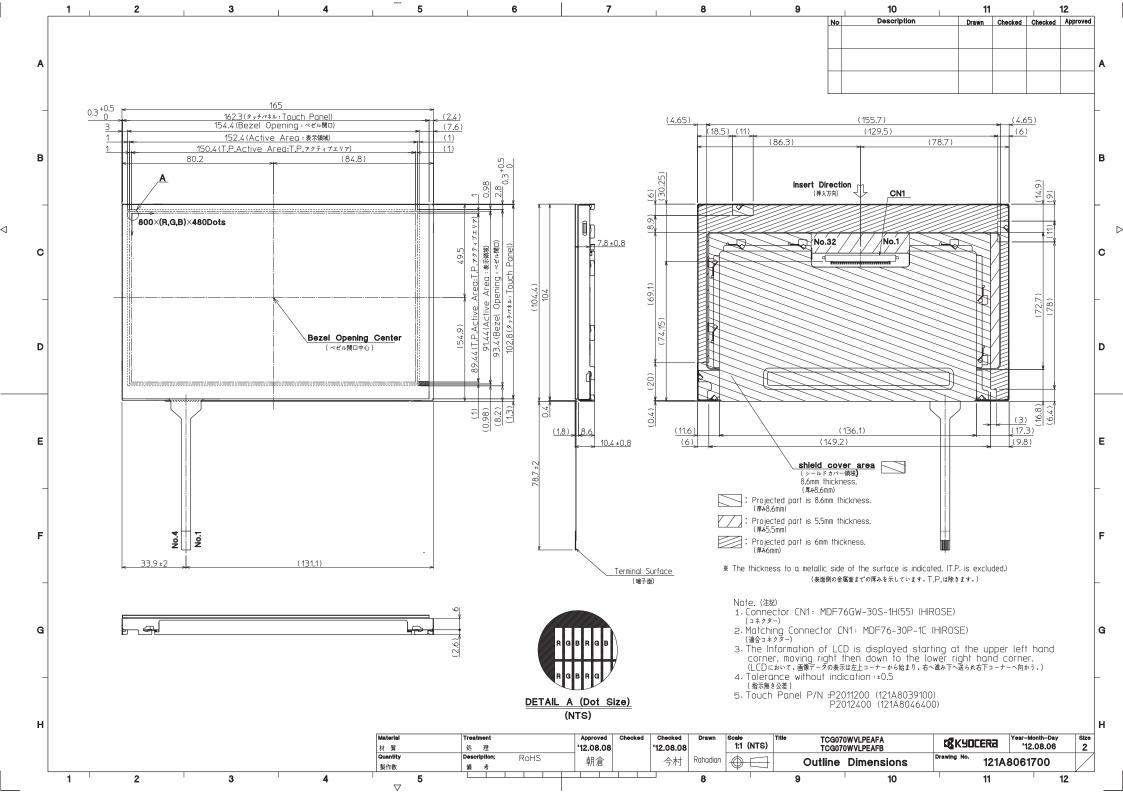
13. Reliability test data

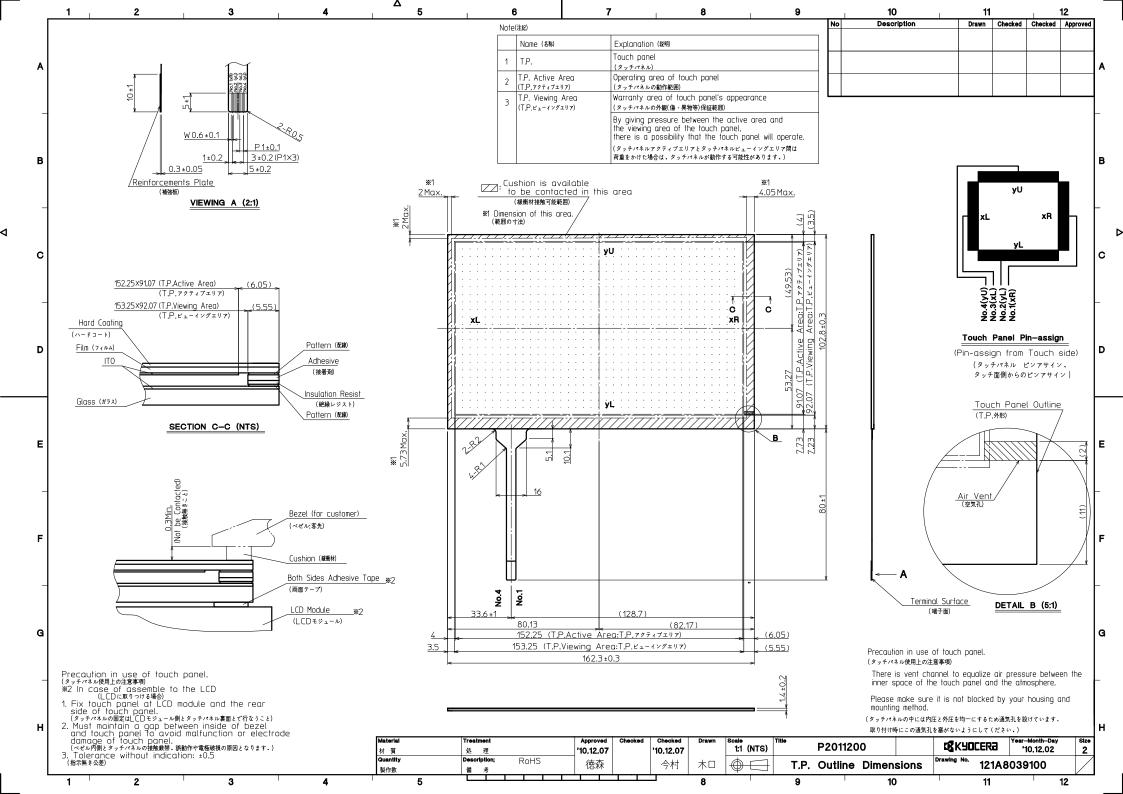
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	No defectNo defectNo defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	No defectNo defectNo defect
Point Activation life	Silicon rubber, Tip: R = 4.0 Hitting force 3N Hitting speed 2 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	: No defect: No defect: No defect: No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

 The reliability test is conducted only to examine the LCD's capability.







Spec No.	TQ3C-8EAF0-E2YAA89-00
Date	August 9, 2012

KYOCERA INSPECTION STANDARD

TYPE: TCG070WVLPEAFA-AA20

KYOCERA CORPORATION LCD DIVISION

Original	Designed by:	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
August 9, 2012	X. Mori	Y. Yamayaki	M.Fyitani	I Hamars	Ho , Suf



Spec No.	Part No.	Page
TQ3C-8EAF0-E2YAA89-00	TCG070WVLPEAFA-AA20	-

Revision record

			ed by:	Engineering dept.		Confirmed by : QA dept.	
	Date	Prepa	ared	Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Description	ons	



Spec No.	Part No.	Page
TQ3C-8EAF0-E2YAA89-00	TCG070WVLPEAFA-AA20	1

Visuals specification

Note

1) Note							
			Note				
General	 Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. This inspection standard about the image quality shall be applied to any defect within 						
	the acti	ve area and shall not be applicable to outside of the area.					
	_	Inspection conditions					
	Lumina		: 500 Lux min.				
		ion distance	: 300 mm.				
	Temper		: 25 ± 5				
D.C.L.	Direction	T	Directly above				
Definition of inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen.				
inspection item			Inspection tool: 5% Transparency neutral density filter.				
			Count dot: If the dot is visible through the filter.				
			Don't count dot: If the dot is not visible through the				
			filter. RGBRGBRGB RGBRGBRGB dot defect				
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen.				
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot				
			defects or black dot defects.				
			R G B R G B R G B R G B R G B R G B R G B R G B R G B				
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non				
	inspection	Foreign particle	operating.				
		(Polarizer, Cell,					
		Backlight)					
		Appearance	Does not satisfy the value at the spec.				
	D (*	inspection					
	Definition of size	Definition of	circle size Definition of linear size				
	01 5126	d = (a + b)/2					



Spec No.	Part No.	Page
TQ3C-8EAF0-E2YAA89-00	TCG070WVLPEAFA-AA20	2

2) Standard

2) Standa		т.	,		T 1 .	. 1	1	
Classification Inspection item		Judgement standard						
Defect	Dot	Bright dot defect		Acceptable number : 4				
(in LCD	defect					nm or more		
glass)		Black dot	defect	Acceptable number	: 5 : 5 mm : 2		or more	
			T	Black dot spacing				
		2 dot join	Bright dot defect	Acceptable number				
			Black dot	Acceptable number				
		3 or more	defect dots join	Acceptable number	: 0			
		Total dot d	-	Acceptable number	: 5 Max		-	
	Others	White dot,		Ticcoptable frameer		0 1/142		
	Others	(Circle)	Dark dot	Size (mm			ceptable number	
		(Circle)		d d	0.2	AC	(Neglected)	
				0.2 < d	0.4		5	
				0.4 < d	0.5		3	
				0.5 < d			0	
			,					
	inspection	Polarizer (Scratch)					
(Defect or				Width (mm)	Length (mm)		Acceptable number	
Polarizer				W 0.1 -			(Neglected)	
between I	Polarizer			0.1 < W 0.3	L 5.0		(Neglected)	
and LCD	nd LCD glass)			5.0 < L		0		
				0.3 < W	-		U	
		Polarizer (Bubble)					
				Size (mm)		Acceptable number		
				d	0.2		(Neglected)	
				0.2 < d	0.3		5	
				0.3 < d	0.5		3	
				0.5 < d	d 0			
		Foreign pa	ırticle					
		(Circular shape)		Size (mm	1)	Acceptable number		
				d 0.2		(Neglected)		
				0.2 < d	0.4		5	
				0.4 < d 0.5		3		
				0.5 < d			0	
		Foreign particle						
		(Linear shape)		Width (mm)	Length (mm)		Acceptable number	
		Scratch	• •	W 0.03	3 - L 2.0		(Neglected)	
							(Neglected)	
				0.03 < W 0.1			3	
					4.0 < L		0	
				0.1 < W	-		(According to	
							circular shape)	



Spec No.	Part No.	Page
TQ3C-8EAF0-E2YAA89-00	TCG070WVLPEAFA-AA20	3

Inspection item	Judgement standard								
Scratch,	(W = Width, L = Length, D = Diameter = (major axis + minor axis)/ 2)								
Foreign particle				Acc	ceptable number				
(Touch screen		W	0.03	L	20		Neglected		
portion)	Q 1	0.03 < W	0.05	L	10	2pce	es within φ20mm		
	Scratch	0.05 < W	0.08	L	6	2pce	es within φ20mm		
		0.08 < W	0.1	L	4	1pce	es within φ30mm		
	Foreign	W	0.05	Negle	ected		Neglected		
	(line like)	0.05 < W	0.1	L	5	2pce	2pces within φ30mm		
	Foreign		D	0.2			Neglected		
	(circle like)	0.2 < D 0.3			2pce	es within φ30mm			
	Above are app	lied to the visib	le area.						
	Unless there	are foreign pa	article a	and dan	nage af	ffecte	d seriously to the		
	electrical perfe	ormance out of t	the activ	e area,	we appi	rove o	f this product.		
Glass crack							Aggentable		
(Touch screen	Item		Size (n	nm)			Acceptable number		
portion)							number		
			/	12 /	X	3			
	Conner	Y				3	2 pcs		
	crack					0	/panel		
			1	Z	< t				
		_	/	·.					
	Crack in	X 5				5			
	other area		Y		1.5	2 pcs			
	than in				1.0	/side			
	corner				Z	< t			
		- 1							
					/				
		/	//						
	Progressive						0 pcs		
	crack	_ `	$\overline{}$				(NG even 1pcs)		
			\sim $\!$						
	Above are applied to the visible area. Unless there are foreign particle and damage affected seriously to the electrical performance out of the active area, we approve of this product.								
Newton's ring	Neglected.								
- · · · · · · · · · · · · · · · · · · ·									
	Newton's ring								

