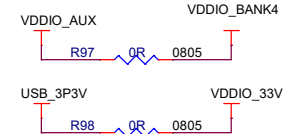
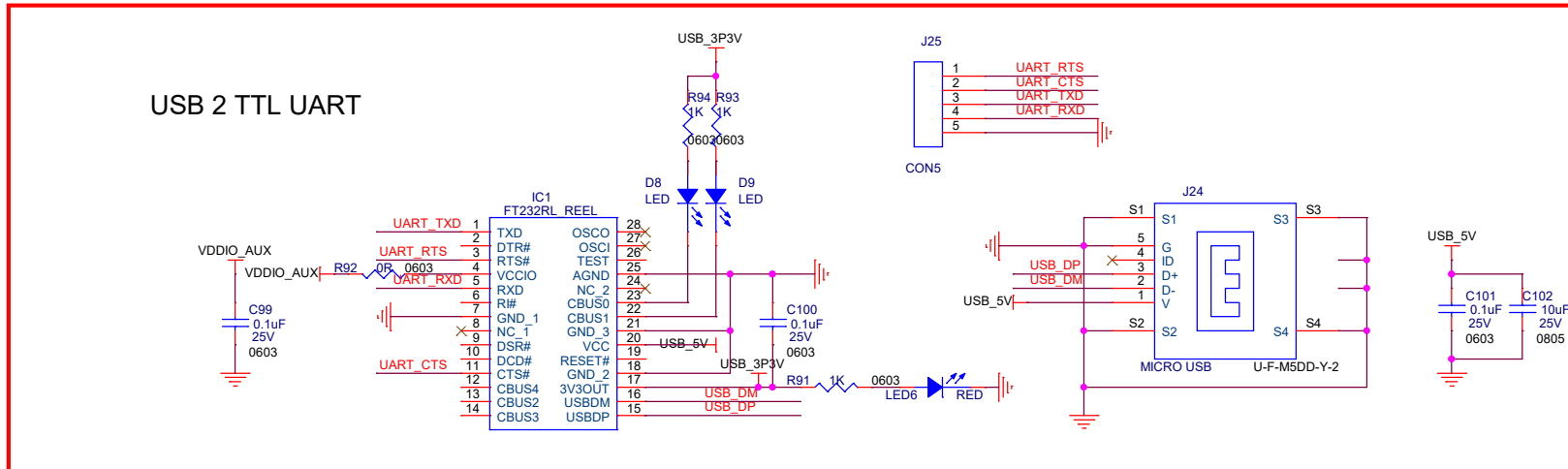
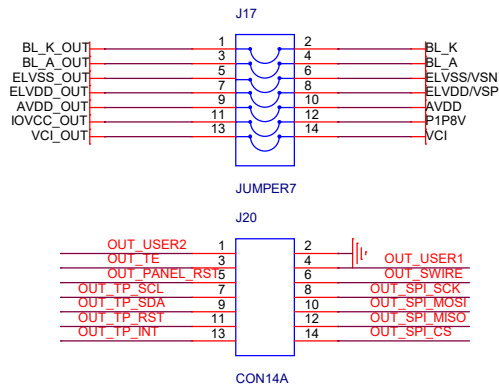
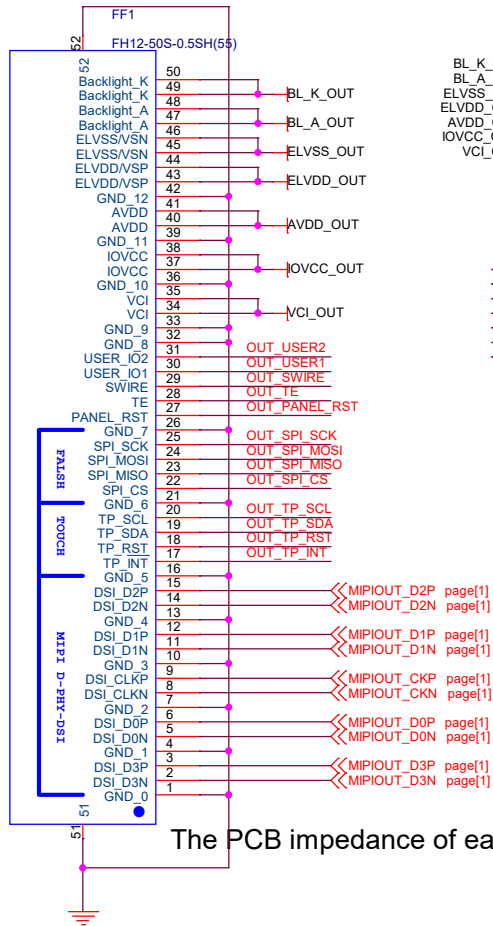


page[1] LED0 << LED0  
 page[1] LED1 << LED1  
 page[1] MSEL0 << MSEL0  
 page[1] MSEL1 << MSEL1  
 page[1] USER\_SW0 << USER\_SW0  
 page[1] UART\_RTS << UART\_RTS  
 page[1] UART\_CTS << UART\_CTS  
 page[1] UART\_TXD << UART\_TXD  
 page[1] UART\_RXD << UART\_RXD



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连接器左1为 PIN1



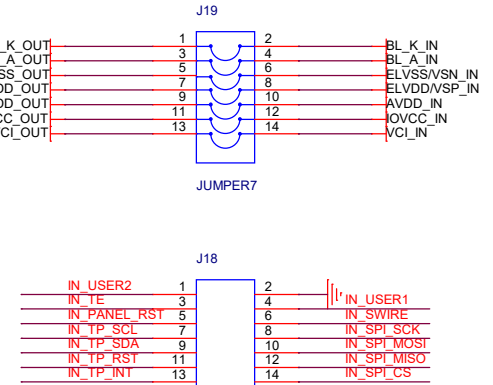
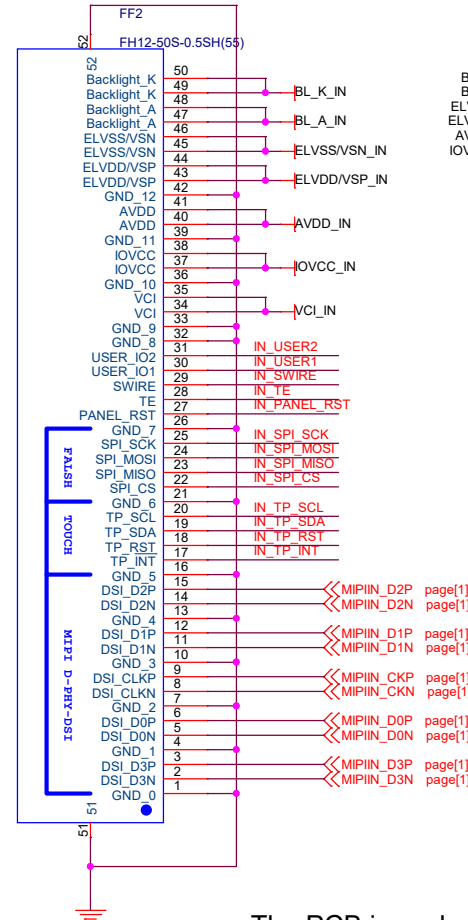
OUT\_TP\_SCL R82 2.2k 0603  
OUT\_TP\_SDA R83 2.2k 0603

MIPI\_OUT The relative length of each lane is less than 50mil, that is,  $\pm 50\text{mil}$

The relative length between P and N is less than 10mil, that is,  $\pm 10\text{mil}$

The PCB impedance of each differential pair is 100 ohms

连接器左1为 PIN1



IN\_TP\_SCL R84 2.2k 0603  
IN\_TP\_SDA R85 2.2k 0603

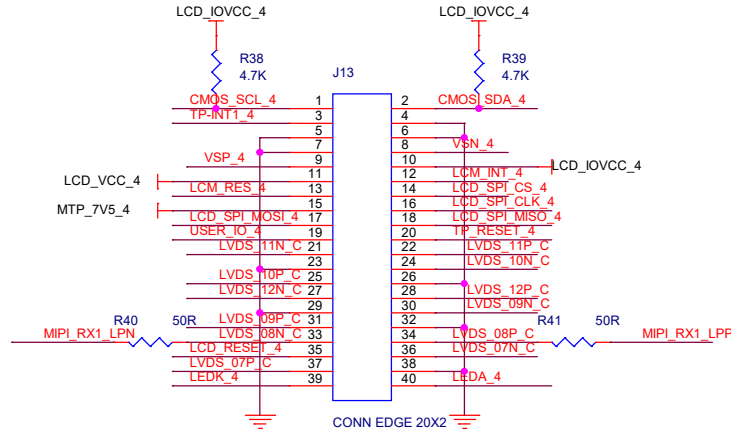
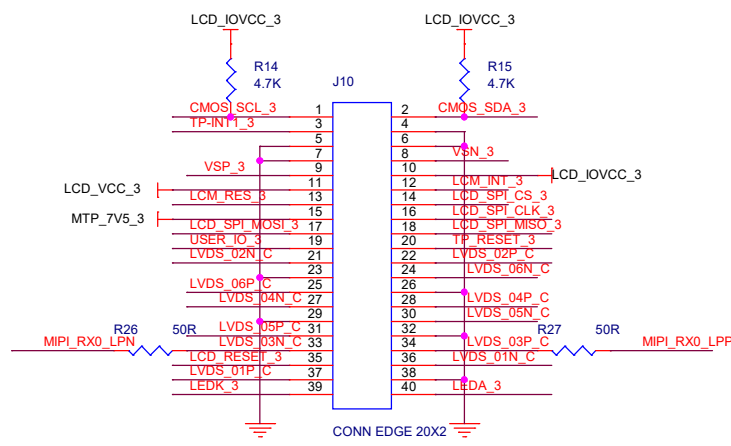
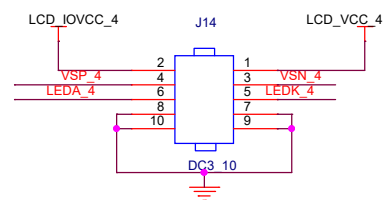
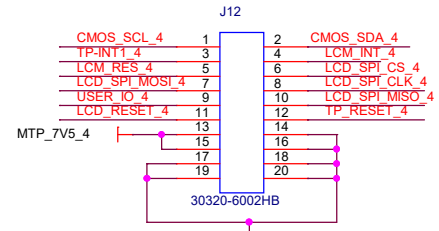
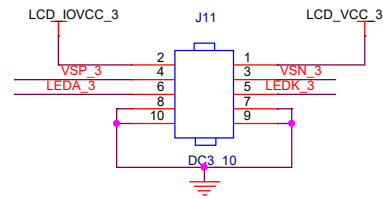
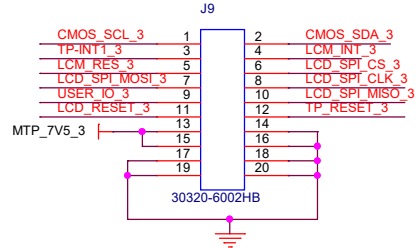
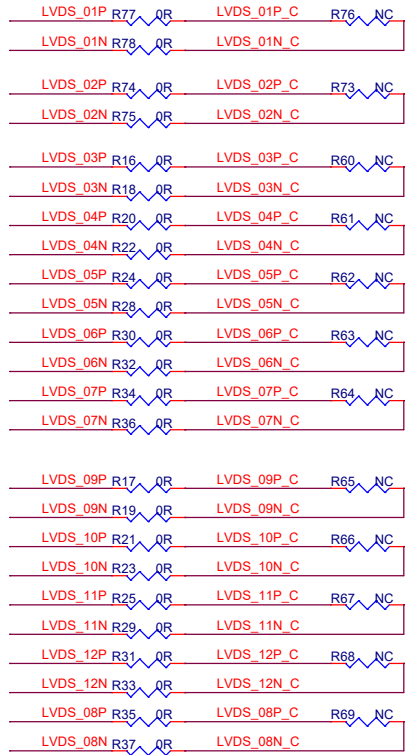
MIPI\_IN The relative length of each lane is less than 50mil, that is,  $\pm 50\text{mil}$

The relative length between P and N is less than 10mil, that is,  $\pm 10\text{mil}$

The PCB impedance of each differential pair is 100 ohms



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|---------|--------------|--------------|
| page[1] | LVDS_01P     | LVDS_01P     |
| page[1] | LVDS_01N     | LVDS_01N     |
| page[1] | LVDS_02P     | LVDS_02P     |
| page[1] | LVDS_02N     | LVDS_02N     |
| page[1] | LVDS_03P     | LVDS_03P     |
| page[1] | LVDS_03N     | LVDS_03N     |
| page[1] | LVDS_04P     | LVDS_04P     |
| page[1] | LVDS_04N     | LVDS_04N     |
| page[1] | LVDS_05P     | LVDS_05P     |
| page[1] | LVDS_05N     | LVDS_05N     |
| page[1] | LVDS_06P     | LVDS_06P     |
| page[1] | LVDS_06N     | LVDS_06N     |
| page[1] | LVDS_07P     | LVDS_07P     |
| page[1] | LVDS_07N     | LVDS_07N     |
| page[1] | LVDS_08P     | LVDS_08P     |
| page[1] | LVDS_08N     | LVDS_08N     |
| page[1] | LVDS_09P     | LVDS_09P     |
| page[1] | LVDS_09N     | LVDS_09N     |
| page[1] | LVDS_10P     | LVDS_10P     |
| page[1] | LVDS_10N     | LVDS_10N     |
| page[1] | LVDS_11P     | LVDS_11P     |
| page[1] | LVDS_11N     | LVDS_11N     |
| page[1] | LVDS_12P     | LVDS_12P     |
| page[1] | LVDS_12N     | LVDS_12N     |
| page[1] | MIPI_RX0_LPN | MIPI_RX0_LPN |
| page[1] | MIPI_RX0_LPP | MIPI_RX0_LPP |
| page[1] | MIPI_RX1_LPN | MIPI_RX1_LPN |
| page[1] | MIPI_RX1_LPP | MIPI_RX1_LPP |



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