

# Xu Liu

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## Education

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|-----------------------------------|--------------------------------------|------------------------|
| <b>Master of Computer Science</b> | Tufts University, US                 | Sept. 2014 - Aug. 2016 |
| <b>Master of Material Science</b> | Southwest Jiaotong University, China | June 2008              |

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## Skills

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C/C++/Java, Html/CSS/JavaScript, OpenGL/WebGL/CUDA, SQL/NoSQL,

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## Projects

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Sorting on CPU and GPU (Computer Architecture <https://github.com/liuxu1005/Computer-Architecture/blob/master/FinalReport/FinalReport.pdf>)  
Water (Computer Graphics)  
Content Centric Network Report (System Design <http://liuxu1005.github.io/system/FinalReport.html>)  
Triangulation/Monotonicity (Computational Geometry <http://liuxu1005.github.io/geometry/triangulation.html>)  
Channel Code/Modulation/Routing (Networks <https://github.com/liuxu1005/Network>)  
JOS (Operating System <https://github.com/liuxu1005/Operating-System-Engineering>)

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## Courses

### Courses from Tufts University

Computation Theory, Algorithm, Programming Language, Data Structure, Discrete Mathematics,  
Artificial Intelligence, Introduction to Machine Learning, Computational Geometry, Computer Graphics,  
Advanced Computer Architecture, Machine Architecture and Assembly Language, Database, System Design

### Courses Taught by self

Operating System (following MIT 6.828 Operating System Engineering)  
Networks (following MIT 6.829 Networks, 6.02 Digital Communication and Tufts comp 112 Networks and Protocol)

### Currently Working on

Compiler, Distributed System

## Other Experiences

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|---|-------------------------------------|---------------------|
| <b>Application Engineer</b>   | Angstrom Advanced Inc. Braintree MA | Mar.2012 - Jan.2015 |
| Technical support on application, installation & troubleshooting for chemical/material analytical instruments such as SEM, AFM/SFM, XRD, EDS/WDS, ICP-AES, GC-MS. |                                     |                     |

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|---|-------|---------------------|
| <b>Lithography Process Engineer / Integrated Process Engineer</b>   | China | Feb.2008 - Aug.2010 |
| Develop and maintain processes for 6" silicon wafer production line |       |                     |