



## Release Notes

Vector SLP4

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## Release 21



### General

Type	Description	Change ID
Breaking Change	<p>A new BSW module for general settings has been introduced: vSet. The module is mandatory for any BSW configuration and will be activated automatically when creating a new project.</p> <p>For the moment the settings are synchronized automatically from the related parameters of the AR EcuC module. So, there is no need for additional configuration.</p> <p>In the next releases vendor specific parameter will be removed from the EcuC module.</p> <div><b>Additional Information</b><p>The goal of this feature is to enable usage of MICROSAR modules with third party EcuC modules that do not provide the MICROSAR specific vendor parameters.</p></div> <p><b>Migration notes for existing projects:</b></p> <p>Activate the module manually within DaVinci Configurator Pro -&gt; Project Settings.</p>	FEAT-2949
Information	<p>The usage of Technical References and User Manuals that are provided with a MICROSAR SIP has been redefined.</p> <ul style="list-style-type: none"><li>&gt; Technical References are now used for module specific documentation only.</li><li>&gt; User Manuals are now used for topics that affect multiple modules (e.g. Identity Management or vVIRTUALtarget). The scope of the document can be both:<ul style="list-style-type: none"><li>&gt; introduction level: first overview on a subject</li><li>&gt; expert level: gives a detailed description on the subject</li></ul></li><li>&gt; Application Notes will be used to describe non-typical use-cases and may not always be adapted to the latest MICROSAR release.</li></ul>	DOCU-70

Type	Description	Change ID
	 <b>Additional Information</b> The transition has been started with R21 and will be continued in the next releases.	
Information	<p>The comments that justify MISRA violations of MICROSAR components are migrated to MISRA 2012 (QAC tool based). Previously MISRA 2004 was supported.</p> <p>The migration will be performed step wise and will be continued in the next releases.</p>	FEAT-3506



### Communication

Type	Description	Change ID
Extension	<p>The J1939Nm and J1939Rm were extended to supports routing of the relevant J1939 messages within shared address spaces and over gatewaying relations:</p> <ul style="list-style-type: none"> <li>&gt; J1939Nm ensures that all necessary addresses are claimed, and that CommandedAddress and NameManagement Parameter Groups are forwarded to the relevant channels.</li> <li>&gt; J1939Rm routes Request, Request2 and Acknowledgement Parameter Groups to all relevant channels.</li> </ul>	FEAT-2881
Extension	<p>To free CPU time on the main BSW core, the Ethernet stack can now be moved on a dedicated core. The PduR therefore supports cross core routings between the Ethernet stack and upper layer modules.</p> <div>  <b>Important Information</b>  Due to the wide range of possible use-cases we recommend consultation with Vector before planning the usage of this functionality. </div> <div>  <b>Additional Information</b>  This feature requires dedicated licensing. </div>	FEAT-3063
Extension	<p>The Com module now considers ComTxModeTimeOffset time after a Tx mode is changed by the application. This is relevant to avoid bursts of Tx messages after a mode switch.</p>	FEAT-3304

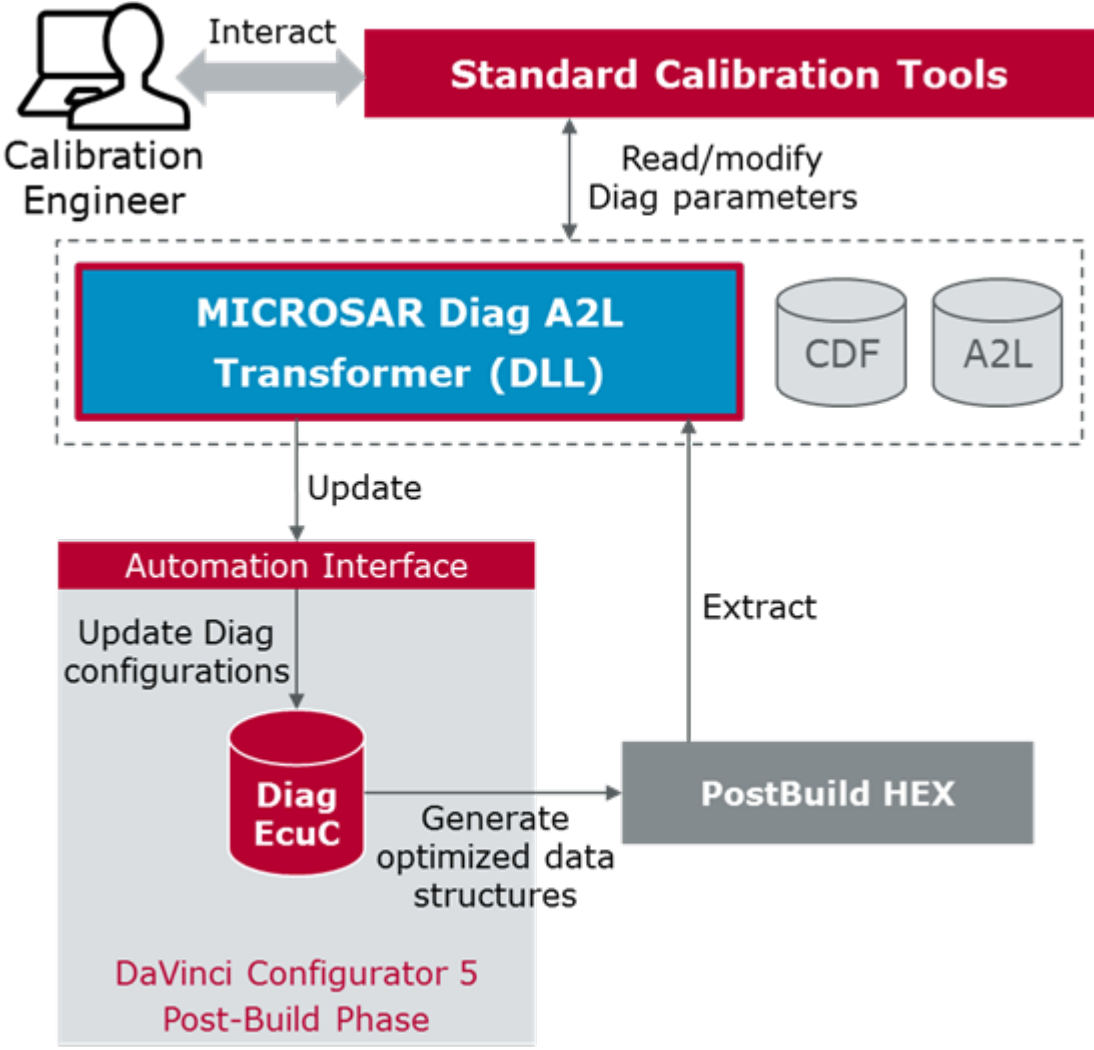
Type	Description	Change ID
	The change has been triggered by AUTOSAR RfC #77936 .	
Extension	<p>MICROSAR Can driver now support Tx FiFo queues provided by the CAN hardware.</p> <p>This feature allows e.g. the transmission of several CAN messages within one Tx polling transmit cycle.</p> <div>  <b>Additional Information</b>            This feature requires licensing of the "Can High-End Features" and is only provided for selected driver implementations.         </div> <div>  <b>Additional Information</b>            In a first step the feature has been realized for Renesas RH850 RSCAN.         </div>	FEAT-3419
Extension	The StbM now avoids accessing uncertain ethernet based clock sources by checking EthIf_GetControllerMode() before accessing the Ethernet clock. As long the mode is not ACTIVE, the functions SetGlobalTime() and GetCurrentTime() now return E_NOT_OK.	FEAT-3859
Information	BSW modules for time synchronization (StbM, CanTsyn, FrTsyn) are now available for SafeBSW projects up to ASIL D.	FEAT-2483
Information	<p>Accuracy of StbM has been improved by several fixes. Most notably:</p> <ul style="list-style-type: none"> <li>&gt; Support of time base specific time difference calculation according to AUTOSAR 4.3.1</li> <li>&gt; Schedule table synchronization for different counter resolutions including counters with a tick duration &lt; 1us.</li> </ul>	FEAT-3603
Information	The accuracy of time synchronization stack (StbM, CanTsyn, FrTsyn) has been improved by a rework of the exclusive areas used internally in these modules.	FEAT-3628



## Diagnostics

Type	Description	Change ID
Extension	The SafeBSW implementation of Dcm has been finalized.	FEAT-2505



Type	Description	Change ID
	 <p><b>Additional Information</b></p> <p>Since R20 the following major services are now safe:</p> <ul style="list-style-type: none"> <li>&gt; DID Services (0x24, 0x2E, 0x2F)</li> <li>&gt; (WWH-)OBD - Mode 0x01, 0x02, 0x03, 0x04, 0x06, 0x07, 0x08, 0x09, 0x0A</li> </ul> <p>&gt; Service 0x86</p>	
Extension	<p>The FiM implementation (AR4.3 based) now provides a master/satellite based multi-core implementation that allows distributing the FiM runtime across different cores.</p>  <p><b>Important Information</b></p> <p>The implementation of the multicore handling is with R21 BETA only. Production release is planned for R22.</p>	FEAT-2740
Extension	<p>The Dem module allows the event info port creation to be optional. The goal was to reduce the number of unconnected ports in case the info port is not required.</p> <p>The creation of even info ports can be enabled and disabled using the configuration switch DemConfigSet/DemEventParameter.DemEventCreateInfoPort.</p>	FEAT-2751
Extension	<p>Calibration engineers can now update the (OBD) diagnostic configuration of MICROSAR Dem and FiM at post-build time using ASAM tools such as vCDMStudio.</p>	FEAT-2799




Type	Description	Change ID
	<p>Diagnostic parameter calibration workflow</p>  <pre>graph TD     CE[Calibration Engineer] &lt;--&gt; Interact  SCT[Standard Calibration Tools]     SCT &lt;--&gt; Read/modify Diag parameters  Box     subgraph Box [ ]         direction LR         M[MICROSAR Diag A2L Transformer (DLL)]         C[(CDF)]         A[(A2L)]     end     M -- Update --&gt; AI[Automation Interface]     subgraph DCP [DaVinci Configurator 5 Post-Build Phase]         AI -- Update Diag configurations --&gt; DE[(Diag EcuC)]         DE -- Generate optimized data structures --&gt; PBH[PostBuild HEX]     end     PBH -- Extract --&gt; M</pre> <p>The diagram illustrates the diagnostic parameter calibration workflow. It begins with a <b>Calibration Engineer</b> interacting with <b>Standard Calibration Tools</b>. These tools read and modify diagnostic parameters from a central component, the <b>MICROSAR Diag A2L Transformer (DLL)</b>, which is associated with <b>CDF</b> and <b>A2L</b> files. The transformer updates the <b>Automation Interface</b>, which in turn updates diagnostic configurations within the <b>DaVinci Configurator 5 Post-Build Phase</b>. This phase generates optimized data structures in the <b>Diag EcuC</b>, which are then extracted by <b>PostBuild HEX</b> back to the transformer.</p>	

Type	Description	Change ID
	<div>  <b>Additional Information</b>  Prerequisites for using this vPblCalib: <ul style="list-style-type: none"> <li>&gt; The calibration tool must support transformers as defined in ASAM MCD-2 MC 1.7.0.</li> <li>&gt; This feature requires post-build loadable (PBL for DIAG) to be licensed.</li> </ul> </div> <div>  <b>Documentation in SIP</b>  Details on the workflow can be found in TechnicalReference_vPblCalib.pdf. </div>	
Extension	<p>The Dem IUMPR group configuration (relevant for OBD related projects) has been extended with the AUTOSAR 4.3 literals</p> <ul style="list-style-type: none"> <li>&gt; DEM_IUMPR_AFRI1</li> <li>&gt; DEM_IUMPR_AFRI2</li> <li>&gt; DEM_IUMPR_PF1</li> <li>&gt; DEM_IUMPR_PF2</li> </ul>	FEAT-3572
Extension	<p>The upstream mapping implemented by DaVinci Configurator Pro now considers the Diagnostic Extract DiagnosticSession.jumpToBootLoader information. The information is derived to the MICROSAR Dcm EcuC configuration.</p>	FEAT-3650
Extension	<p>If a CANdela Studio CDD file is imported to DaVinci Configurator Pro, an integral datatype having a byte length of 8 is converted into a byte array with 8 bytes. This conversion is applied to DID parameters, routine signals and parameters of extended data records.</p>	FEAT-3897
Extension	<p>DaVinci Configurator Pro now considers the parameter DemDebounceCounterUnconfirmedThreshold provided by the VCC input DIAG-EcuC file.</p> <p>[[Note:The parameter will be converted to the MICROSAR parameter DemDebounceCounterPreconfirmedThreshold]].</p>	FEAT-4185

## Ethernet

Type	Description	Change ID
Extension	<p>The SoAd BSD API now also supports Integrity as operation system.</p> <div>  <b>Additional Information</b>            The BSD API of SoAd now supports Linux, QNX and Integrity         </div>	FEAT-2971
Extension	The precision of ARP timeout calculation implemented by the TcpIp module has been improved and is now defined by the MainFunction cycle time. Previously the granularity was fixed to 1s.	FEAT-3234
Extension	<p>DoIP now supports IPv4 and IPv6 in parallel and thus transmits vehicle announcements with both IP versions if configured.</p> <p>If IPv6 socket connections are configured accordingly, the IPv6 vehicle announcement is sent immediately with the link-local address. Once an IP address has been assigned additional vehicle announcements can be send with the dynamically assigned address.</p>	FEAT-3259
Extension	The BSD socket API of SoAd considers the frame priorities according to IEEE 802.1Q-2011, which are defined by the data definition.	FEAT-3755
Information	<p>The most common MICROSAR Ethernet stack modules will be available as SafeBSW modules for projects up to ASIL B until end of 2018.</p> <div>  <b>Additional Information</b>            ASIL B can be provided for the following BSW modules: Eth, EthIf, EthSm, UdpNm, EthTSyn, TcpIp (IP v4), Soad, DoIP, Sd.            Additional modules and use-cases can be provided on request.         </div>	FEAT-2469 FEAT-2463 FEAT-2521 FEAT-2452 FEAT-2471 FEAT-2507 FEAT-2876 FEAT-2877 FEAT-3295

**IO****IO**

Type	Description	Change ID
Information	<p>A new application Note is provided that describes how a complex driver could be developed that interacts with MCAL and Rte.</p> <div> <b>Documentation in SIP</b> AN-ISC-8-1228_How_to_create_Cdd_IoHwAb.pdf</div>	FEAT-3253

**Measurement and Calibration**




Type	Description	Change ID
Extension	Using vDbg it is now possible to access internal runtime variables of StbM, CanTSyn and FrTSyn using Xcp or Vx1000.	FEAT-3629




**Memory**

Type	Description	Change ID
Extension	The Fee (Standard) now supports asynchronous cancel operations of underlying FIs module.	STORY-6501

**Runtime Environment**


Type	Description	Change ID
Extension	SafeBSW release of SomeIpXf for projects up to ASIL D.	FEAT-2508
Extension	Release and feature completion of the SomeIpTp module introduction: <ul style="list-style-type: none"><li>&gt; SomeIpXf based communication using the SomeIpTp is now supported by the Rte for S/R and C/S communication.</li><li>&gt; QM release of the SomeIpTp component will be provided in Q4 2018</li></ul>	FEAT-2543
Extension	Rte mode users can now be distributed across multiple cores and BSW partitions.	FEAT-2544

Type	Description	Change ID
	 <b>Additional Information</b> Modes are provided by multiple service SWCs such as EcuM, WdgM or Dem.	
Extension	<p>The Rte now supports n:1 port connection for external signal based communication. In the past only intra ECU communication has been supported with n:1 relations.</p> <p>With this feature it is now possible to connect ports of multiple SWCs to the same delegation port and finally map to one SystemSignal.</p>  <b>Additional Information</b> Some features such as TxConfirmation are not supported with external n:1 port connections.	FEAT-2747
Extension	<p>If the Com module returns COM_SERVICE_NOT_AVAILABLE because the service is currently not available, the Rte now returns RTE_E_COM_STOPPED to the calling SWC runnable.</p> <ul style="list-style-type: none"> <li>&gt; Applicable for inter ECU communication</li> <li>&gt; Based on SWS_Rte_06830</li> </ul>	FEAT-3534
Extension	<p>The Rte now allows to configure the implementation way (macro or function) of exclusive areas in a module specific way. Previously this has been a global option only.</p> <p>This feature allows fine tuning of the implementation to optimize runtime (by using the macro implementation where possible).</p>  <b>Documentation in SIP</b> The implementation is defined by the parameter Rte/RteBswModuleInstance/RteBswExclusiveAreaImpl/RteExclusiveAreaGenerateFunction.	FEAT-3762
Extension	The Rte now supports the fan-out for E2E protected signals using E2eXf with Profile 4,5,6 and 7.	FEAT-3767
Extension	The Rte now supports scalling in combination with ComXf transformer. However, the supported use-cases are yet limited and will be extended in the next releases.	FEAT-4115



Type	Description	Change ID
	<p>Currently scaling is supported if:</p> <ul style="list-style-type: none"> <li>&gt; the record has one level of primitive elements</li> <li>&gt; explicit sender-receiver communication is used</li> </ul>	
Information	<p>The Rte generator now operates with a 64bit implementation. This allows loading of larger ECU configurations.</p> <div>  <p><b>Additional Information</b></p> <p>To make use of the Rte, update the latest version of the external component setup (version &gt;= 2.10) must be installed.</p> </div> <div>  <p><b>Additional Information</b></p> <p><a href="#">Download Center - DaVinci External Component Setup</a></p> </div>	FEAT-3327
Information	<p>The generation time of the Rte has been improved. The optimization applies when calling the generation via command line and no changes to Rte related configuration elements have been made since the last code generation.</p> <div>  <p><b>Additional Information</b></p> <p>In this case the Rte generator detects that no changes have been made and skips parts of the code generation. A similar optimization was already implemented when code is generated via user interface.</p> </div>	FEAT-3351
Information	<p>The Rte now implements a check, that ensures that the init values of different receivers have the same value. A new info is now shown when different receivers use different init values. This may be a problem as the Rte only uses a single init value for all receivers.</p> <p>The info indicates the init value that is used in the generated code.</p>	FEAT-3499

## Security

Type	Description	Change ID
Breaking Change	<p>A new BSW module vSecPrim has been introduced that replaces the previous SecMod component. The module includes the security algorithm primitives that are e.g. used by Crypto (Sw) module.</p> <ul style="list-style-type: none"> <li>&gt; The APIs and file names of primitives have not been changed. This applies also to the primitive implementation</li> <li>&gt; A new module (vSecPrim) including BSWMD file and generator has been introduced: vSecPrim <ul style="list-style-type: none"> <li>&gt; Using this module it is possible to activate or deactivate the required algorithms.</li> </ul> </li> <li>&gt; The delivery now includes all algorithms and no longer the subset that is required for a specific delivery.</li> </ul> <p><b>Migration notes for existing projects:</b></p> <p>If you use Cry (Sw) or Crypto (Sw), the following steps are required:</p> <ul style="list-style-type: none"> <li>&gt; Enable the module vSecPrim in DaVinci Configurator Pro</li> <li>&gt; Configure the required security primitives for your project. <ul style="list-style-type: none"> <li>&gt; Validators help synchronizing the algorithm selection with the Cry/Crypto requirements</li> </ul> </li> <li>&gt; Generate code to configure vSecPrim</li> <li>&gt; As the location of the files in the SIP has changed (from SecMod -&gt; vSecPrim) the build environment may have to be adapted.</li> </ul>	FEAT-3330
Extension	The SHE key update protocol implemented by Crypto (Sw) now provides support for UID, FID and counter.	FEAT-3157
Extension	<p>vFvM has been extended with new functionalities for the JASPAR FvM algorithm:</p> <ul style="list-style-type: none"> <li>&gt; The trip counter can now be stored in NvM and loaded during ECU initialization</li> <li>&gt; Before a synchronization message is received vFvM now behaves as follows. This is independent from the start acceptance window: <ul style="list-style-type: none"> <li>&gt; Trip- and Reset counter are set to the initial value</li> <li>&gt; Message Counter is initially set to zero and incremented afterwards</li> </ul> </li> <li>&gt; Trip counter flag support: <ul style="list-style-type: none"> <li>&gt; The trip flag is the least significant bit of the trip counter. It is used for synchronizing the freshness</li> </ul> </li> </ul>	FEAT-3313



Type	Description	Change ID
	<p>value.</p> <ul style="list-style-type: none"> <li>&gt; The flag is configured using the parameter FvMTripFlagBitSize.</li> </ul>	
Extension	<p>The existing SecOC development mode now also ignores FvM errors and Csm errors such as an empty KeySlot.</p> <p>This allows end of line tests where the key has not yet been programmed to the ECU.</p>	FEAT-3515
Extension	<p>DaVinci Configurator Pro is now able to derive the SecOC configuration based on the AUTOSAR 4.3.1 system template.</p> <ul style="list-style-type: none"> <li>&gt; Support AUTOSAR 4.3.1 SecOC configuration</li> <li>&gt; Support transmission of authenticator and payload part in two separate messages</li> </ul>	FEAT-3583
Extension	<p>The AUTOSAR 4.3 based Crypto module now supports additional algorithms:</p> <ul style="list-style-type: none"> <li>&gt; ECDSA 384</li> <li>&gt; AES 256</li> </ul> <p>The algorithms are accessible using Csm and are implemented in software.</p>	FEAT-3609
Extension	<p>SecOC now provides optional callout functions that allow the application to process the security details of PDUs that have been received resp. that will be transmitted.</p> <ul style="list-style-type: none"> <li>&gt; SecOC_GenerateAuthenticationInfo()</li> <li>&gt; SecOC_VerifyAuthenticationInfo()</li> </ul> <p>The feature can be enabled by the configuration switch SecOCGeneral/SecOCEnableCustomAuthenticationAPI</p> <div>  <p><b>Documentation in SIP</b></p> <p>Additional information on the API can be found in the SecOC technical reference (TechnicalReference_SecOC.pdf).</p> </div>	FEAT-3647
Extension	<p>The Csm (AR4.3 implementation) now allows to store keys in an encrypted way in NvM.</p> <p>The module therefore now provides pre- and post job callouts which can be used for decryption a key respect-</p>	FEAT-3858



Type	Description	Change ID
	<p>ively a decrypted key can be deleted or overwritten.</p> <div>  <b>Documentation in SIP</b> </div> <p>The callout hooks are configured by the parameters:</p> <ul style="list-style-type: none"> <li>&gt; Define callout function names: <ul style="list-style-type: none"> <li>&gt; Csm/CsmCallouts/CsmCallout/CsmPreJobCalloutFunc</li> <li>&gt; Csm/CsmCallouts/CsmCallout/CsmPostJobCalloutFunc</li> </ul> </li> <li>&gt; Link job with callout function: <ul style="list-style-type: none"> <li>&gt; Csm/CsmJobs/CsmJob/CsmJobCalloutRef</li> </ul> </li> </ul>	
Extension	<p>SecOC provides a new callout function SecOCVerificationStatusCalloutWithSecuredPdu that provides the PDU payload and the freshness verify value along with the verification status.</p> <div>  <b>Documentation in SIP</b> </div> <p>Using the configuration option SecOC/SecOCRxPduProcessing/SecOCVerificationStatusWithSecuredPduPropagationMode it is possible to set when the callout is invoked:</p> <ul style="list-style-type: none"> <li>&gt; BOTH: 'True' and 'False' verification status will be propagated.</li> <li>&gt; FAILURE_ONLY: Only 'False' verification status will be propagated,</li> <li>&gt; NONE: No verification status will be propagated.</li> </ul>	FEAT-3891
Information	<p>The Crypto (Sw) implementation now provides the algorithm AES-GCM and SipHash as SafeBSW implementations for safety related projects up to ASIL D.</p>	FEAT-2447

## State Management

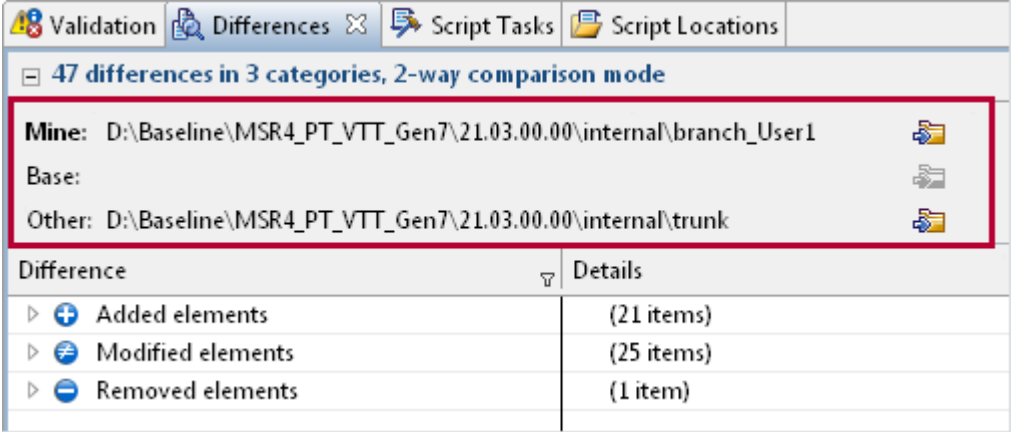
Type	Description	Change ID
Extension	EcuM now supports the startup and shutdown of more than one partitions.	FEAT-2721

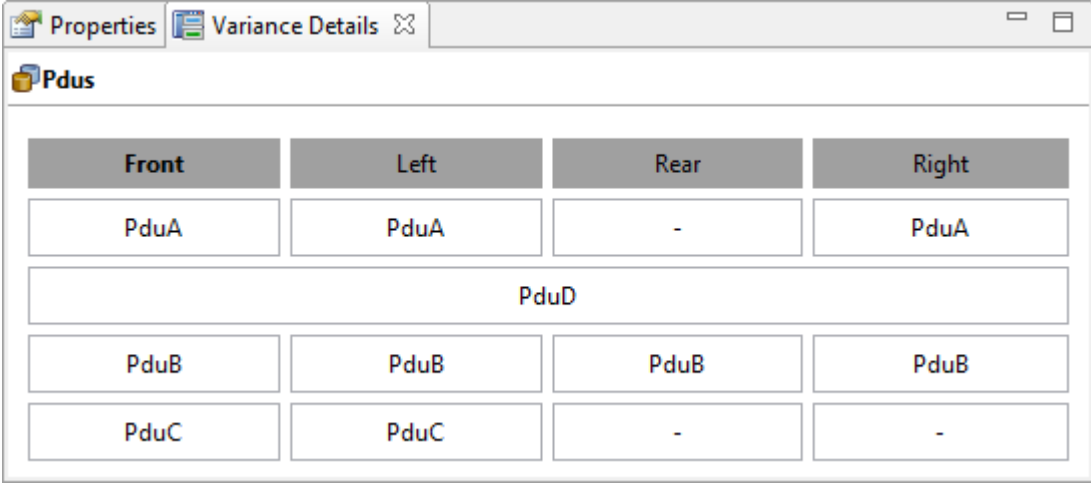
Type	Description	Change ID
	In the past only core specific initialization was supported but no partition specific initialization.	
Extension	<p>Using the BswM auto configuration assistants in DaVinci Configurator Pro it is now possible to configure a so called rapid ECU shutdown sequence for EcuM Flex use-cases. A typical use-case is the shutdown for ECU reprogramming.</p> <ul style="list-style-type: none"> <li>&gt; the shutdown is performed independent of the communication state (ComM/Nm) and ECU state.</li> <li>&gt; Nonvolatile data is stored before shutdown</li> <li>&gt; wake up events during the shutdown are ignored</li> <li>&gt; run &amp; post run requests are ignored</li> </ul> <div>  <b>Additional Information</b>  Rapid shutdown is not supported by EcuM Fix use-case. </div>	FEAT-2782
Extension	<p>For wake ups during the UP/RUN phase, EcuM currently considers communication channel wakeup sources only - as defined by AUTOSAR.</p> <p>In some use-cases this is not sufficient.</p> <p>EcuM therefore now provides the option to consider wakeup sources which have no dependency to a communication channel.</p> <div>  <b>Documentation in SIP</b>  The option can be enabled through the configuration switch EcuMGeneral/EcuMNonChannelWakeupInRun. </div>	FEAT-3268

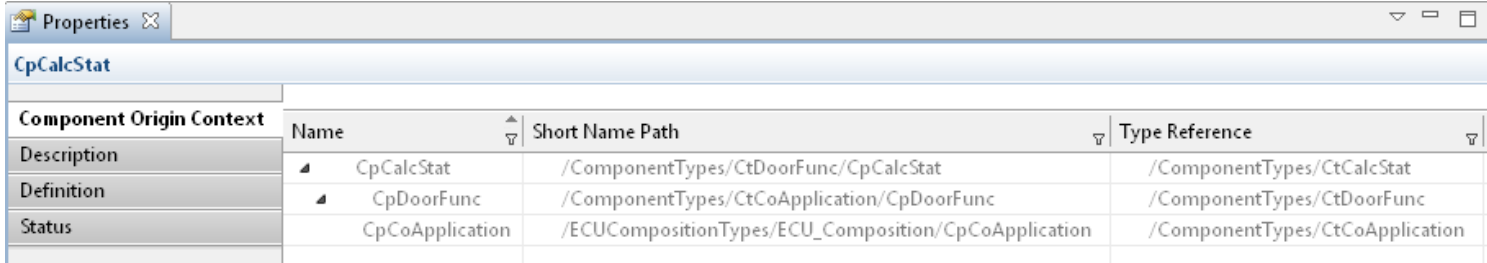
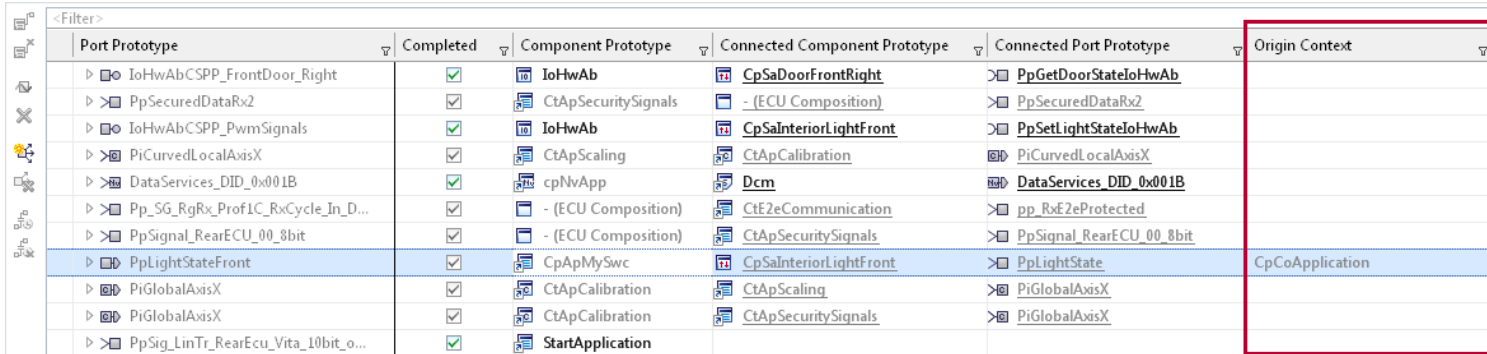
## Watchdog

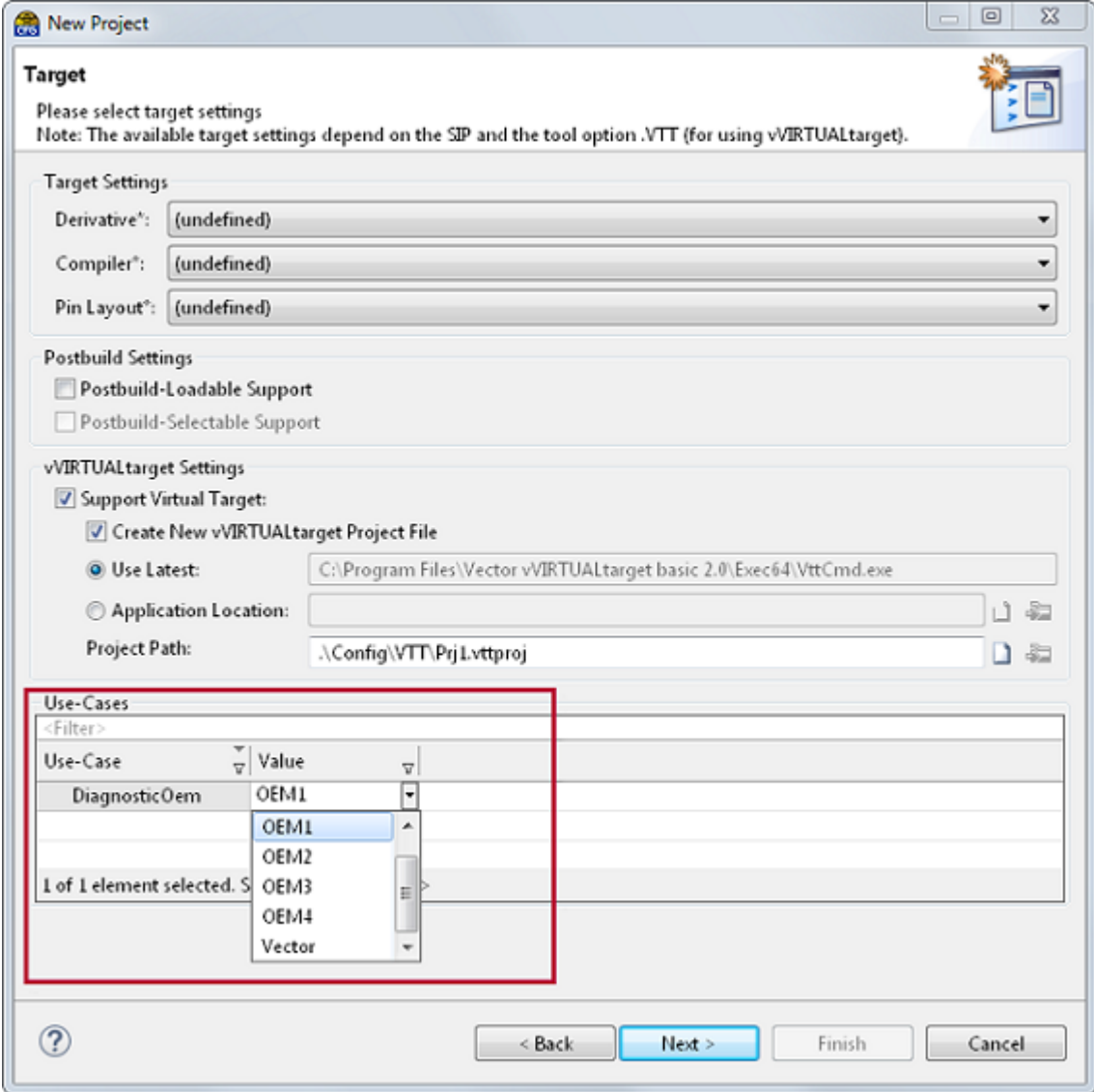
Type	Description	Change ID
Extension	WdgM now allows status reporting via callback functions (C functions), Client-Server-Ports (via Rte) and Mode-Switch-Ports (via Rte) in single and multi core usage. In previous version such configuration caused compiler and linker errors.	FEAT-3497

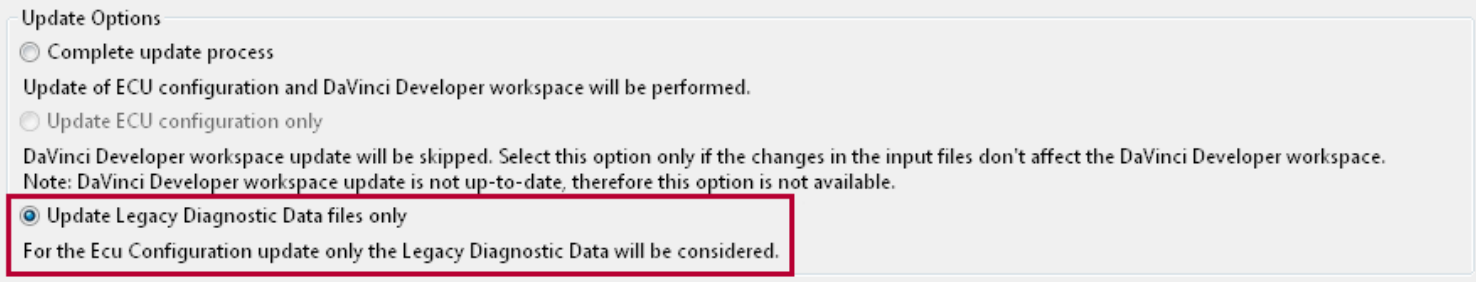
## Tooling

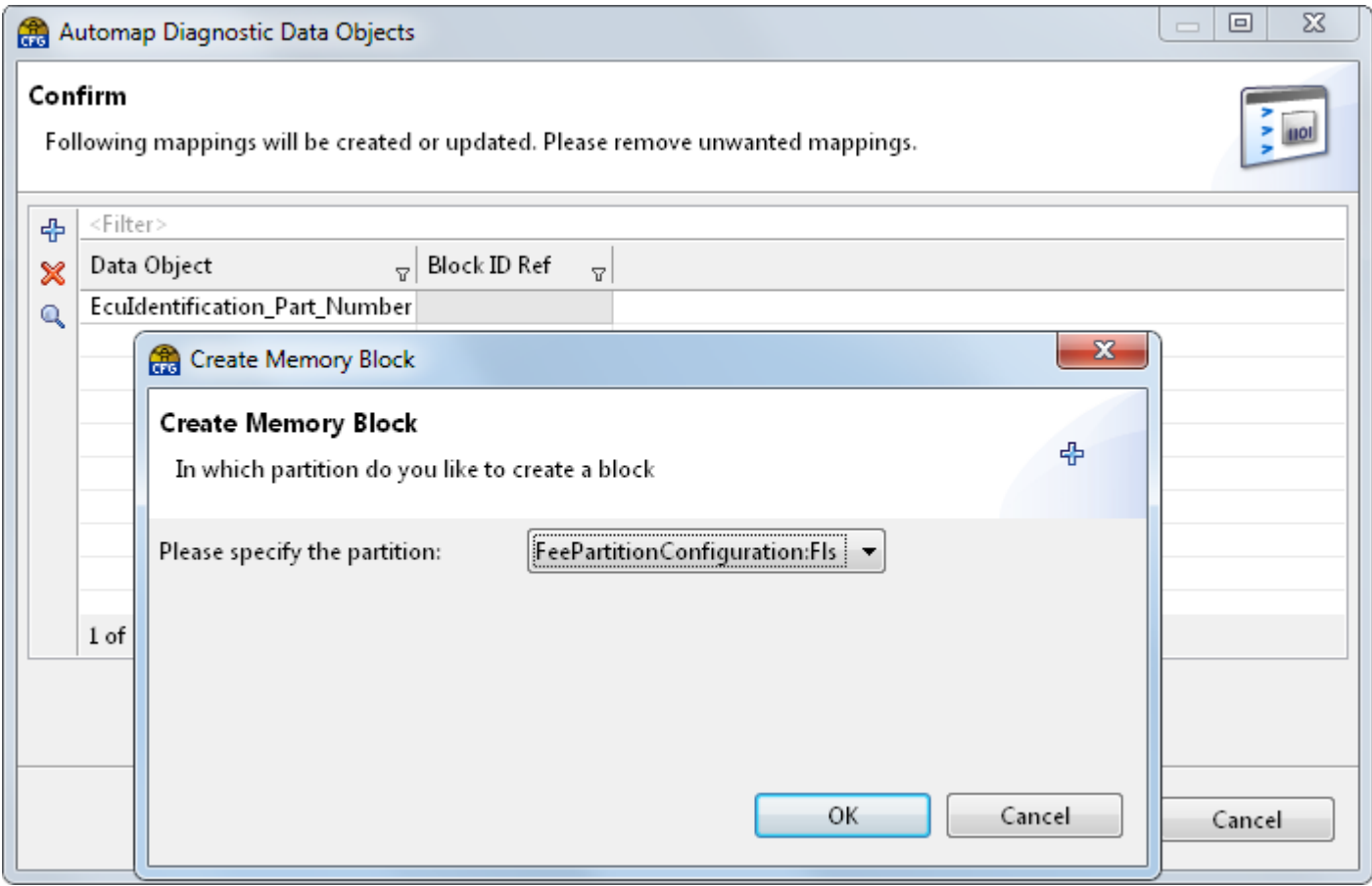
Type	Description	Change ID
Breaking Change	<p>The runtime of code generators for the SWC template file generation has been reduced for setups with multiple output folders (i.e. one folder per SWC template).</p> <p><b>Migration notes for existing projects:</b></p> <p>The Rte contract phase header files are no longer generated to the &lt;GenData&gt;\ContractPhase folder. Instead the files are now generated into the folder defined in the DaVinci Configurator Pro "SWC Templates and Contract Phase Headers" settings.</p> <p>This requires either an adaption of the project settings or a modification in the build environment to the new contract phase header location.</p>	FEAT-3352 FEAT-3351
Extension	<p>The Difference Details view of DaVinci Configurator Pro shows now the path to the projects that are used for comparison.</p> 	FEAT-2449
Extension	DaVinci Configurator Pro now allows pasting EcuC configuration items in the Mode Management use-case editors.	FEAT-2481
Extension	<p>To illustrate differences in BSW variances (MICROSAR Identity Manager) in a better way, DaVinci Configurator Pro now provides a new Variance Details View.</p> <p>The view highlights the variant specific settings for containers, parameters and data mappings and is available</p>	FEAT-2553

Type	Description	Change ID
	<p>in basic and use-case editors.</p> <p>The view can be opened in the View menu. Choose Variance Details.</p> <p>New DaVinci Configurator Pro Variance Details</p> 	
Extension	<p>The ECU Software Components Editor of DaVinci Configurator Pro shows now the origin context of a SWC prototype or port prototype. The origin context is the path to the SWC prototype in the input system extract (DaVinci Developer workspace), that may include a composition hierarchy. Available also in Component Connection Assistant and automation interface.</p>	FEAT-2857

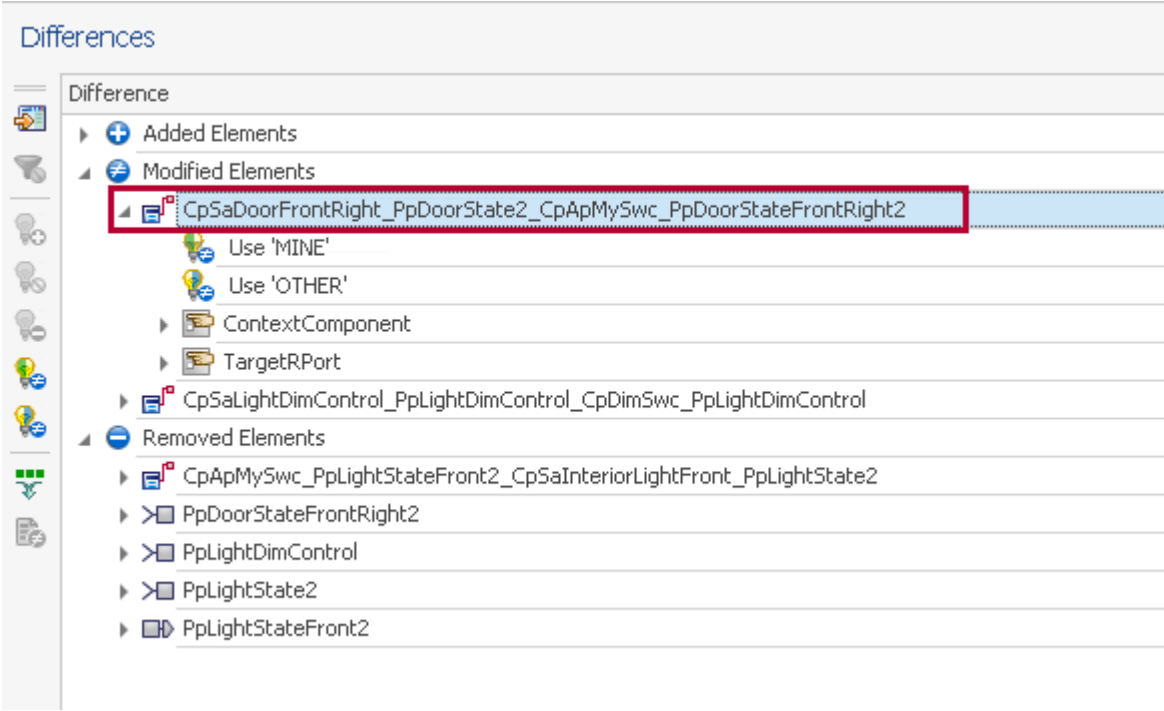
Type	Description	Change ID
	<p>Origin context in the Properties View of a SWC prototype. The tree shows the inverse parent path through the composition hierarchy</p> <div></div> <p>Origin context in the Application Ports List View of a SWC prototype. Shows the top-level composition prototype</p> <div></div>	
Extension	<p>DaVinci Configurator Pro supports now use-case specific preconfiguration. If such preconfiguration is defined in the SIP, the use-case can be selected in the New Project Assistant and in the Project Settings Editor.</p> <p>The Eparm tool (part of DaVinci Configurator Pro Option MD) supports the definition of use cases and according values to be offered for selection.</p>	FEAT-2886



Type	Description	Change ID
	<p>Use-case selection in New Project Assistant</p> 	

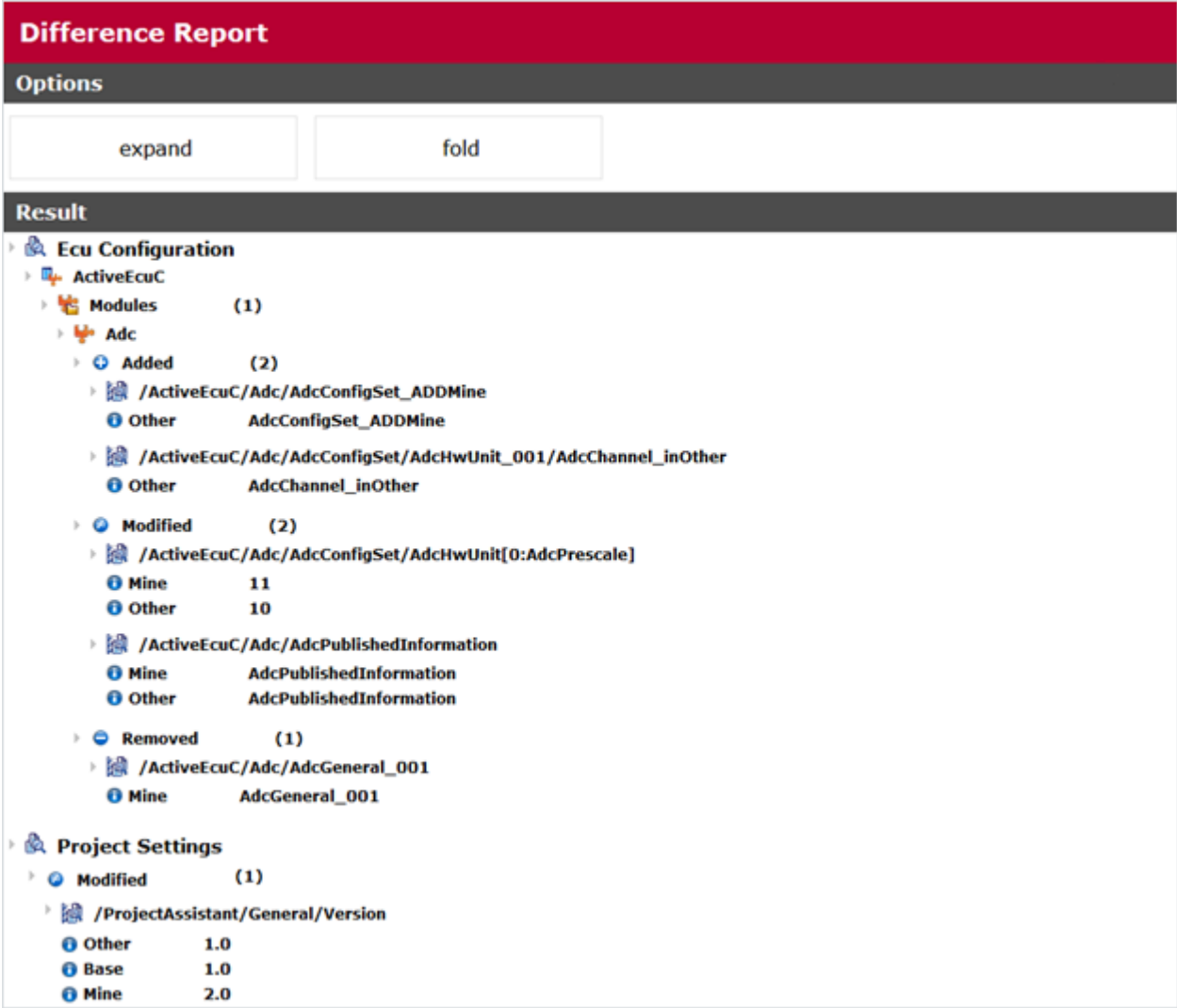
Type	Description	Change ID
Extension	<p>A preprocessing of input files (system template) with DaVinci Configurator Pro is now possible using Groovy Scripts that are known from the automation interface (option WF).</p> <p>The existing VASE Script is still supported but considered to be deprecated.</p>	FEAT-3027
Extension	In DaVinci Developer, the IsService flag can now be modified after the port interface creation.	FEAT-3339
Extension	DaVinci Configurator Pro now provides a link that allows to open the documentation folder of the SIP. The folder includes BSW related documentation such as the Technical References.	FEAT-3345
Extension	<p>DaVinci Configurator Pro supports now a selective update of diagnostic module configurations (only in combination with CDD or PDX files).</p> 	FEAT-3349
Extension	The Automap Diagnostic Data Objects Assistant of DaVinci Configurator Pro supports now the creation of new memory blocks.	FEAT-3367

Type	Description	Change ID
		
Extension	The Difference View of DaVinci Developer allows now individual merge decisions also for SWC connectors.	FEAT-3400



Type	Description	Change ID
		
Extension	<p>DaVinci Configurator Pro now raises a warning if at least one user-defined parameter is present in the configuration.</p> <p>This is vital as using the user-defined setting may overwrite relevant pre-configurations provided by e.g. validation routines or the OEM. Thus setting a parameter to user-defined may result in malfunction of the ECU or compile errors.</p>	FEAT-3426
Extension	DaVinci Configurator Pro has been improved to better cope with E2E protections defined in a DaVinci Developer 4 workspace. Includes a validation rule and solving actions for automatic correction of the ECU extract model.	FEAT-3646
Extension	<p>The DaVinci Configurator Pro automation interface has been extended with new high level functionality:</p> <ul style="list-style-type: none"> <li>&gt; An API has been introduced that allows easy definition of the order with which runnables are mapped to a task.</li> </ul>	FEAT-3945

Type	Description	Change ID
	<ul style="list-style-type: none"> <li>&gt; Simplified the possibility to complete missing SWC connections in the system extract               <ul style="list-style-type: none"> <li>&gt; Background: Some delegation ports on compositions are connected to inner atomic SWCs but do not forward the connection to delegation ports of the toplevel composition. We call these delegation ports OriginContextPorts.</li> <li>&gt; The OriginContextPort is now offered as additional info and filtering criteria for the existing selection APIs.</li> <li>&gt; A new selection API for the OriginContextPorts has been introduced.</li> </ul> </li> </ul> <div>  <b>Additional Information</b>            To make use of the powerful automation interface the option DaVinci Configurator Pro.WF is required.         </div> <div>  <b>Additional Information</b>  <a href="#">More details on DaVinci Configurator Pro and its option .WF</a> </div>	
Information	<p>The performance and browser compatibility of reports generated by DaVinci Configurator Pro has been improved for the following reports:</p> <ul style="list-style-type: none"> <li>&gt; Diff&amp;Merge Report</li> <li>&gt; Update Report</li> <li>&gt; Generation Execution Report</li> </ul>	FEAT-3545

Type	Description	Change ID
	<p>Difference Report</p> 	
Information	DaVinci Configurator Pro supports now the processing of "VAL_BLK" ApplDataTypes in input files by conversion to an uint8 array.	FEAT-3659




## Release 20

### General


Type	Description	Change ID
Information	Several MICROSAR BSW modules make use of float datatypes. The usage of a "nofloat" compiler option is therefore no longer supported.	-

### Communication

Type	Description	Change ID
Breaking Change	<p>The time synchronization stack has been extended with more AR4.3 features:</p> <ul style="list-style-type: none"><li>&gt; StbM now supports<ul style="list-style-type: none"><li>&gt; Virtual local time: Rate correction based on the network time</li><li>&gt; Pure local time base: Time base derived from a local clock / counter</li><li>&gt; New API StbM_GetTimeBaseStatus()</li><li>&gt; StbM_SetGlobalTime() can now also be used for offset time bases</li></ul></li><li>&gt; CanTSyn and FrTSyn now support the AR4.3 defined message format which is incompatible to the message format defined by AR4.2.2.<ul style="list-style-type: none"><li>&gt; The message format is configurable to support OEMs that require AR4.2.2 message format on the network.</li></ul></li><li>&gt; EthTsyn is now able to detect Master / Slave conflicts.<ul style="list-style-type: none"><li>&gt; The slave node reports runtime DET error if SYNC messages from multiple master nodes are received.</li><li>&gt; The slave node detects the first Master and ignores messages from other master nodes.</li><li>&gt; The master node reports a runtime DET error if SYNC messages are received from other master nodes.</li></ul></li></ul> <p><b>Migration notes for existing projects:</b></p> <p>If the OEM requires time synchronization according AR4.2.2 (message format) set the configuration parameter CanTSynMessageCompatibility resp. FrTSynMessageCompatibility to TRUE.</p>	FEAT-2461


Type	Description	Change ID
	 <b>Additional Information</b> Typically Vector will preconfigure this item according to the OEM the SIP has been ordered for.	
Breaking Change	<p>The MICROSAR COM Stack is now able to handle PDU fan-outs in the PduR. This reduces resource consumption in the Com module as the signals exists only once even if being transmitted on multiple networks.</p> <p>The PDU fan-out must be modelled in the system template.</p>  <b>Important Information</b> In order to realize this feature the naming rules of the upstream mapping have been changed. <ul style="list-style-type: none"> <li>&gt; Names of PDUs and signals that are created by DaVinci Configurator Pro based on the System Template are changed during the first database update.</li> <li>&gt; Due to internal improvement in our tooling there have also been other changes with respect to names that are created by the upstream mapping (System Extract -&gt; EcuC).</li> </ul> <p>Note: This change affects all elements even if no fan-out or fan-in is used.</p>  <b>The implementation has some limitations</b> <ul style="list-style-type: none"> <li>&gt; If a Com I-PDU Group is assigned only to PDUs with a fan-out or fan-in relation the BswM is currently not able to switch these PDU groups based on the rules created by the auto configuration. <ul style="list-style-type: none"> <li>&gt; This is a absolutely untypical use-case and we are currently not aware that such databases are provided by OEMs.</li> <li>&gt; Enabling and disabling these PDU Groups have to be done manually in the BswM configuration as otherwise the PDUs will not be processed. Future release will improve this handling.</li> </ul> </li> </ul> <p><b>Migration notes for existing projects:</b></p> <p>Due to the fan-out/fan-in realization and due to the associated renaming of items we have a high impact on the ECUC configuration:</p>	FEAT-3109

Type	Description	Change ID
	<ul style="list-style-type: none"> <li>&gt; We recommend a ECUC database update when updating to the new SIP. After the update rework may be required. The extend of the rework is expected to be low as an automatic conversion has been implemented.</li> <li>&gt; After the update, delete all Com*Notification parameters with the value "Rte_COMCbk*". Otherwise the Rte will not update the notifications properly. Once this is done the Rte update mechanism will work properly again for future updates. <ul style="list-style-type: none"> <li>&gt; You can use this as a search query in the Find View using the query "Rte_COMCbk*" to see if you have not missed any parameter.</li> <li>&gt; Use the find view to jump to the related item and use the multi select feature in grid view to remove all parameter instances with such a value.</li> <li>&gt; The affected parameters are /MICROSAR/Com/ComConfig/ComSignal(Group)/Com*Notification</li> <li>&gt; The correct Com*Notification values will automatically written within the next calculation phase of the Rte</li> </ul> </li> <li>&gt; Check connection of Service Ports to your SWCs as some names have changed (especially for ComM)</li> <li>&gt; Existing configuration items that are created by the upstream mapping will be removed and new items will be added during the first database update. Vector has implemented a set of rules that take over most configuration settings. <ul style="list-style-type: none"> <li>&gt; Manual changes to the global PDU in the EcuC module cannot be migrated. If manual changes have been done to the global PDU collection (we expect that this is a rather untypical use-case) these must be redone.</li> <li>&gt; As the new items have changed names, symbolic name values that are used in the application code have to be adapted. SWCs are not affected as the Rte handles the change.</li> </ul> </li> <li>&gt; The global PDU in the EcuC module has been split. In the past there was one global PDU that was used for several BSW module interactions (e.g. CanIf to PduR and PduR to Com). Now there is one global PDU for each BSW module interaction (e.g. one PDU for CanIf to PduR and another PDU for PduR to Com). <ul style="list-style-type: none"> <li>&gt; This change allows more freedom in the global PDU settings but requires selecting the correct global PDU instance when configuring global PDU references manual (e.g. when using complex drivers).</li> </ul> </li> </ul>	
Breaking Change	<p>The MICROSAR specific tracing feature has been removed from the CAN stack (&lt;MSN&gt;_ENABLE_TRACING). The feature is not defined by AUTOSAR and deprecated since some time now.</p> <p><b>Migration notes for existing projects:</b></p>	FEAT-3167


Type	Description	Change ID
	Extend the code individually in case runtime measurement or tracing is required. This is possible since typically source code is delivered.	
Breaking Change	<p>The Meta Data handling for Tx CAN PDUs now also covers the CAN-FD and extended CAN-ID flags and now have 32bits instead of 29bit as before.</p> <p><b>Migration notes for existing projects:</b></p> <p>To continue to have the old behavior the CanIfTxPduCfg/CanIfTxPduIdMask must typically be extended to 32bits and the upper 3 bits have to be masked with 1. If the mask is not extended the meta data will be used to define the Tx message with respect to CAN identifier format (standard/extended) and CAN2.0/CAN-FD message type.</p> <div>  <p><b>Additional Information</b></p> <p>Please Note:</p> <ul style="list-style-type: none"> <li>&gt; The change does not affect standard CAN PDU Tx objects handling fixed and static PDU ID. It is relevant for J1939 PDUs and specific gateway PDU configurations where the CAN ID can be set dynamically by the upper layer.</li> <li>&gt; MICROSAR modules will provide solving actions to correct the settings to the new mask. A rework is mainly required if the setting has been done by hand in the past.</li> </ul> </div>	FEAT-3173
Breaking Change	<p>The state machine of the E2E library has been updated to comply with AUTOSAR 4.2.2</p> <ul style="list-style-type: none"> <li>&gt; Introduction of a new status E2E_P_NONEWDATA</li> <li>&gt; Check status E2E_P_NONEWDATA was formerly mapped to state machine status E2E_P_REPEATED</li> </ul> <p><b>Migration notes for existing projects:</b></p> <p>Check if and how the new check (E2E_P_NONEWDATA) status result shall be handled by the application.</p>	FEAT-3401
Extension	The Com module and the Rte now support signals with the (application sided) datatype float32 and float64.	FEAT-2680
Extension	The IpduM now supports the static PDU Layout for contained PDU as defined in AUTOSAR 4.3.1 - RfC76543.	FEAT-3072
Extension	The VASE script that optimizes the signal routing by creating description based routings (instead of standard signal routing relations) now also supports signal groups.	FEAT-3134


Type	Description	Change ID
Extension	GeneralPurposeIPdus can now also be secured using SecOC. In the past this was limited to ComIPdus. Implementation based on AR4.3.1 - RfC77428.	FEAT-3155
Information	ASIL D release of E2eXf and ComXf is now available.	FEAT-2498
Information	QM release of vIpc is now available.	FEAT-2936
Information	The runtime of the "Can over SPI" module was improved.	FEAT-3308



## Diagnostics


Type	Description	Change ID
Breaking Change	<p>FiM has been reworked to AR4.3 architecture. The FiM Technical Reference provides in chapter 3.2 (Major Changes in AUTOSAR 4.3 version of FiM ) a summary of the changes.</p> <ul style="list-style-type: none"> <li>&gt; Usage of Dem monitor status bits for calculating the FID states instead of using the UDS status bits</li> <li>&gt; FIDs that should be blocked depending on an event's pending status now need to be configured separately. Previously, the pending status of any event that was connected via a normal Inhibition configuration was considered.</li> </ul> <p><b>Migration notes for existing projects:</b></p> <p>The configuration is taken over from the previous FIM version.</p> <p>In case of OBD projects, please rework your configuration:</p> <ul style="list-style-type: none"> <li>&gt; Set the container structure FimInhibitionConfiguration with the parameter FimInhInhibitionMask to FIM_PENDING for all IUMPER ports.</li> </ul>	FEAT-2745
Breaking Change	<div>  <div> <p><b>Important Information</b></p> <p>Only relevant for diagnostic extract (DEXT) users that were using th R20S2 development (sprint) delivery.</p> </div> </div> <p>The names of diagnostic entities such as DTCs, DIDs and related datatypes derived from DEXT had been</p>	FEAT-3109




Type	Description	Change ID
	<p>changed from R19 to R20S2 by accident.</p> <p>With the final R20 version the names are again in line with the rule set that was used with R19 and before.</p> <p><b>Migration notes for existing projects:</b></p> <p>Only relevant if DEXT has been used in combination with a R20S2 (or later) sprint delivery:</p> <ul style="list-style-type: none"> <li>&gt; Adapt the port mapping for DCM and DEM ports according to the new names and datatypes</li> <li>&gt; If Dem and Dcm are used without Rte APIs, the interface has to be adapted.</li> </ul> <p>In general the new names are the old names without the added hash values.</p> <p>The new names are now equal to the names that were derived from the DEXT with R19 deliveries.</p>	
Extension	<p>The diagnostic extract workflow supports the automatic connection between SWCs and Dem/Dcm ports. This feature is now available for:</p> <ul style="list-style-type: none"> <li>&gt; DCM IO control ports</li> <li>&gt; DEM freeze frame data ports (require implementation data types on SWC)</li> <li>&gt; DEM event monitor and info ports.</li> </ul>	FEAT-2514
Extension	<p>Release of Dcm S/R communication as it was introduced by FEAT-371, FEAT-1899 and FEAT-2962. This includes the usage of vDiagXf.</p> <div>  <p><b>Documentation in SIP</b></p> <p>Usage of the DiagXf in combination with Dcm is described in AN-ISC-8-1218_Atomic_Dcm_S-R_Interfaces_with_Diagnostic_Transformer.pdf that is delivered as part of the SIP.</p> </div>	FEAT-2563
Extension	<p>A new SWC vDem42 is provided that is able to wrap some of the AR4.3 specific DEM APIs. The module is intended to serve as wrapper layer in case a SWC expects AR4.2 DEM interfaces that are no longer provided by the new AR4.3 DEM.</p> <p>The following APIs are wrapped:</p> <ul style="list-style-type: none"> <li>&gt; Dem_ClearDTC</li> </ul>	FEAT-2894


Type	Description	Change ID
	<ul style="list-style-type: none"> <li>&gt; Dem_SetDTCSuppression</li> <li>&gt; Dem_GetDTCSuppression</li> <li>&gt; CallbackEventStatusChange</li> <li>&gt; GeneralCallbackEventStatusChange</li> <li>&gt; GetEventFreezeFrameData</li> <li>&gt; GetEventFreezeFrameDataEx</li> <li>&gt; GetDTCOfEvent</li> <li>&gt; GetDebouncingOfEvent</li> <li>&gt; GetEventExtendedDataRecord</li> <li>&gt; GetEventExtendedDataRecordEx</li> <li>&gt; GetEventFailed</li> <li>&gt; GetEventTested</li> <li>&gt; GetEventStatus</li> <li>&gt; GetFaultDetectionCounter</li> <li>&gt; ApplicationErrors</li> </ul> <p>vDem42 is realized as SWC and generated by DaVinci Configurator Pro once the related module has been enabled. The module will be part of all deliveries that include the new AR4.3 based DEM.</p> <div>  <div> <b>Additional Information</b>            Quality status is QM. The component is therefore not suitable for usage in a safety partition.         </div> </div>	
Extension	The J1939Dcm now supports the new AR4.3 based Dem.	FEAT-2897
Extension	Support of PR-Ports for Dcm for: <ul style="list-style-type: none"> <li>&gt; DIDs with configured DcmDspDidRead and DcmDspDidWrite (DIDs for ReadDataByIdentifier and WriteDataByIdentifier)</li> <li>&gt; DIDs with configured DcmDspDidControl (DIDs for InputOutputControlByIdentifier)</li> </ul>	FEAT-2924

Type	Description	Change ID
	 <b>Additional Information</b> <p>In most programs the usage of PR ports is disabled to be compatible with existing projects.</p> <p>The feature can be enabled with the option DcmSenderReceiverPRPortsEnabled for all Dcm S/R interfaces.</p>	
Extension	<p>The DID signal handling now provides a backward compatibility to the combined signal structure that has been used with MICROSAR3 and older (&lt;R19) MICROSAR4 releases. The backward compatibility simplifies a takeover of existing software as the DID signal granularity can remain unchanged.</p> <p>Data identifiers to be combined to one signal only must have the CANdela Studio attribute ASR3_Legacy_Combine_DID set to 1.</p> <ul style="list-style-type: none"> <li>&gt; Without this setting signals are not combined. This is the default for AUTOSAR4 based projects.</li> <li>&gt; If set, the signals are combined to a single signal as it was done by MICROSAR3. The data type of the combined signal is a byte array.</li> </ul>  <b>Additional Information</b> <p>The attribute (ASR3_Legacy_Combine_DID) must be defined in the CDDT. So it may be necessary to contact the CDDT owner (e.g. OEM) and request the addition of the attribute.</p>	FEAT-3212
Extension	Provisioning of the Dem_SetEventAvailable port.	FEAT-3283
Information	Additional performance optimizations for diagnostic data import into DaVinci Configurator Pro.	FEAT-2470
Information	<p>Additional Services of the Dcm are now Safe and can be used in SafeBSW projects.</p> <ul style="list-style-type: none"> <li>&gt; DID Service 0x22</li> <li>&gt; Memory Services 0x23, 0x3D</li> <li>&gt; RID Services 0x31</li> </ul>	FEAT-2505

Type	Description	Change ID
	 <b>Additional Information</b> Additional services will be made safe with R21 (Q4 2018).	
Information	DiagXf, the transformer module used for S/R interaction with Dcm, is now available for projects up to ASIL D.	FEAT-2510
Information	QM release of the Dcm OBD major monitoring feature including DTR functionality.	FEAT-2572 FEAT-1724 FEAT-2635 FEAT-1723

## Ethernet

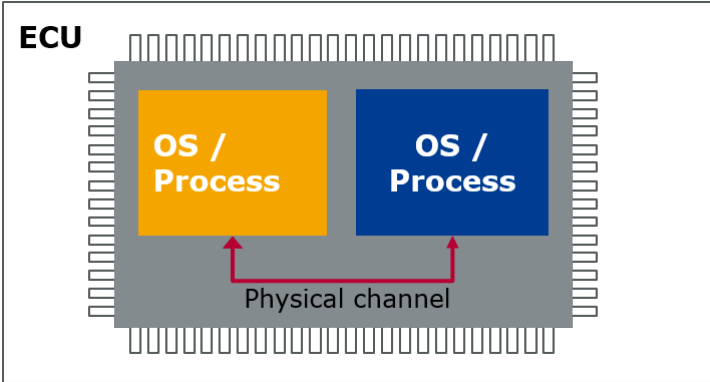
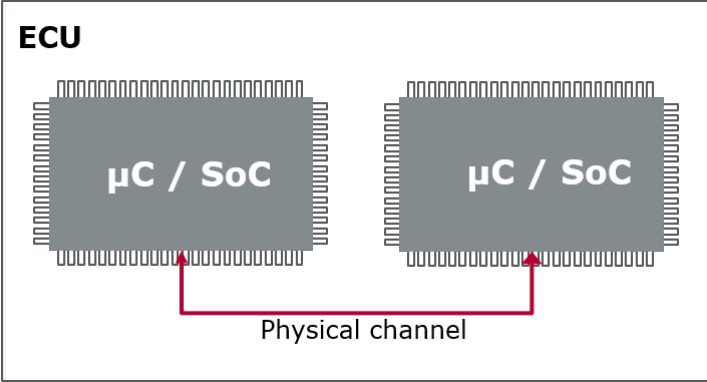
Type	Description	Change ID
Breaking Change	The support of DoIP draft standards have been discontinued: DIS/FDIS. From now on only the final standard (IS, 2012) is supported.	FEAT-3362
Extension	The SomeIpTp (Transport protocol for SOMEIP) has been realized and is available for development deliveries. Using the SomeIpTp very large data (up to 4GB) can be transmitted which cannot be handled using IP fragmentation.   <b>Important Information</b> SomeIpTp interaction with the Rte will be provided in Q2 2018. Until then SomeIpTp can be evaluated standalone without SomeIpXf and Rte interaction. We plan the release of SomeIpTp with R21 (Q4 2018).	FEAT-2543
Extension	The Soad BSD API option now supports multiple Linux ETH interfaces (e.g. VLANs).	FEAT-2697
Extension	Support "ANY" in the system template as IP address wildcard. In the ECUC the existing implementation still expects "". The upstream mapping now transfers these two representations.	FEAT-2774
Extension	The BSD API of the SoAd now supports also QNX as host operating system.	FEAT-2971

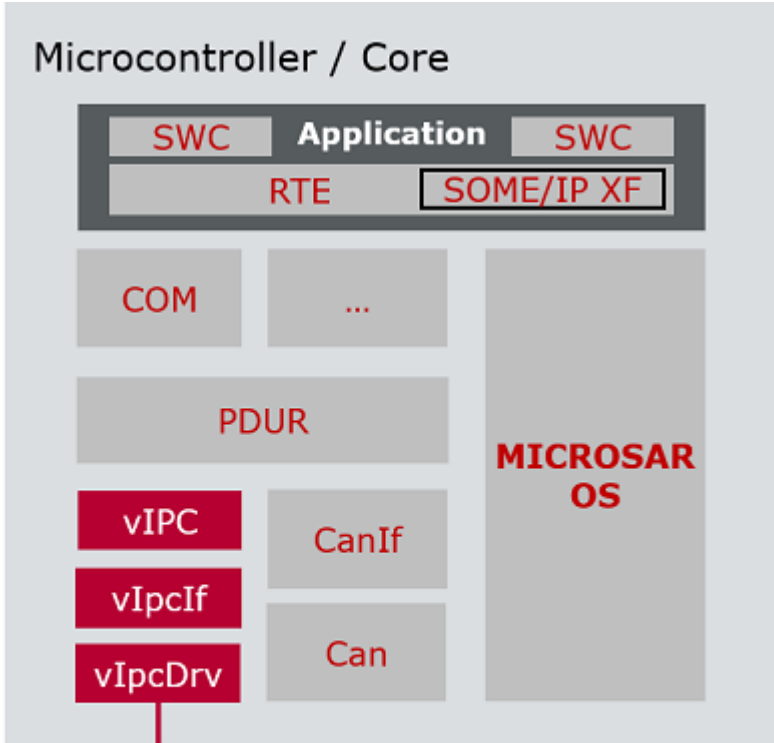
Type	Description	Change ID
Extension	<p>It is now possible to use only one SoAd Socket Connection (Multicast local address, listen-only) for receiving multicast events from multiple servers. When receiving a SD SubAck message Service Discovery sets the socket remote address to the servers unicast address.</p> <p>By deactivating the configuration option SdGeneral/SdSetRemAddrOfClientRxMulticastSoCon it is now possible to disable the SetRemoteAddress-call for multicast-endpoints per SD Instance.</p> <p>By default this new option is enabled which represents the old behavior and only the first server is configured correctly.</p>	FEAT-3326
Extension	IPv4 and IPv6 dual stack use-cases are now supported by the DaVinci Configurator Pro upstream mapping.	FEAT-3378
Extension	DaVinci Configurator Pro now provides the upstream mapping for the System Template element pduCollectionSemantics for SocketConnectionIpduIdentifier. The result is mapped to the ECUC parameter SoAdIfTriggerTransmit.	FEAT-3388
Extension	<p>The DHCP client is now able the request that same IP address after a reboot. Therefore the IP address can be included into the DHCPDISCOVER message.</p> <p>The IP address must be stored by the application in non volatile data and provided to the DHCP client after reboot.</p> <div>  <p><b>Documentation in SIP</b></p> <p>The required API is described in TechnicalReference_TcpIp.pdf chapter "DHCPv4 Requested IP Address Callout".</p> </div>	FEAT-3441
Extension	The DoIP component now supports more that 255 target addresses as defined by AUTOSAR RFC 79727.	FEAT-3472
Information	Wakeup line based activation is now released in the MICROSAR Ethernet transceiver.	FEAT-2465 FEAT-705
Information	The runtime of Sd_RxIndication() has been optimized.	FEAT-2989

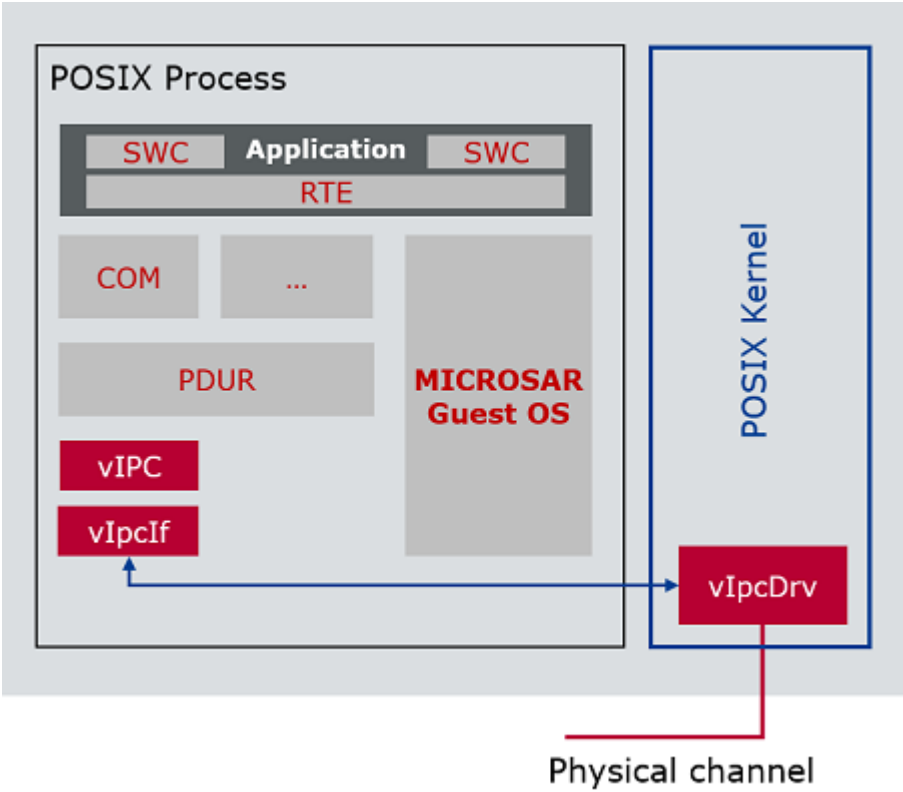
**Gateway**

Type	Description	Change ID
Information	The RAM consumption of the PduR was reduced. The reduction becomes significant if there are many low-level interface routings configured.	FEAT-3285

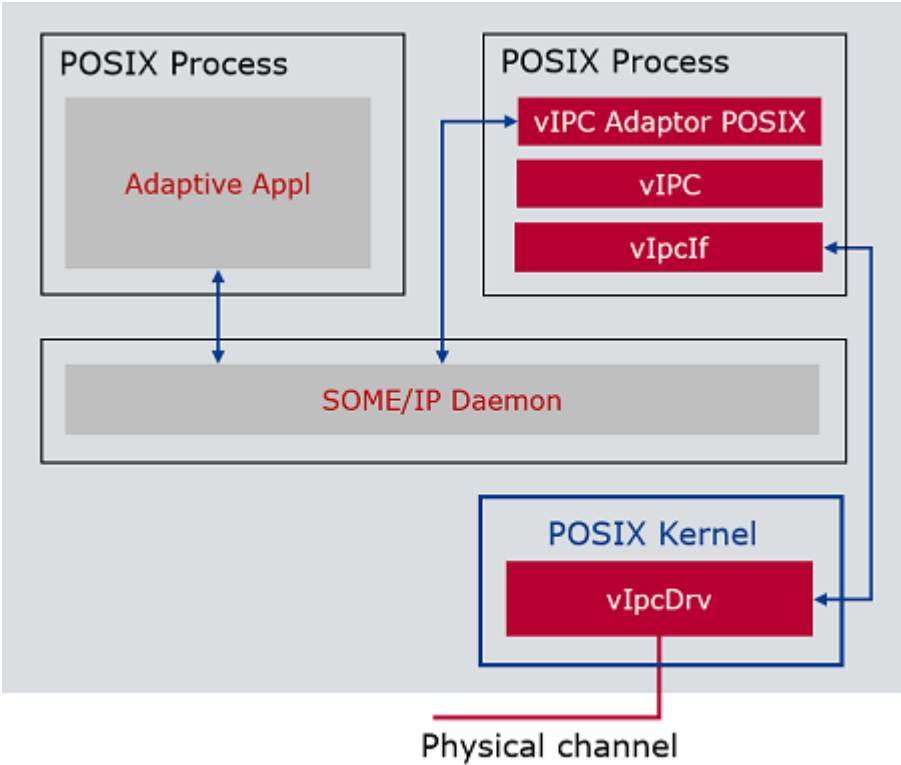

**Inter Process Communication (IPC)**

Type	Description	Change ID
Extension	<p>The MICROSAR IPC (Inter Processor Communication) has been completely reworked. A new set of modules (vIpc...) has been introduced which allows transmission and reception of data packets of configurable size between different cores of a microcontroller or different processes of a POSIX OS. With this release MICROSAR IPC supports inter core communication via shared memory for MICROSAR and POSIX based operating systems.</p> <div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;"> <p><b>ECU</b></p>  </div> <div style="text-align: center;"> <p><b>ECU</b></p>  </div> </div>	FEAT-2937


Type	Description	Change ID
	<p>MICROSAR integration:</p>  <p>The diagram illustrates the internal architecture of a Microcontroller / Core. At the top, a dark grey bar contains 'SWC', 'Application', and 'SWC'. Below this, a light grey bar contains 'RTE' and 'SOME/IP XF'. The main body of the core is divided into several sections. On the left, there is a vertical stack of components: 'COM', '...', 'PDUR', 'vIPC', 'vIpcIf', and 'vIpcDrv'. To the right of this stack are 'CanIf' and 'Can'. On the far right, a large grey block is labeled 'MICROSAR OS'. A red line originates from the 'vIpcDrv' component and points downwards to the text 'Physical channel'.</p> <p>Physical channel</p>	

Type	Description	Change ID
	<p>POSIX integration with MICROSAR:</p>  <p>The diagram illustrates the POSIX integration with MICROSAR. It shows a 'Physical channel' at the bottom, which connects to a 'vIpcDrv' (Virtual Ipc Driver) component. This component is part of the 'POSIX Kernel'. A blue arrow indicates the data flow from 'vIpcDrv' to 'vIpcIf' (Virtual Ipc Interface), which is then processed by 'vIPC' (Virtual Ipc Control) within the 'POSIX Process'. The 'POSIX Process' also contains a 'MICROSAR Guest OS' and other components like 'COM', 'PDUR', and 'RTE' (Real-time Executive). The 'Application' layer is shown at the top, flanked by 'SWC' (Software Component) blocks.</p>	



Type	Description	Change ID
	<p>Planned AdaptiveMICROSAR integration:</p>  <p>The diagram illustrates the planned AdaptiveMICROSAR integration architecture. It features a central 'SOME/IP Daemon' box. To its left is a 'POSIX Process' containing an 'Adaptive Appl' box. To its right is another 'POSIX Process' containing three stacked boxes: 'vIPC Adaptor POSIX', 'vIPC', and 'vIpcIf'. Below the daemon is a 'POSIX Kernel' box containing a 'vIpcDrv' box. A 'Physical channel' is shown at the bottom, connected to the 'vIpcDrv' box. Arrows indicate the flow of communication: a double-headed arrow between 'Adaptive Appl' and 'SOME/IP Daemon'; a single-headed arrow from 'Adaptive Appl' to 'vIPC Adaptor POSIX'; a single-headed arrow from 'vIPC Adaptor POSIX' to 'vIPC'; a single-headed arrow from 'vIPC' to 'vIpcIf'; a single-headed arrow from 'vIpcIf' to 'vIpcDrv'; and a single-headed arrow from 'vIpcDrv' to the 'Physical channel'.</p>	
	<div><b>Additional Information</b></div> <p>The solution will be extended in the near future by the following capabilities:</p> <ul style="list-style-type: none"><li>&gt; Support for communication with AdaptiveMICROSAR and MICROSAR guests in POSIX systems.</li><li>&gt; Inter-microcontroller communication via SPI and UART.</li></ul>	

## J1939

Type	Description	Change ID
Extension	<p>The upstream mapping for J1939 NAME messages has been implemented for DBC based configurations.</p> <p>In the DBC file, NAME messages are identified as NM messages with PGN 0x9300. Such messages trigger the ECUC parameter J1939NmAcceptsCommandedName to be set for the sending and receiving J1939NmNode.</p> <div><b>Additional Information</b><p>The messages (PGN 0x9300) themselves are not derived to the ECU configuration as this is not required by the embedded software.</p></div>	FEAT-2862

## Measurement and Calibration

Type	Description	Change ID
Extension	The a2I file that is generated by the Rte now includes the display format that has been defined on DataPrototype level in the SWC design.	FEAT-3159
Extension	<p>Introduction of a new API Xcp_SetStimMode() that allows modification of the STIM handling:</p> <ul style="list-style-type: none"><li>&gt; Single Shot Mode (default and the old behavior): A newly received STIM list is written when Xcp_Event is called for the corresponding Event channel and invalidated afterwards.</li><li>&gt; Continuous mode (new behavior, can be activated by Xcp_SetStimMode()): A newly received STIM list is written when Xcp_Event is called for the corresponding channel. Its state is kept so that it is written again after the Xcp_Event function is called again for the same Event channel.</li></ul>	FEAT-3275

## Runtime

Type	Description	Change ID
Extension	The Rte now supports external SWC triggers (ExternalTriggerOccurredEvent) with the RTE API Rte_Trigger for ECU internal SWC interaction.	FEAT-2476
Extension	To simplify multi core projects a new validator has been added by the Os. In the EcuC module configuration is now ensured that AUTOSAR cores have logical IDs from 0 to n and Non-AUTOSAR cores from n+1 to m when m	FEAT-2733

Type	Description	Change ID
	cores are configured in the Os.	
Extension	<p>The Os core has been extended with several new features.</p> <ul style="list-style-type: none"> <li>&gt; Support shared stack for tasks with same internal resource</li> <li>&gt; Support shared stack for non-preemptive Basic tasks</li> <li>&gt; Reference to OS_APPMODE_ANY outside OS configuration shall result in a validation error</li> <li>&gt; If missing, the Os now creates the core definition in the ECUC module automatically</li> <li>&gt; The Timing Hooks can now be used in serial production up to ASIL D</li> </ul>	<p>FEAT-2815</p> <p>FEAT-2816</p> <p>FEAT-3190</p> <p>FEAT-3204</p>
Extension	The Rte now supports inter-partition communication of NvRAM S/R ports.	FEAT-2974
Extension	The Rte supports the API Rte_IrvIWriteRef.	FEAT-3182
Extension	The Rte now recognizes the usage of Rte API types as internal datatypes that are used by SWCs internally. In the past this caused compiler errors due to type redefinitions.	FEAT-3335
Information	The Rte C/S communication use case using SomeIpXf use-case has been released. This includes the release of the E2E profiles 5 and 6 of E2eXf.	<p>FEAT-2586</p> <p>FEAT-2484</p>

## Security

Type	Description	Change ID
Extension	<p>Crypto (SW) now supports additional algorithms:</p> <ul style="list-style-type: none"> <li>&gt; RSA for generating and verifying signatures with PSS and PKCS#1 v.1.5 - Prehashing Variants: SHA-1, SHA-256</li> <li>&gt; RSA encryption and decryption with PKCS#1 v.1.5</li> <li>&gt; RSA CRT DSA Verification with PKCS#1 v.1.5 - Prehashing Variants: SHA-1 and SHA-256</li> <li>&gt; ECDSA with curve ANSIP256r1, NIST P-256, SECp256r1 for generating and verifying signatures - Prehashing Variants: None, SHA-1, SHA-256</li> <li>&gt; HMAC with SHA1 and SHA256</li> <li>&gt; ECDHE with curve ANSIP256r1 and SECp256r</li> </ul>	<p>FEAT-2735</p> <p>FEAT-3143</p>

Type	Description	Change ID
	<ul style="list-style-type: none"><li>&gt; Concatenation KDF (Nist special publication 800-56A)</li><li>&gt; Certificate installation and update according to ISO15118</li><li>&gt; CTR-DRBG using AES-128 according to NIST SP 800-90A with and without Derivation Function</li></ul>	
Extension	It is now possible to configure a secured area with a PDU. The SecOC has been extended to support such secured areas based on AUTOSAR RfC 77090.	FEAT-3073
Extension	The SecOC retry mechanism has been improved to fasten up response time. If the current freshness value did not lead to successful MAC authentication the FvM will be queried immediately for new freshness value and a new authentication attempt will be started.  Previously the second attempt was executed in the next MainFunction.	FEAT-3079
Extension	The handling of the key update in the Crypto (SW) has been improved. Key can now only be updated by itself or a configured Master Key.	FEAT-3157
Information	The runtime of Crypto (Sw) has been optimized by caching AES round keys in the driver.  The optimization can be enabled with the switch CryptoGeneral/CryptoMacPrimitives/CryptoCmacAesRoundkeyReuse	FEAT-3420

## State Management


Type	Description	Change ID
Extension	The CanNm now supports the retry of the first message transmission request as specified in the AUTOSAR 4.2.  The functionality is optional and configurable.	FEAT-3390
Extension	The CanNm settings RepeatMsgInd, NodeDetection and NodeId are now configurable per channel and support post-build selectable (IDM).	FEAT-3405
Information	The runtime of the BswM module has been improved.	FEAT-3172

## V2G

Type	Description	Change ID
Extension	The Smart Charging solution of Vector now supports the energy transfer mode WPT (Wireless Power Transfer)	FEAT-2765

Type	Description	Change ID
	according to ISO/IEC 15118 ED2 CD2. Please note: This feature is provided by a dedicated vScc add-on.	
Information	QM release of the GB/T 27930 charging component vCanCcGbt.	FEAT-2534

### vVIRTUALtarget

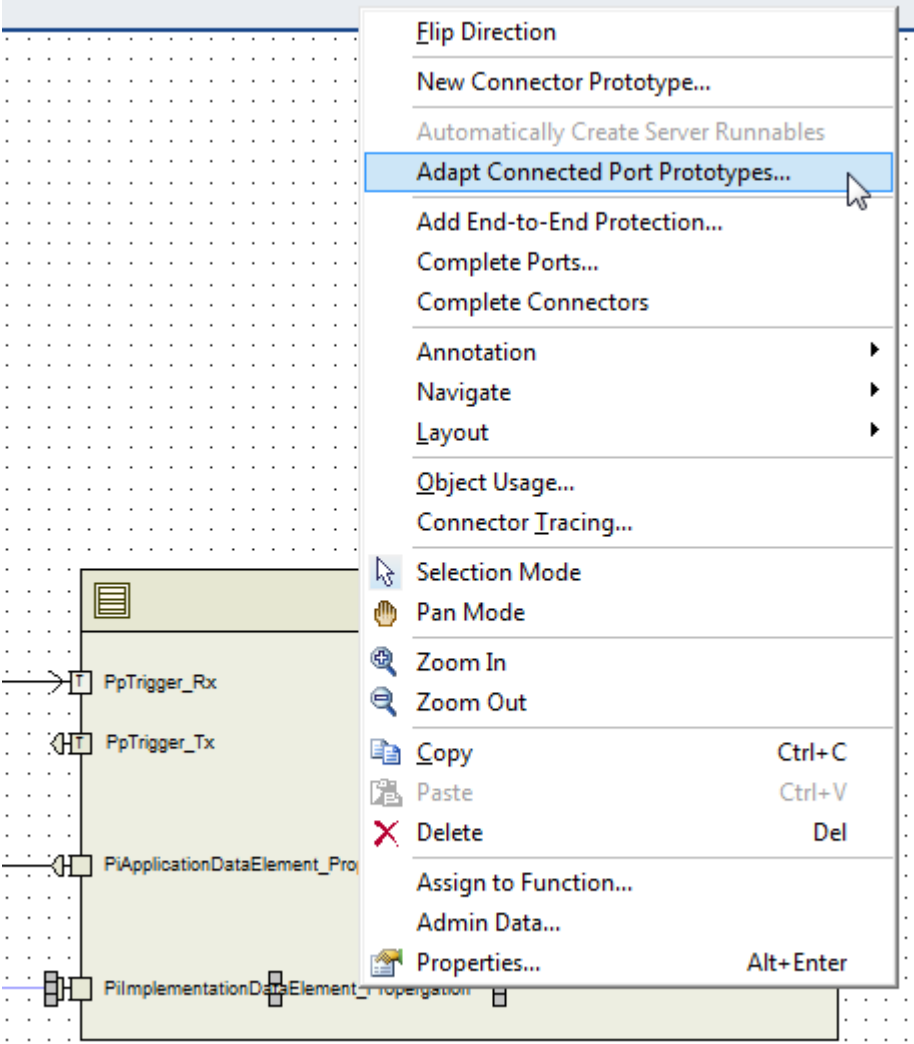
Type	Description	Change ID
Extension	<p>AUTOSAR 4.3 based Crypto (Hw) modules are now simulated by a dedicated vVIRTUALtarget.Basic Crypto module.</p> <div><b>Additional Information</b><p>The Crypto (Sw) module configuration is not considered as the algorithms are executed in software anyhow.</p></div>	FEAT-2716 FEAT-3425
Extension	<p>vVIRTUALtarget.Basic projects that are configured to generate code for both the real and the virtual target are now easier to handle:</p> <p>&gt; The API Infix as it is used by many MCAL modules is now considered by the vVIRTUALtarget.Basic BSW modules automatically. This removes the need to adapt higher layer modules to the API names.</p>	FEAT-3288

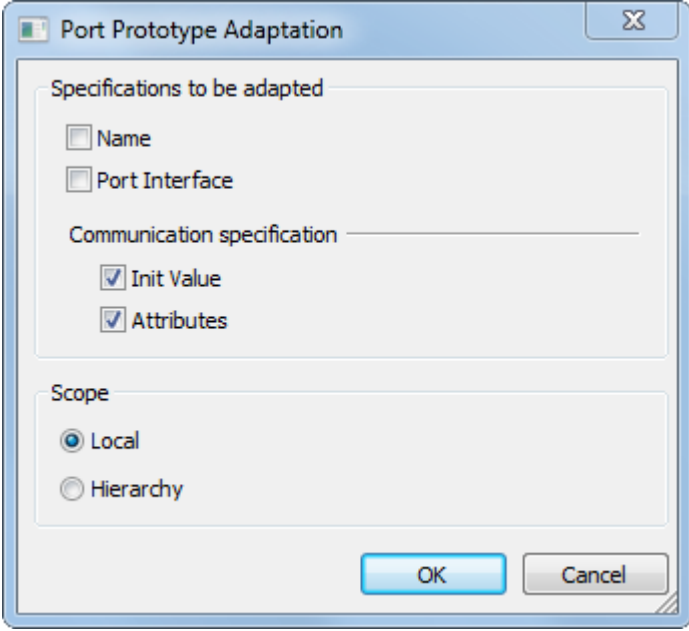
### Watchdog

Type	Description	Change ID
Information	<p>The TechnicalReference of WdgM has been updated with respect to the supported AR 4.2 functionality. Additionally, the module now also reports AR4.2.2 as supported AR version.</p> <p>No additional features have been realized.</p>	FEAT-3029

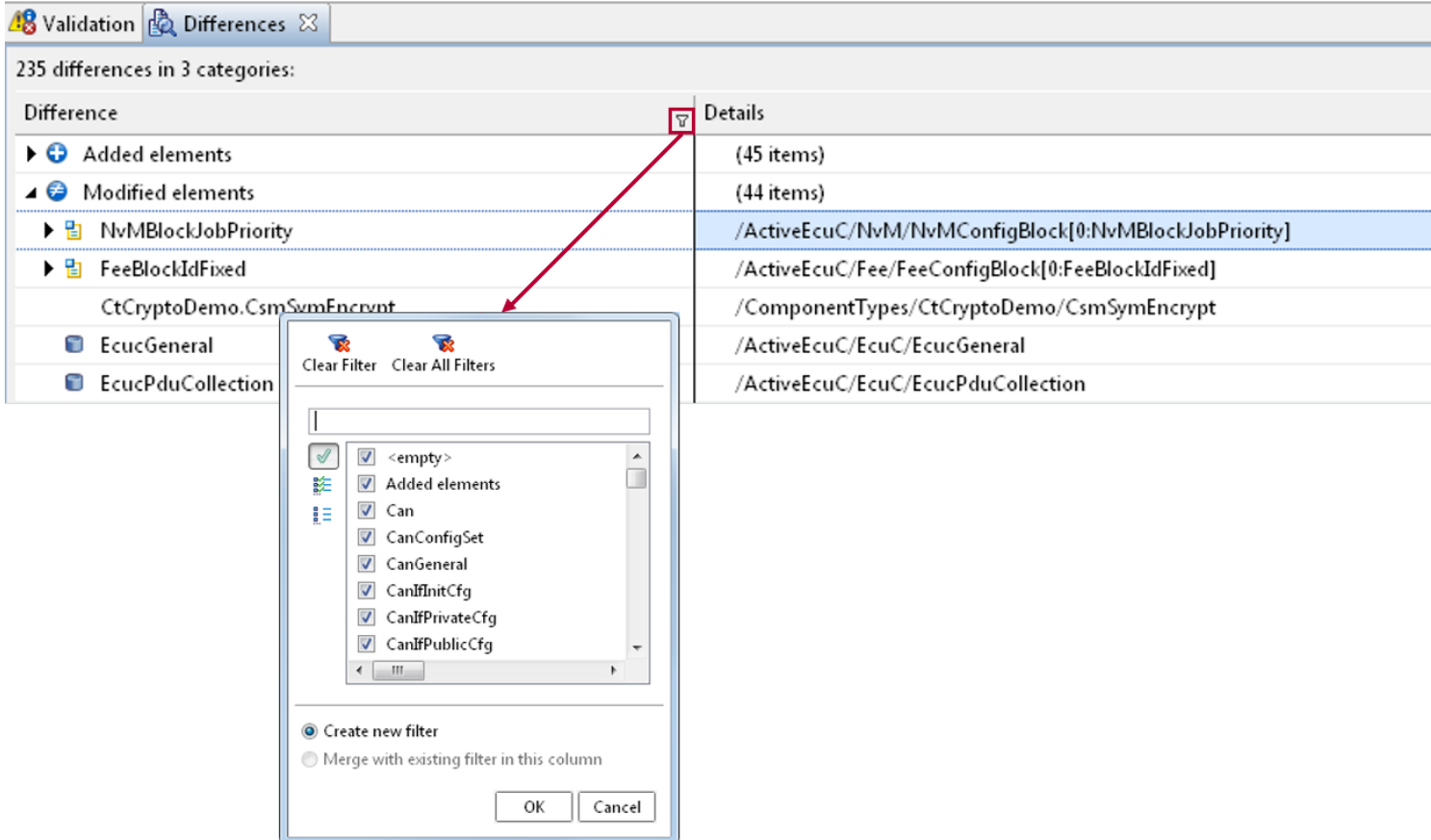
**Tooling**

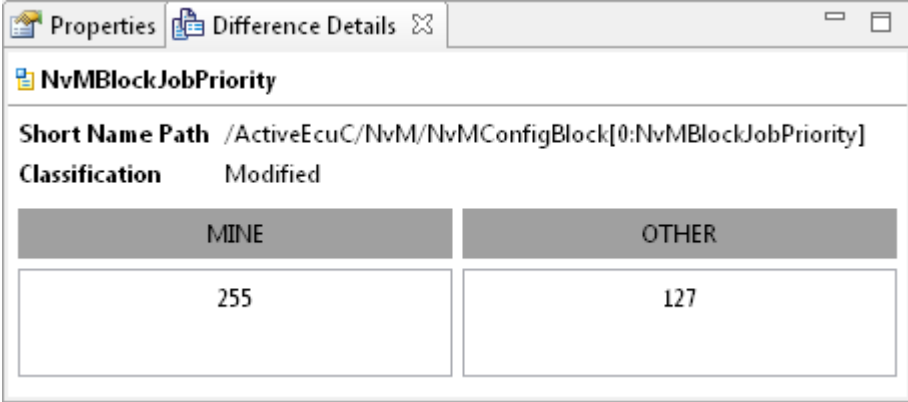
Type	Description	Change ID
Breaking Change	<p>The compatibility evaluation of constants between application- and implementation data types is now more precise and allows more use-cases:</p> <ul style="list-style-type: none"><li>&gt; Several receivers of a port can now use application- or implementation data types with physical and internal init values, respectively. In the past this triggered error RTE40248.</li><li>&gt; The DaVinci Developer feature "Adapt Connected Port Prototypes" now creates constants that are compatible with the application data types that are used for the adapted port.</li></ul>	FEAT-3133

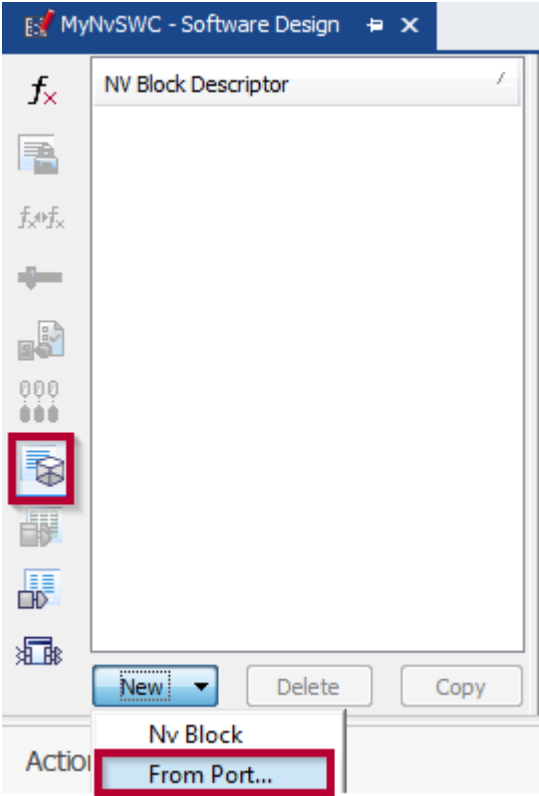
Type	Description	Change ID
	<p>DaVinci Developer port prototype context menu:</p> 	

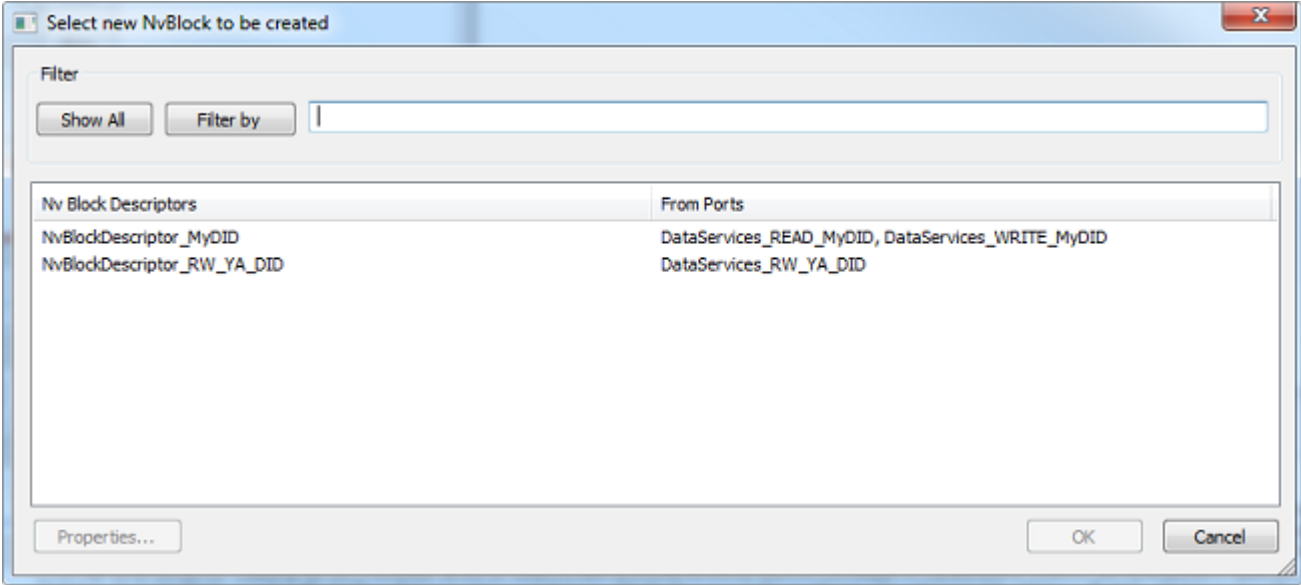
Type	Description	Change ID
	<p>Select options:</p>  <p><b>Migration notes for existing projects:</b></p> <p>The Rte now throws RTE51035 in some projects that have been working with previous Rte versions. However the configuration used to be invalid in the past. This has not been relevant in the past as the Rte did not support that feature.</p> <p>The error RTE51035 is thrown if the application / implementation init values of 1:N connected ports do not match. To resolve the issue, correct the init value configuration according to the hints provided by error message.</p>	
Extension	<p>The difference view of DaVinci Configurator Pro allows filtering of differences e.g. for</p> <ul style="list-style-type: none"> <li>&gt; show only diffs in CAN module</li> <li>&gt; show only diffs of ComIPdus</li> </ul>	FEAT-2449

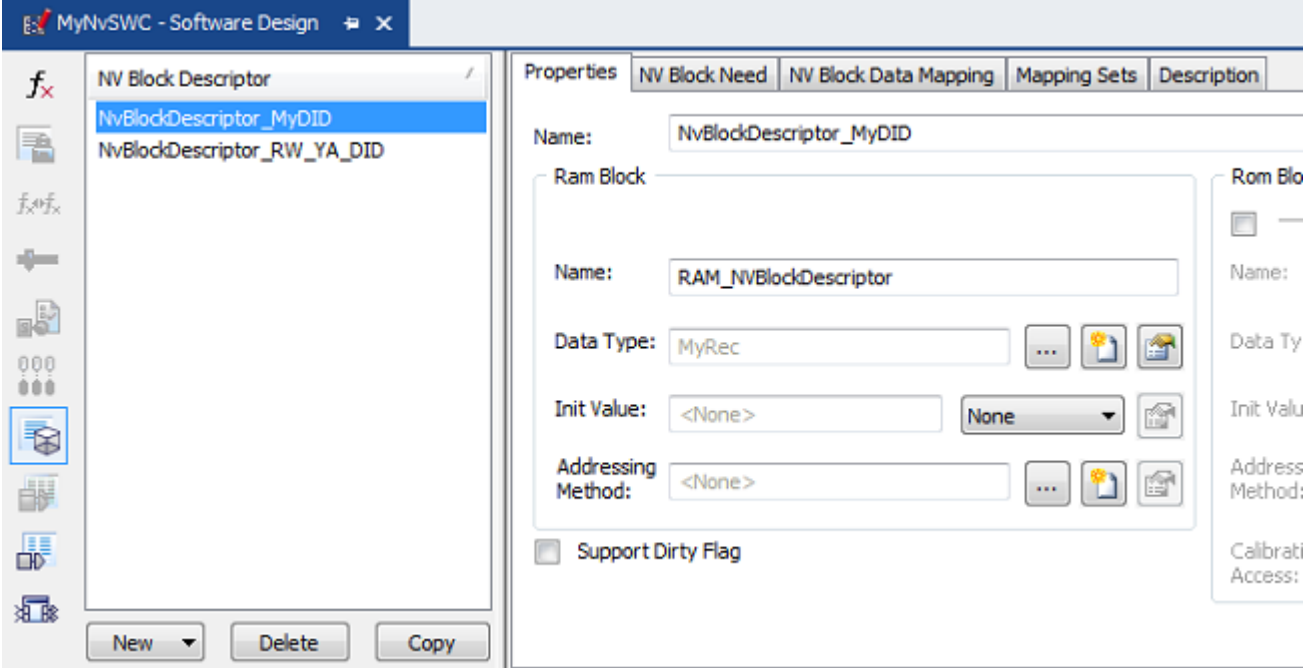
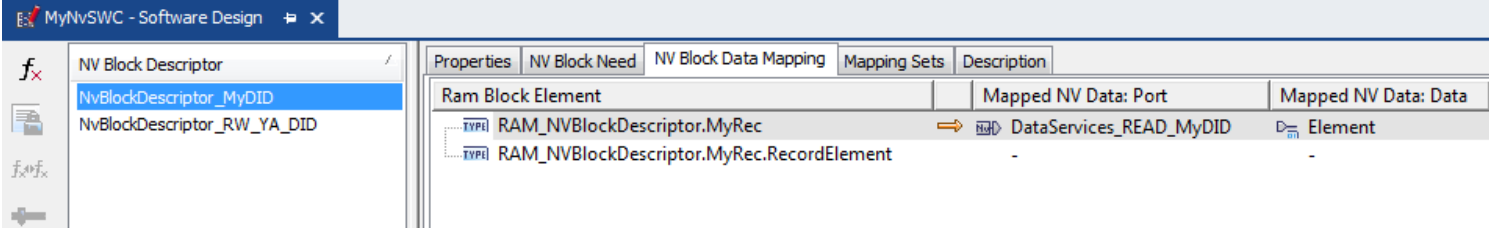



Type	Description	Change ID																
	<p>This allows to focus on relevant topics during a Diff&amp;Merge session.</p>  <p>Validation Differences</p> <p>235 differences in 3 categories:</p> <table><tr><th>Difference</th><th>Details</th></tr><tr><td>▶ + Added elements</td><td>(45 items)</td></tr><tr><td>◀ - Modified elements</td><td>(44 items)</td></tr><tr><td>▶ NvMBlockJobPriority</td><td>/ActiveEcuC/NvM/NvMConfigBlock[0:NvMBlockJobPriority]</td></tr><tr><td>▶ FeeBlockIdFixed</td><td>/ActiveEcuC/Fee/FeeConfigBlock[0:FeeBlockIdFixed]</td></tr><tr><td>CtCryptoDemo.CsmSymEncrypt</td><td>/ComponentTypes/CtCryptoDemo/CsmSymEncrypt</td></tr><tr><td>EcucGeneral</td><td>/ActiveEcuC/EcuC/EcucGeneral</td></tr><tr><td>EcucPduCollection</td><td>/ActiveEcuC/EcuC/EcucPduCollection</td></tr></table> <p>Clear Filter Clear All Filters</p> <p>Create new filter Merge with existing filter in this column</p> <p>OK Cancel</p>	Difference	Details	▶ + Added elements	(45 items)	◀ - Modified elements	(44 items)	▶ NvMBlockJobPriority	/ActiveEcuC/NvM/NvMConfigBlock[0:NvMBlockJobPriority]	▶ FeeBlockIdFixed	/ActiveEcuC/Fee/FeeConfigBlock[0:FeeBlockIdFixed]	CtCryptoDemo.CsmSymEncrypt	/ComponentTypes/CtCryptoDemo/CsmSymEncrypt	EcucGeneral	/ActiveEcuC/EcuC/EcucGeneral	EcucPduCollection	/ActiveEcuC/EcuC/EcucPduCollection	
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CtCryptoDemo.CsmSymEncrypt	/ComponentTypes/CtCryptoDemo/CsmSymEncrypt																	
EcucGeneral	/ActiveEcuC/EcuC/EcucGeneral																	
EcucPduCollection	/ActiveEcuC/EcuC/EcucPduCollection																	

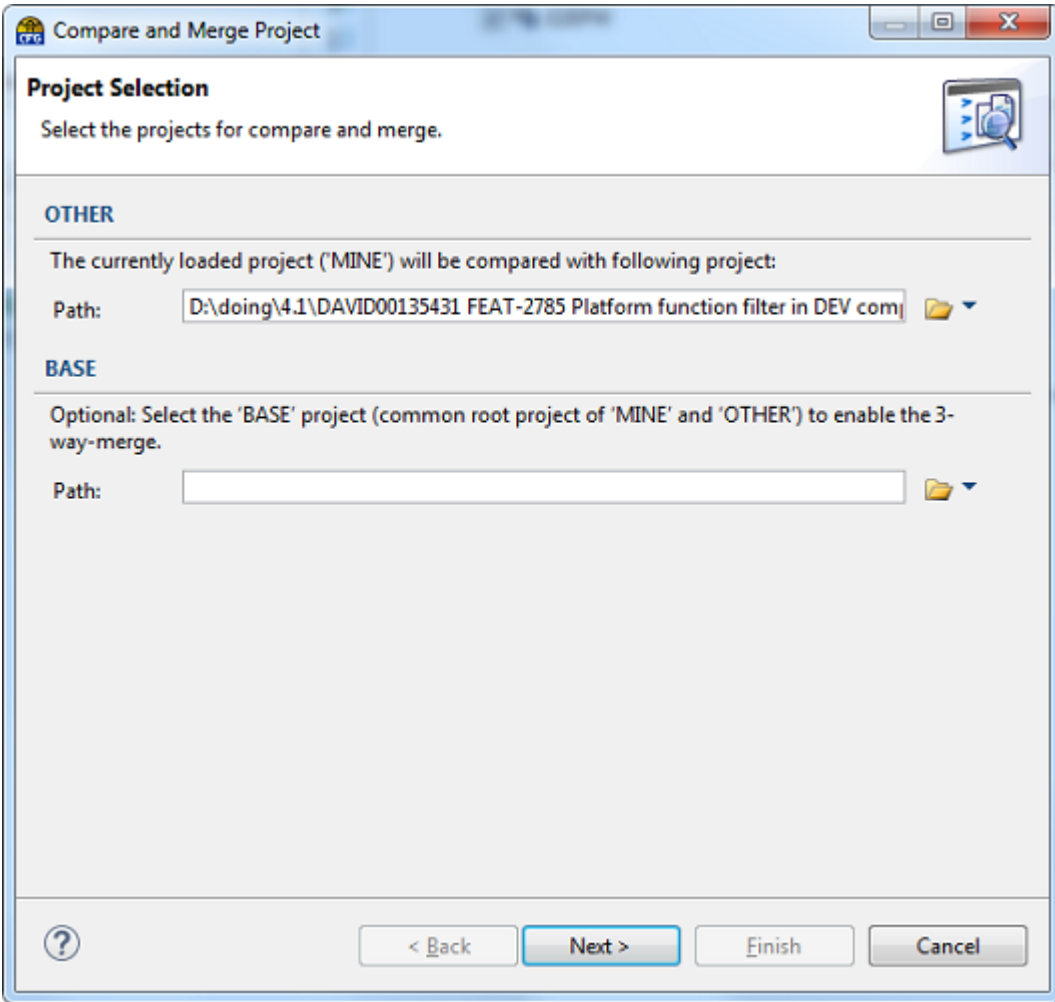
Type	Description	Change ID
	<p>Improved layout of Difference Details View:</p> 	
Extension	DaVinci Developer now provides a new convenience functionality that allows easy creation of a NvBlockSwc matching to given ports. NV Block Descriptors are automatically created and mapped to the ports.	FEAT-2485
Extension	DaVinci Developer provides a new convenience function that allows easy creation of a new NvBlock based on a existing NvPort including a 1:1 port mapping.	FEAT-2485

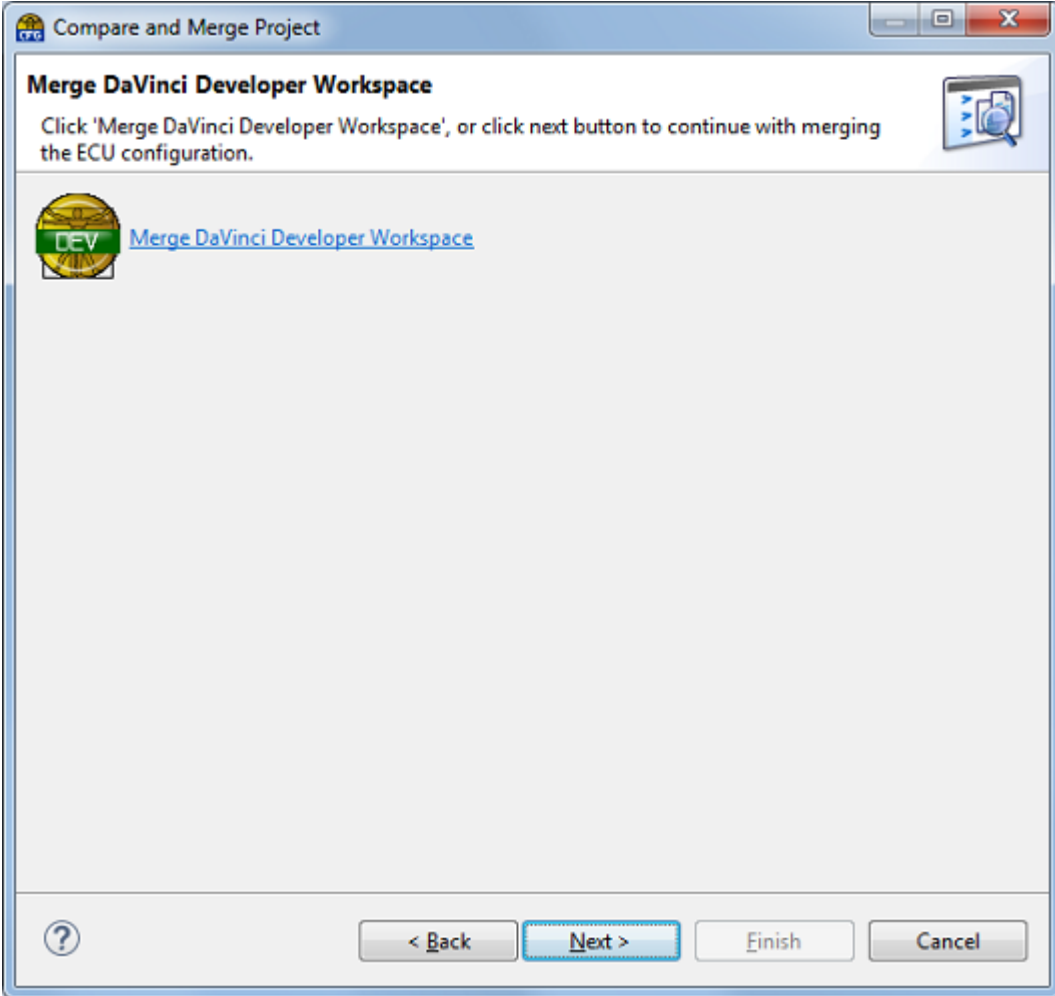
Type	Description	Change ID
	<p>The assistant is launched from a NvSWC-T: New -&gt; From Port...</p> 	

Type	Description	Change ID
	<p>The wizard allows selecting the Nv blocks that shall be created. The assistant illustrates only NvPorts that do not yet have a mapping.</p> 	

Type	Description	Change ID
	<p>Once confirmed the assistant create the NvBlock for the selected NvPort:</p>  <p>Additionally the port mapping between the NvBlock and the NvPort is created:</p> 	

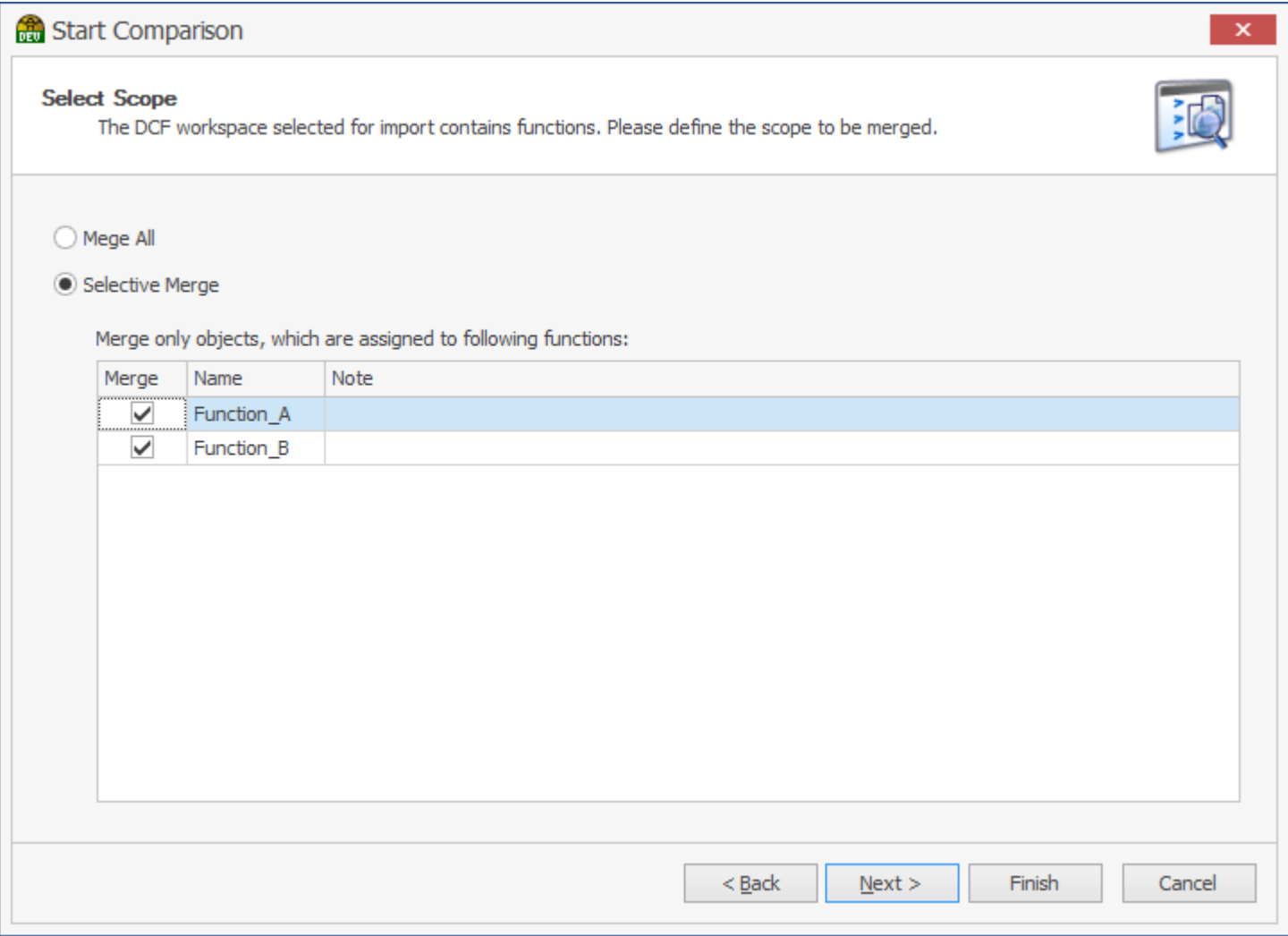
Type	Description	Change ID
	 <b>Additional Information</b> read/write DIDs, which are provided by the transformed struct-interfaces	
Extension	<p>The platform functions can now be used in DaVinci Developer and DaVinci Configurator for filtering of system template elements (such as SWCs and port mappings).</p> <p>Additionally, the usability of DaVinci Developer has been improved regarding assignment of objects to platform functions and selective merge of platform functions.</p>	FEAT-2494 FEAT-2785

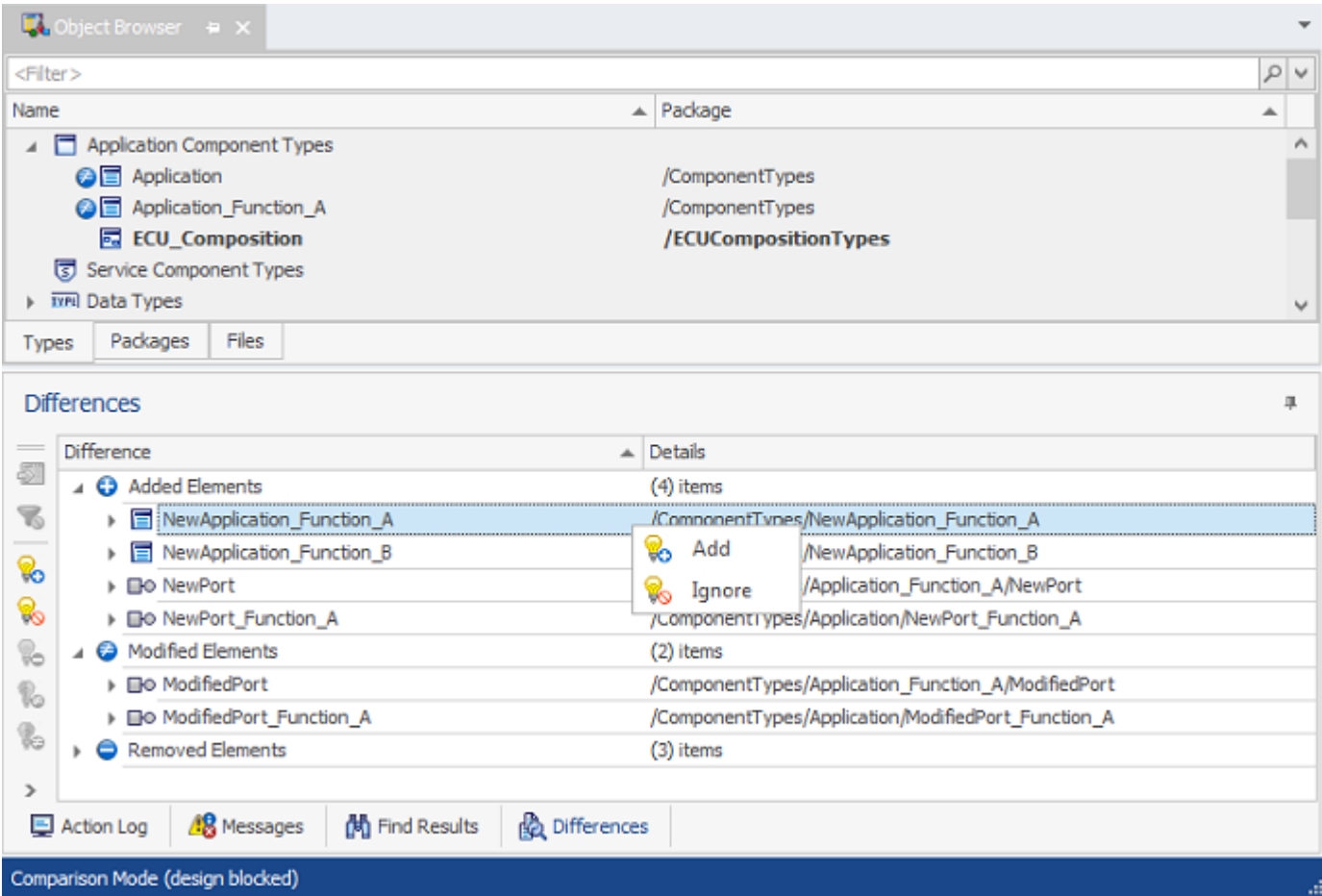
Type	Description	Change ID
	<p>The DaVinci Developer platform function look and feel is now in-line with DaVinci Configurator Pro from where it is launched:</p> 	

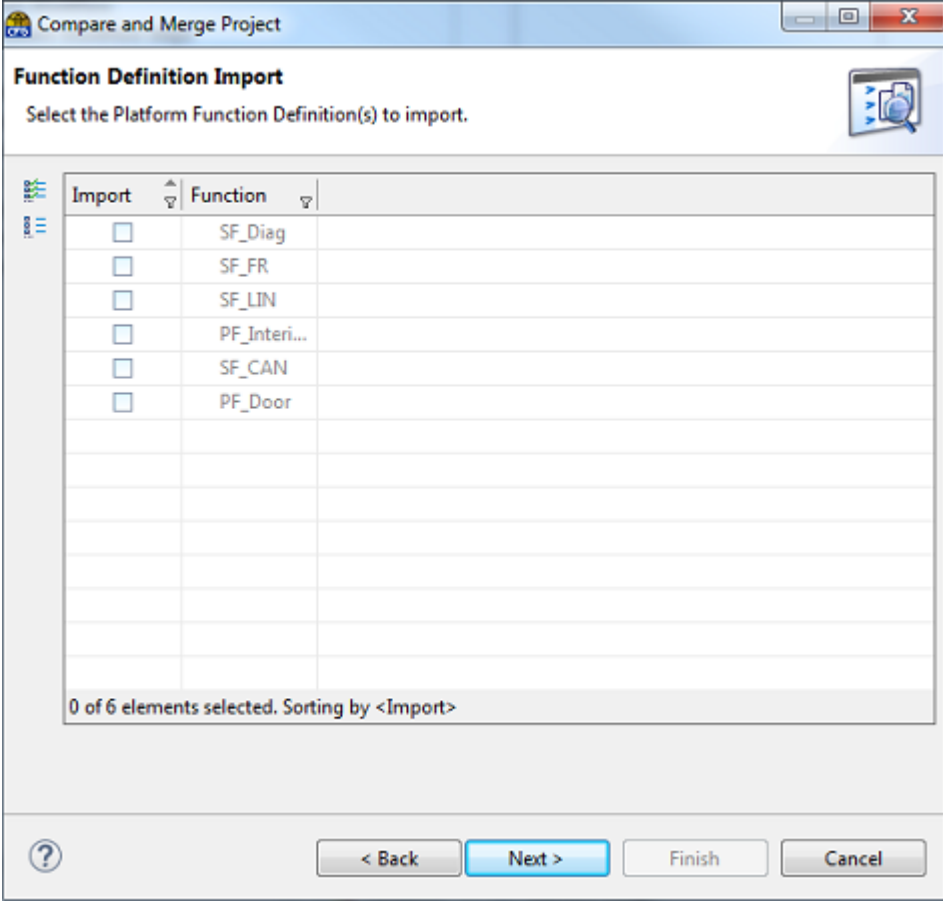

Type	Description	Change ID
		

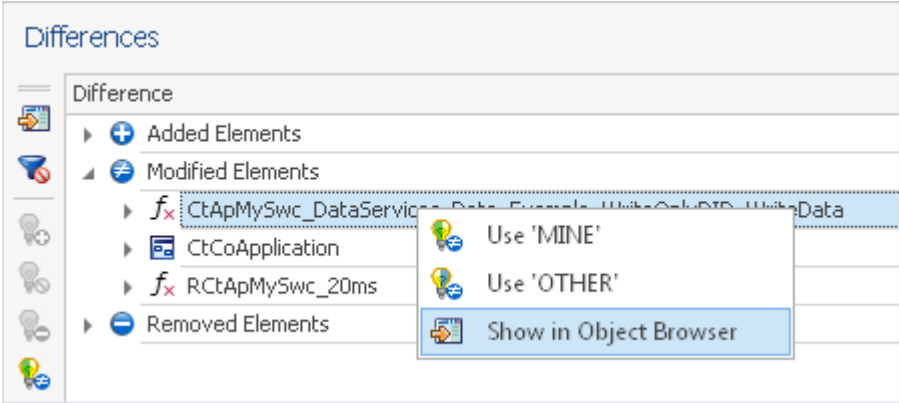


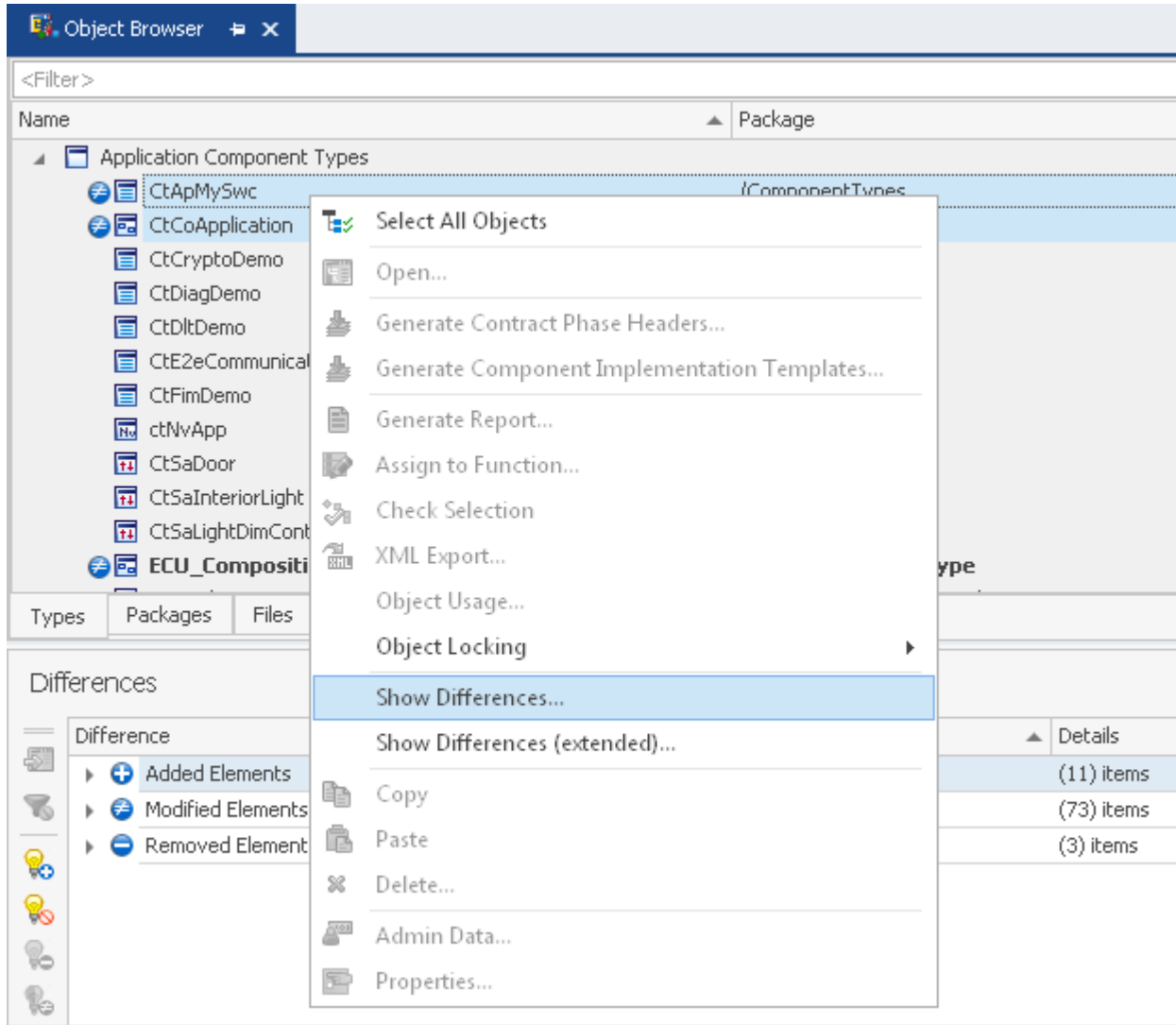
Tooling

Type	Description	Change ID
	<div>Function selection that shall be considered during the Diff&amp;Merge session in DaVinci Developer:</div> <div></div>	

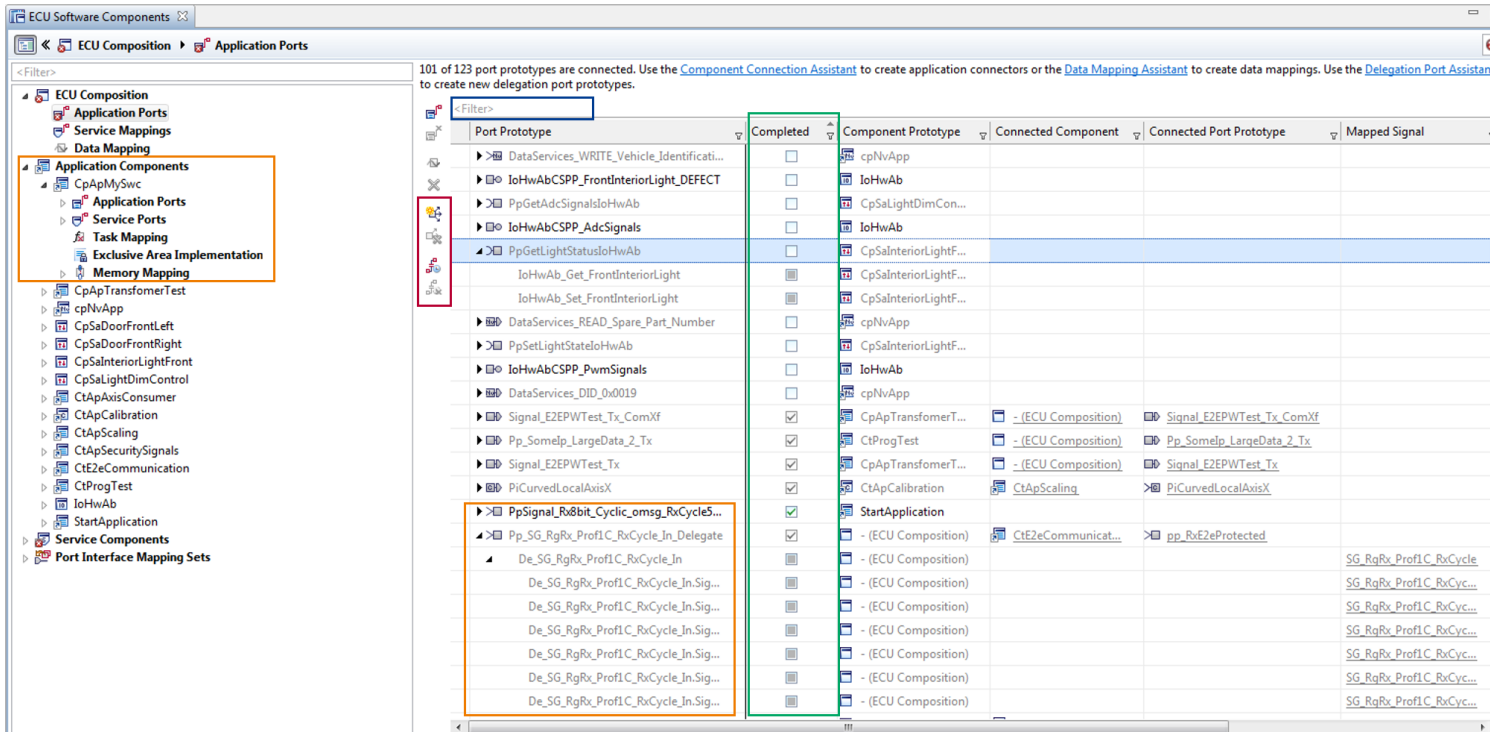
Type	Description	Change ID
	<p>The DaVinci Developer difference illustration and merge control is now similar to DaVinci Configurator Pro (as introduced in R19):</p>  <p>The screenshot displays the 'Object Browser' window with a filter bar and a tree view. The tree view shows 'Application Component Types' expanded, with sub-items 'Application', 'Application_Function_A', and 'ECU_Composition'. Below the tree view are tabs for 'Types', 'Packages', and 'Files'. The 'Differences' window is also open, showing a comparison of two versions. It lists 'Added Elements' (4 items) and 'Modified Elements' (2 items). A context menu is open over 'NewApplication_Function_A' with options 'Add' and 'Ignore'. The bottom status bar indicates 'Comparison Mode (design blocked)'.</p>	





Type	Description	Change ID
	<p>The general concept of the function based Diff&amp;Merge:</p>  <p> <b>Documentation in SIP</b></p> <p>The usage of the platform support concept is explained in detail in AN-ISC-8-1208_DaVinciTeamAndPlatformSupport.pdf</p>	

Type	Description	Change ID
Extension	<p>Project diff/merge function of DaVinci Developer 4 has been improved:</p> <p>Navigation from Difference View to Object Browser is now possible</p> 	FEAT-2494


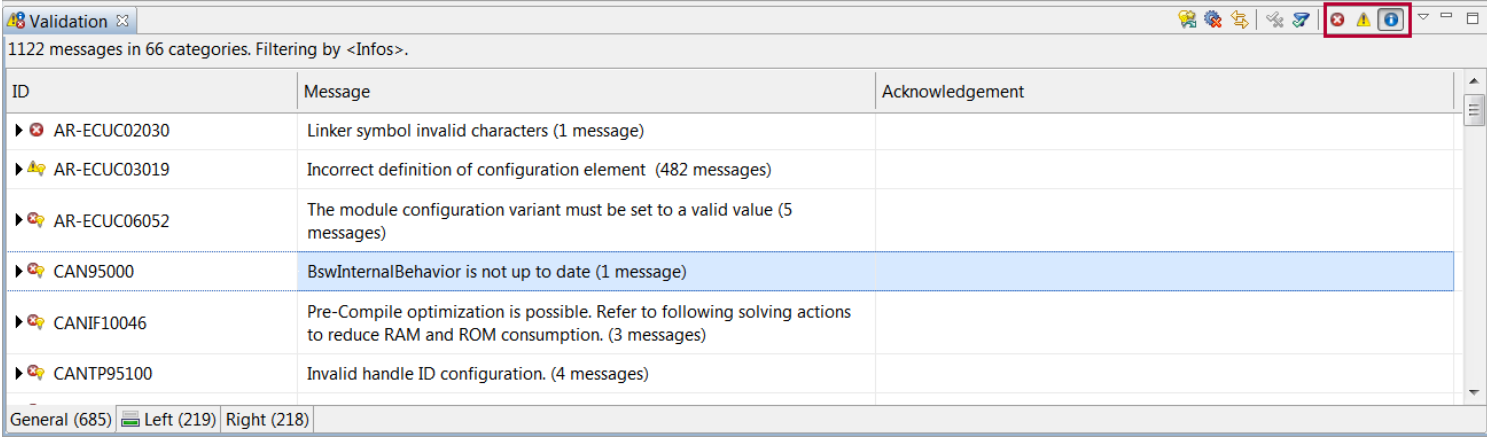
Type	Description	Change ID
	<p>The Object Browser can now be used to compare two objects including sub-objects and references.</p> 	
Extension	The user interface of DaVinci Configurator Pro has been improved with respect to the display of variant projects:	FEAT-2553


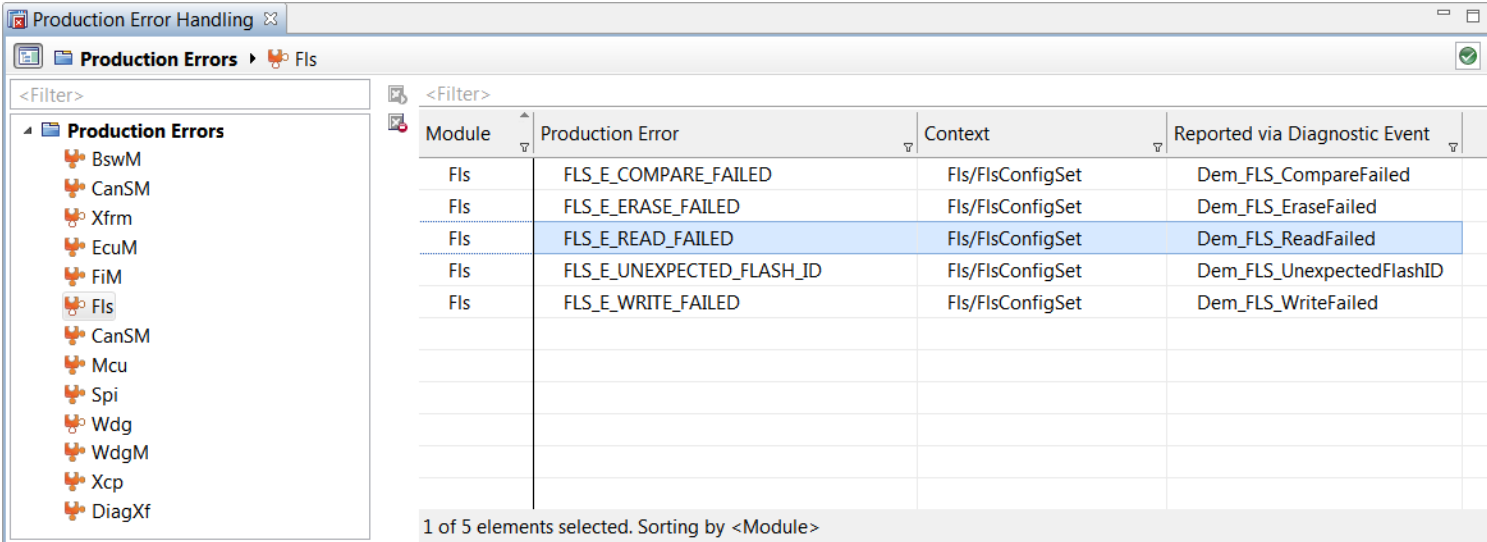
Type	Description	Change ID
	<ul style="list-style-type: none"><li>&gt; Properties View: The 'Variant' tab is now shown for multi-instance parameters</li><li>&gt; In configuration reports, variant container and parameter are now only shown once with a column that indicates the value for each variant.</li></ul>	
Extension	It is now possible to start the DaVinci Configurator Pro configuration report generation from command line.	FEAT-2553
Extension	<p>The layout of DaVinci Configurator Pro form pages was optimized so that existing space is used in a better way:</p> <ul style="list-style-type: none"><li>&gt; Controls on form pages are now wider for better display of long content/strings</li><li>&gt; Scrolling behavior has been improved especially in case of multiple nested trees or tables</li><li>&gt; Display option in the toolbar of the main window: toggles the way how references are displayed (complete path or short name only)</li></ul>	FEAT-2553
Extension	The Input File Assistant of DaVinci Configurator Pro has been reworked to improve usability and to allow the support of future use-cases.	FEAT-2594
Extension	<p>Various usability and performance improvements in the user interface of DaVinci Configurator Pro:</p> <ul style="list-style-type: none"><li>&gt; Use-case editor expansion and selection state in grid is preserved during variant switch</li><li>&gt; Basic editor expansion and selection state in tree is preserved during variant switch</li><li>&gt; Automatic selection of newly created elements in grids</li><li>&gt; Performance optimization for large grids views</li></ul>	FEAT-2791
Extension	The ECU Software Components Editor of DaVinci Configurator Pro has been enhanced with better support of incomplete designs and usability improvements.	FEAT-2857

Type	Description	Change ID																																																																																																																																																						
	<div><p>101 of 123 port prototypes are connected. Use the <a href="#">Component Connection Assistant</a> to create application connectors or the <a href="#">Data Mapping Assistant</a> to create data mappings. Use the <a href="#">Delegation Port Assistant</a> to create new delegation port prototypes.</p><table><thead><tr><th>Port Prototype</th><th>Completed</th><th>Component Prototype</th><th>Connected Component</th><th>Connected Port Prototype</th><th>Mapped Signal</th></tr></thead><tbody><tr><td>&gt; DataServices_WRITE_Vehicle_Identificati...</td><td><input type="checkbox"/></td><td>cpNvApp</td><td></td><td></td><td></td></tr><tr><td>&gt; IoHwAbCSPP_FrontInteriorLight_DEFECT</td><td><input type="checkbox"/></td><td>IoHwAb</td><td></td><td></td><td></td></tr><tr><td>&gt; PpGetAdcSignalsIoHwAb</td><td><input type="checkbox"/></td><td>CpSaLightDimCon...</td><td></td><td></td><td></td></tr><tr><td>&gt; IoHwAbCSPP_AdcSignals</td><td><input type="checkbox"/></td><td>IoHwAb</td><td></td><td></td><td></td></tr><tr><td>&gt; PpGetLightStatusIoHwAb</td><td><input type="checkbox"/></td><td>CpSaInteriorLightF...</td><td></td><td></td><td></td></tr><tr><td>IoHwAb_Get_FrontInteriorLight</td><td><input checked="" type="checkbox"/></td><td>CpSaInteriorLightF...</td><td></td><td></td><td></td></tr><tr><td>IoHwAb_Set_FrontInteriorLight</td><td><input checked="" type="checkbox"/></td><td>CpSaInteriorLightF...</td><td></td><td></td><td></td></tr><tr><td>&gt; DataServices_READ_Spare_Part_Number</td><td><input type="checkbox"/></td><td>cpNvApp</td><td></td><td></td><td></td></tr><tr><td>&gt; PpSetLightStateloHwAb</td><td><input type="checkbox"/></td><td>CpSaInteriorLightF...</td><td></td><td></td><td></td></tr><tr><td>&gt; IoHwAbCSPP_PwmSignals</td><td><input type="checkbox"/></td><td>IoHwAb</td><td></td><td></td><td></td></tr><tr><td>&gt; DataServices_DID_00019</td><td><input type="checkbox"/></td><td>cpNvApp</td><td></td><td></td><td></td></tr><tr><td>&gt; Signal_E2EPWTest_Tx_ComXf</td><td><input checked="" type="checkbox"/></td><td>CpApTransformerT...</td><td>- (ECU Composition)</td><td>&gt; Signal_E2EPWTest_Tx_ComXf</td><td></td></tr><tr><td>&gt; Pp_Somelp_LargeData_2_Tx</td><td><input checked="" type="checkbox"/></td><td>CtProgTest</td><td>- (ECU Composition)</td><td>&gt; Pp_Somelp_LargeData_2_Tx</td><td></td></tr><tr><td>&gt; Signal_E2EPWTest_Tx</td><td><input checked="" type="checkbox"/></td><td>CpApTransformerT...</td><td>- (ECU Composition)</td><td>&gt; Signal_E2EPWTest_Tx</td><td></td></tr><tr><td>&gt; PiCurvedLocalAxisX</td><td><input checked="" type="checkbox"/></td><td>CtApCalibration</td><td>CtApScaling</td><td>&gt; PiCurvedLocalAxisX</td><td></td></tr><tr><td>&gt; PpSignal_Rx8bit_Cyclic_omsg_RxCycle5...</td><td><input checked="" type="checkbox"/></td><td>StartApplication</td><td></td><td></td><td></td></tr><tr><td>&gt; Pp_SG_RgRx_Prof1C_RxCycle_In_Delegate</td><td><input checked="" type="checkbox"/></td><td>- (ECU Composition)</td><td>CtE2eCommunicat...</td><td>&gt; pp_RxE2eProtected</td><td></td></tr><tr><td>&gt; De_SG_RgRx_Prof1C_RxCycle_In</td><td><input checked="" type="checkbox"/></td><td>- (ECU Composition)</td><td></td><td></td><td>SG_RgRx_Prof1C_RxCycle</td></tr><tr><td>&gt; De_SG_RgRx_Prof1C_RxCycle_In.Sig...</td><td><input checked="" type="checkbox"/></td><td>- (ECU Composition)</td><td></td><td></td><td>SG_RgRx_Prof1C_RxCyc...</td></tr><tr><td>&gt; De_SG_RgRx_Prof1C_RxCycle_In.Sig...</td><td><input checked="" type="checkbox"/></td><td>- (ECU Composition)</td><td></td><td></td><td>SG_RgRx_Prof1C_RxCyc...</td></tr><tr><td>&gt; De_SG_RgRx_Prof1C_RxCycle_In.Sig...</td><td><input checked="" type="checkbox"/></td><td>- (ECU Composition)</td><td></td><td></td><td>SG_RgRx_Prof1C_RxCyc...</td></tr><tr><td>&gt; De_SG_RgRx_Prof1C_RxCycle_In.Sig...</td><td><input checked="" type="checkbox"/></td><td>- (ECU Composition)</td><td></td><td></td><td>SG_RgRx_Prof1C_RxCyc...</td></tr><tr><td>&gt; De_SG_RgRx_Prof1C_RxCycle_In.Sig...</td><td><input checked="" type="checkbox"/></td><td>- (ECU Composition)</td><td></td><td></td><td>SG_RgRx_Prof1C_RxCyc...</td></tr><tr><td>&gt; De_SG_RgRx_Prof1C_RxCycle_In.Sig...</td><td><input checked="" type="checkbox"/></td><td>- (ECU Composition)</td><td></td><td></td><td>SG_RgRx_Prof1C_RxCyc...</td></tr></tbody></table></div>	Port Prototype	Completed	Component Prototype	Connected Component	Connected Port Prototype	Mapped Signal	> DataServices_WRITE_Vehicle_Identificati...	<input type="checkbox"/>	cpNvApp				> IoHwAbCSPP_FrontInteriorLight_DEFECT	<input type="checkbox"/>	IoHwAb				> PpGetAdcSignalsIoHwAb	<input type="checkbox"/>	CpSaLightDimCon...				> IoHwAbCSPP_AdcSignals	<input type="checkbox"/>	IoHwAb				> PpGetLightStatusIoHwAb	<input type="checkbox"/>	CpSaInteriorLightF...				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	<div><ul style="list-style-type: none"><li>&gt; Red: Set a Port Terminator to mark an intentionally unconnected port, create delegation ports from within DaVinci Configurator Pro.</li><li>&gt; Blue: General grid filter to search for arbitrary items</li><li>&gt; Green: Quick overview to show if the port is connected to other SWCs or to network signals</li><li>&gt; Orange: New tree structures to ease access to the configuration</li></ul></div>																																																																																																																																																							
Extension	<div><p>The DaVinci Configurator Pro automation interface was extended</p><ul style="list-style-type: none"><li>&gt; by a new high-level convenience API that allows simple adding of 1:1 SWC port mappings and 1:1 data mappings</li><li>&gt; by the possibility to import module configurations</li></ul></div>	<div><p>FEAT-2942</p><p>FEAT-3344</p></div>																																																																																																																																																						

Type	Description	Change ID
Extension	<p>There is a new type of validation result in DaVinci Configurator Pro that points out a license violation. License violations occur if a MICROSAR feature is used that has not (yet) been licensed. Please contact Vector if such a validation result pops up.</p> <div>  <b>Additional Information</b>            The MICROSAR Product Information provides more details on the usage of features that have not been licensed.         </div>	FEAT-2979
Extension	DaVinci tools now inform users on a imminent evaluation license expiration. The warning will be shown starting 14 days before the license expires and considers both, tool and SIP license.	FEAT-3041
Extension	<p>DaVinci tools and code generators now support AUTOSAR 4.3.1 schema.</p> <div>  <b>Additional Information</b>            The schema support does not imply that additional AR4.3.1 features have been implemented.         </div>	FEAT-3078
Extension	DaVinci Developer now supports SERVICE-PROXY-SW-COMPONENT-TYPE.	FEAT-3113
Extension	Post-Build Selectable (MICROSAR Identity Manager) now also supports variant communication clusters without the need for a workaround in the upstream mapping process.	FEAT-3158
Extension	<p>The Vector ARXML Editor now stores and restores the last user settings and window layout.</p> <div>  <b>Additional Information</b>            The tool is distributed and updated using the DaVinci External Components Setup. Please update your local installation.         </div> <div>  <b>Additional Information</b>  <a href="#">DaVinci_ExternalComponentsSetup.*</a> </div>	FEAT-3231
Extension		FEAT-3254



Type	Description	Change ID
	<p>It is now possible to add a new DaVinci Developer workspace to an existing .dpa project from within DaVinci Configurator Pro.</p> 	
Extension	The DaVinci Configurator Pro reference selection dialog now shows the elements that make use of the items that can be chosen. This simplifies the selection of the correct reference targets especially when dealing with references on global PDUs.	FEAT-3263
Extension	<p>In DaVinci Configurator Pro it is now possible to hide validation messages based on their severity. This allows focusing on the typically more relevant errors of the configuration.</p> 	FEAT-3343
Extension	The SipModificationChecker now also considers changes in XML, PDF files as well as MSSV plugins for SafeBSW deliveries.	FEAT-3403
Information	The DaVinci tools now support the new Vector license model.	FEAT-2552

Type	Description	Change ID
	 <b>Additional Information</b> The existing licenses (Flexnet and Keyman) remain valid and are supported also in future.	
Information	<p>The Production Error Editor of DaVinci Configurator Pro supports now all production errors (Dem Event references) of all modules, including third party MCAL modules. In the past some Events have been missing.</p> <p>Additionally the user interface was reworked to improve usability.</p> 	FEAT-2800

## Release 19

### General

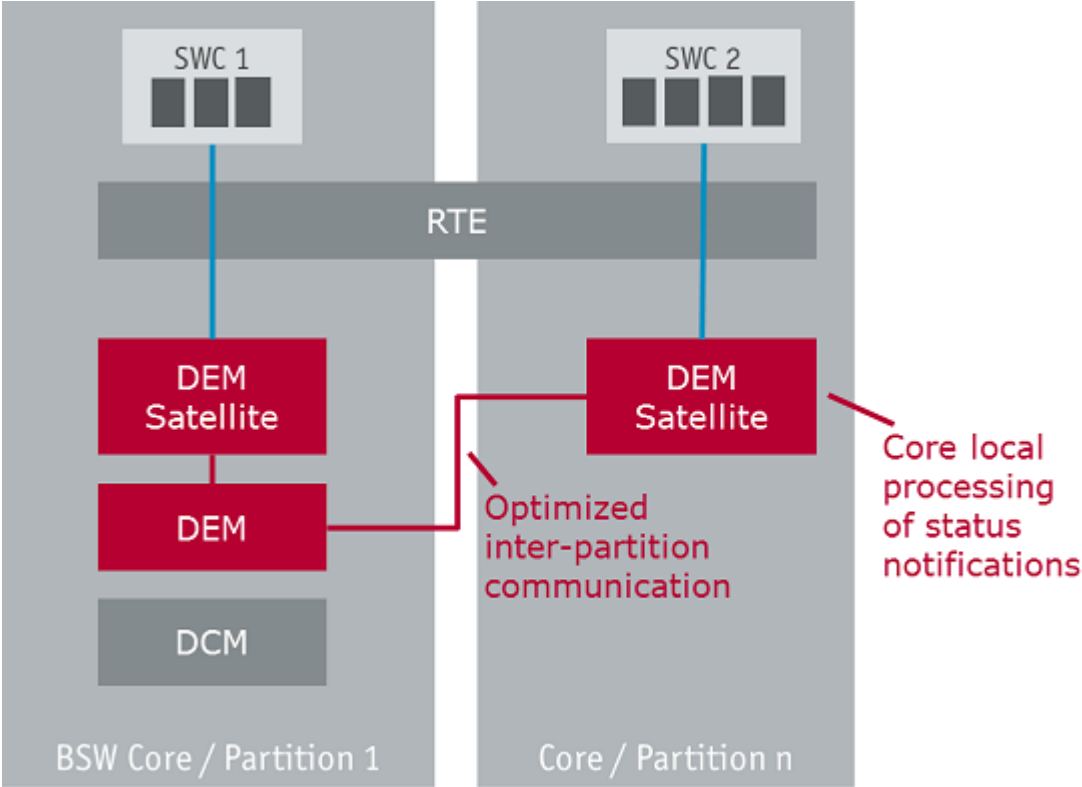
Type	Description	Change ID
Extension	The MemMap.h template now supports module specific MemMap files as defined by AR4.2	FEAT-2455

### Communication

Type	Description	Change ID
Extension	The Com module now supports PDUs that are not mapped to a PDU group. In this case the PDU is now initially started and never stopped as defined by AUTOSAR.	FEAT-2726
Extension	The Com module now supports MASKED_NEW_EQUALS_X and MASKED_NEW_DIFFERS_X filters for Signal Group Arrays. This is relevant if the ComXf is used.	FEAT-2833
Extension	Signal groups that are handled by the ComXf module can now also be routed by the Com signal gateway.	FEAT-2864
Extension	IpduM now provides a request queue for TriggerTransmit of container PDUs according to AR4.2.	FEAT-2968
Extension	The CAN stack now supports Rx ranges for non- NM PDUs.	FEAT-3091
Information	The transformer modules ComXf and E2eXf have been released as QM.	FEAT-2498
Information	The FlexRay stack now supports multiple FlexRay controllers. This feature has been released.  Note: For one cluster only a single controller can be used.	FEAT-2725

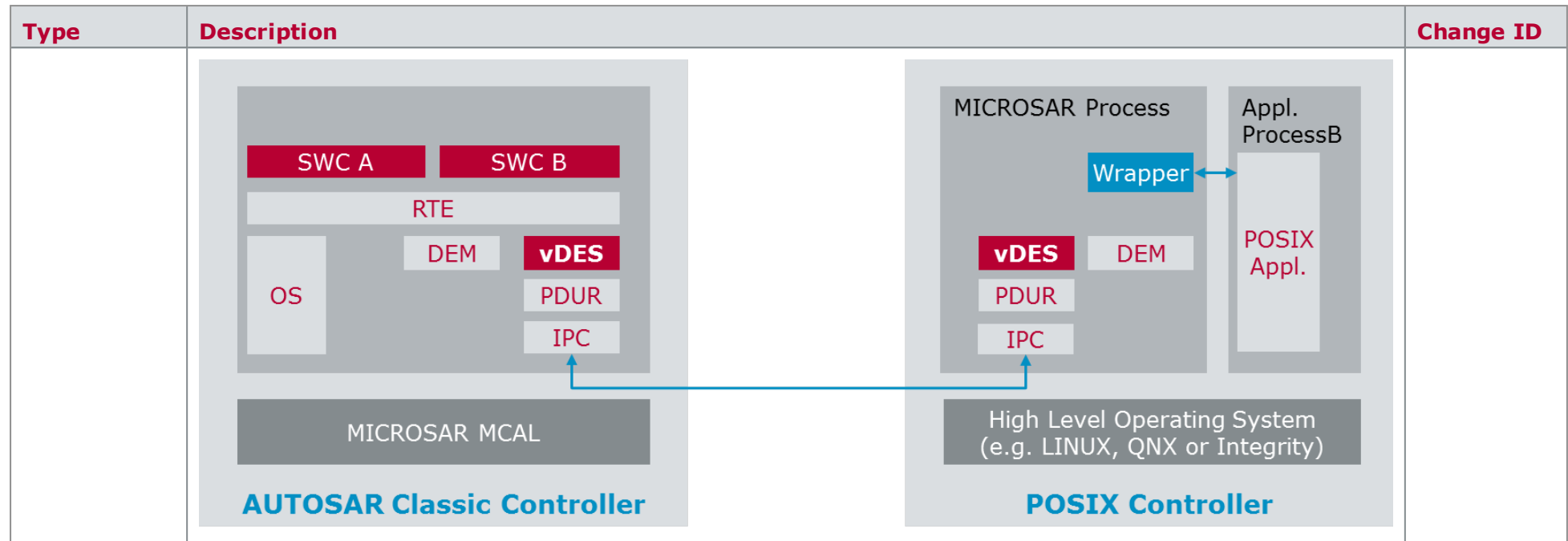
### Diagnostics

Type	Description	Change ID
Breaking Change	The Dem has been reworked to support multi-core and safety projects in an optimized way. The DTC handling is processed on the core where the DTC status is updated. This reduces the costs for inter-core communication drastically.  As part of the rework the APIs of the DEM modules have been updated to AR4.3 definition.	FEAT-2761

Type	Description	Change ID
	 <p>Note: With R19 this feature may not yet be rolled out to all programs.</p> <p><b>Migration notes for existing projects:</b></p> <p>The DEM configuration need to be reworked mainly in order to define the application context of the master and the satellite instances.</p> <p>Due to AR4.3 API changes, the application may have to be adapted according to the updated APIs.</p>	
Extension	The following services of the Dcm are now released for safety-projects: 0x10, 0x14, 0x19, 0x27, 0x28, 0x3E, 0x85	FEAT-2505

Type	Description	Change ID
	Note: Not all services are safe yet. These services must be disabled if Dcm is used in safety projects. More information can be found in the safety manual.	
Extension	Dem now supports the following new functionalities: <ul style="list-style-type: none"><li>&gt; Dem_GetOperationCycleState</li><li>&gt; indicator state CONT_BLINKING for OBD MIL</li></ul>	FEAT-2783
Extension	Dem provides the API Dem_GetDTCSuppression(). The API returns the suppression status of a given DTC.	FEAT-2892
Extension	The Dcm S/R communication using vDiagXf now supports <ul style="list-style-type: none"><li>&gt; array data types: uint8[], sint8[], uint16[], sint16[], uint32[], sint32[]</li><li>&gt; uint8 as Boolean data type</li></ul>	FEAT-2962 FEAT-2770
Extension	The Dem configuration workflow using a Diagnostic Extract now supports more than 255 DTCs without the need to manually assign dedicated DemFreezeFrameClass and DemEnableConditionGroups.	FEAT-3106
Information	The vDes module has been released as QM. The component allows synchronization of Dem DTC status information from a slave controller to the master controller that runs the diagnostic (MICROSAR) stack. A typical use-case is to allow a second processor (e.g. running POSIX OS to set DTC status. The communication takes place over the MICROSAR IPC solution.	FEAT-2492

Type	Description	Change ID
	<p>The diagram illustrates the vDES architecture for Master and Slave microcontrollers. The Master μC contains an Application, DCM, DEM, vDES (Master), and PDUR. The Slave μC contains an Application, vDES (Slave), DEM, and PDUR. Both are connected to CAN/FR/... and CAN/IPC/... controllers. A dashed line indicates communication between the Master and Slave vDES components.</p>	

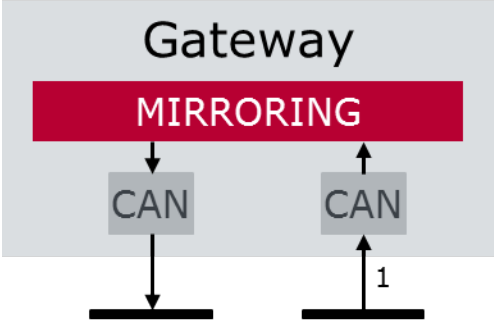
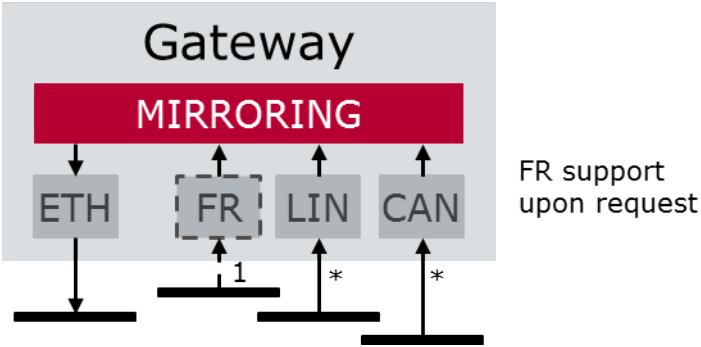


## Ethernet

Type	Description	Change ID
Extension	VLAN-based UdpNm can now be derived from a AR4.3 System Template.	FEAT-2522
Extension	Optimized modelling of (SOME/IP) client/server calls is now supported as defined by AR4.3. This allows to use the same pair of PDUs and System Signals for multiple clients, which use the same server service on the same Ethernet channel (VLAN).	FEAT-2666
Extension	Support more than 255 DoIP Target Addresses	FEAT-2985
Information	The feature IPv4 Fragmentation in the TcpIp module (FEAT-1481) has been released.	FEAT-2479

## Gateway

Type	Description	Change ID
Information	The Mirroring module (vMirror) has been released as QM. It allows CAN to CAN mirroring	FEAT-2729

Type	Description	Change ID
	 <p>As well as the mirroring of several internal networks to (diagnostic) Ethernet connector</p> 	

## IPC

Type	Description	Change ID
Information	The IPC communication CAN over SPI has been released. This special CAN driver allows a CAN like communication (with respect to upper layer modules and configuration) over an SPI channel.	FEAT-2746

## J1939

Type	Description	Change ID
Extension	The CAN stack now supports baudrate detection as it is defined by the J1939-16 standard.	FEAT-2738

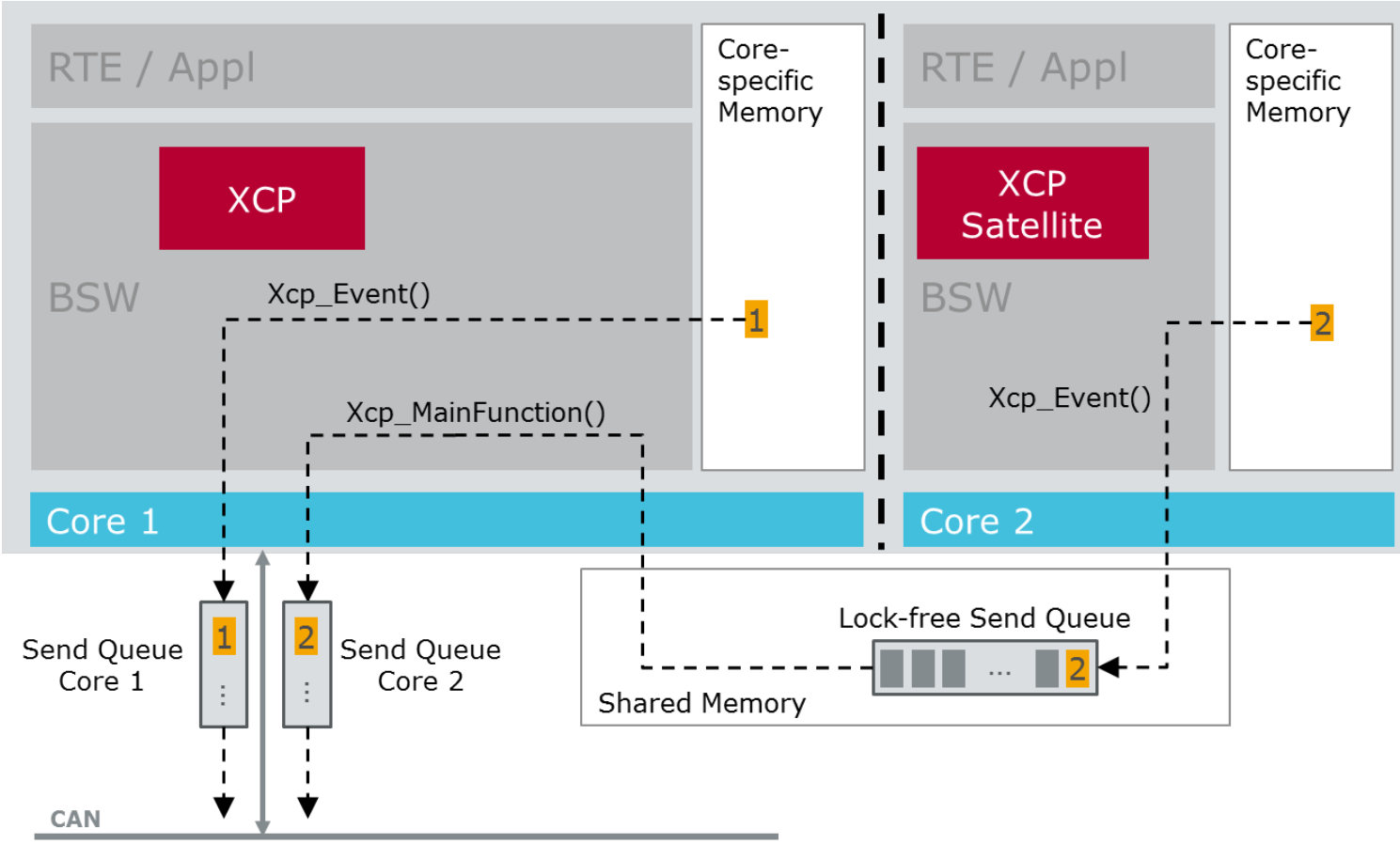


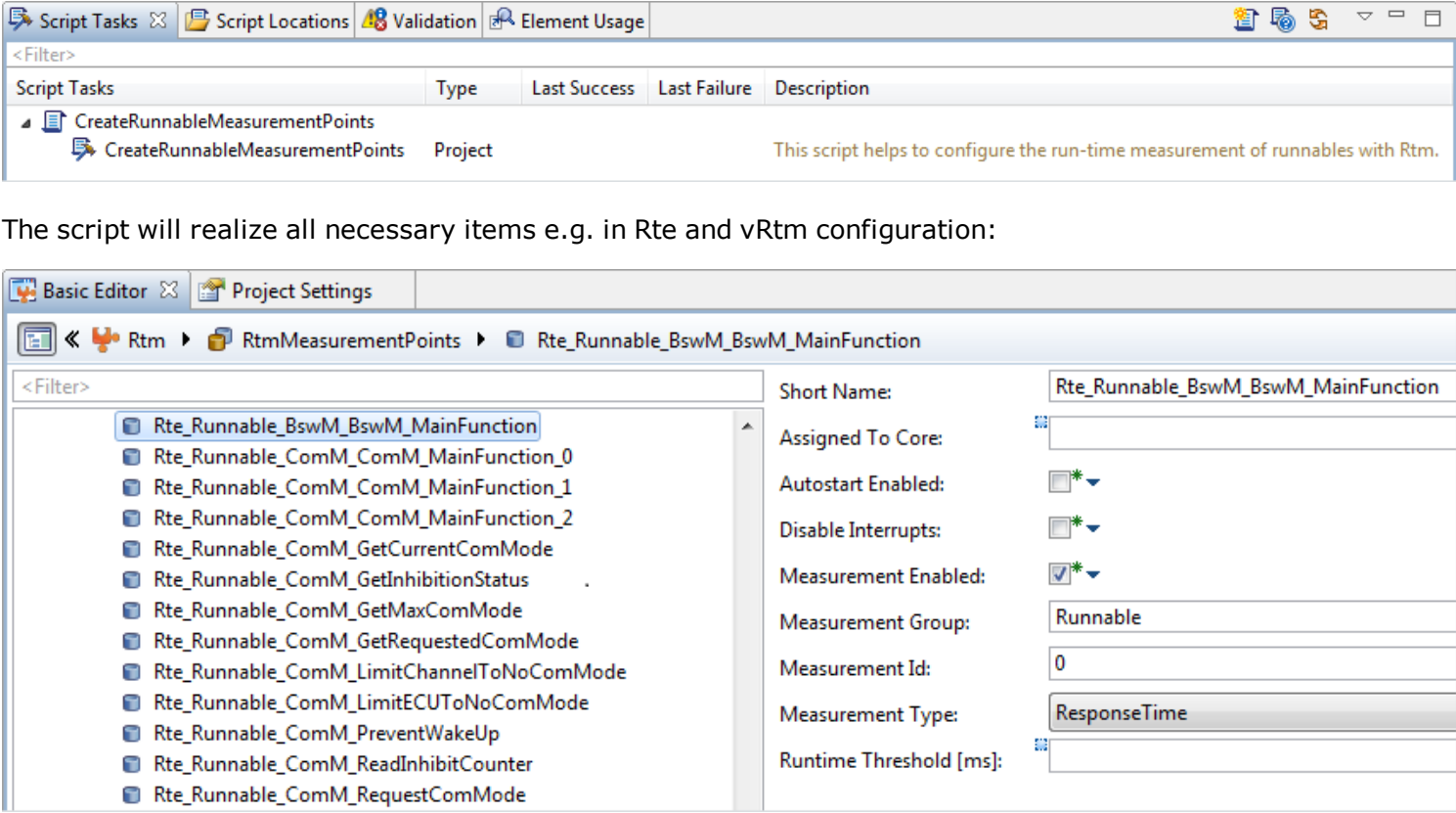
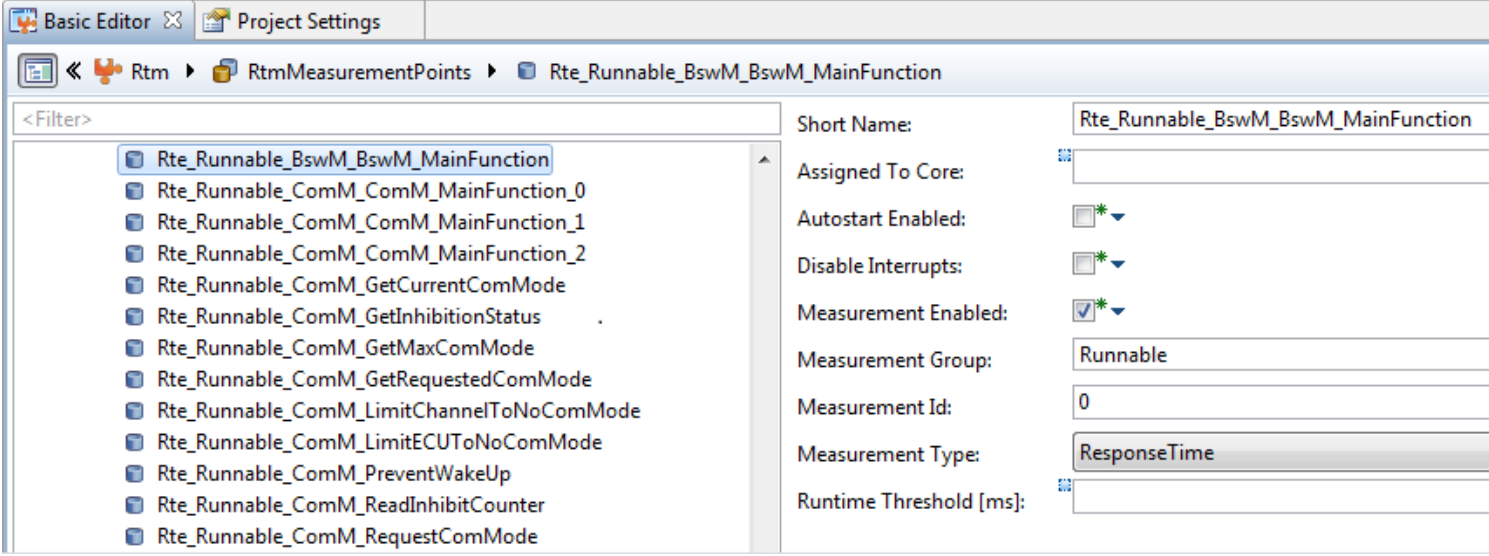
Type	Description	Change ID
	Note: This feature is available for selected CAN drivers only.	
Extension	J1939 Commanded NAME support is now provided.	FEAT-2862

## Measurement and Calibration

Type	Description	Change ID
Extension	The vRtm net runtime measurement can now be used with the latest Gen7 MICROSAR Os. Net runtime measurement eliminates the runtime of tasks that interrupt the execution of the code that shall be measured.	FEAT-2789
Extension	The map and curve support of the Rte now supports plain array implementation data types in combination with shared Axis.	FEAT-3119
Extension	The a2I display format can now be defined on Data Prototype level for all kinds of MC relevant elements in the SWC design. The Rte will export this information to the generated Rte.a2I.	FEAT-3159
Information	The vRtm multi-core option has been released. This allows runtime-measurement on any core as well as cross-core communication response time measurements.	FEAT-2571



Type	Description	Change ID
	 <p>The diagram illustrates the architecture for XCP and XCP Satellite across two cores, Core 1 and Core 2. Each core has an RTE / Appl layer and a BSW layer. Core 1 contains an XCP module, while Core 2 contains an XCP Satellite module. Both have access to Core-specific Memory. The BSW layer in Core 1 includes Xcp_Event() and Xcp_MainFunction(). The BSW layer in Core 2 includes Xcp_Event(). A Lock-free Send Queue in Shared Memory is connected to the Xcp_Event() functions on both cores. Below the cores, Send Queue Core 1 and Send Queue Core 2 are shown, both connected to a CAN bus. Arrows indicate data flow from the XCP modules through the BSW and Send Queues to the CAN bus.</p>	
Information	<p>The script that allows vRtm to measure runnable runtimes has been released. The script not only configures vRtm and the Rte but also generates the required Rte hook functions. The script can be triggered within the DaVinci Configurator Pro "Script Tasks" View.</p> <p>Using the Script Task View the scripted can be launched:</p>	FEAT-2692

Type	Description	Change ID
	 <p>The script will realize all necessary items e.g. in Rte and vRtm configuration:</p> 	
Information	The precision of vRtm has been improved additionally by a reworked overhead calculation.	FEAT-2845




## Nv Memory

Type	Description	Change ID
Breaking Change	<p>The Fee (Small Sector) as typically used for RH850 has been extended with an additional erase pattern in each dataset. This further increases the reset robustness of the component.</p> <p><b>Migration notes for existing projects:</b></p>	FEAT-3025

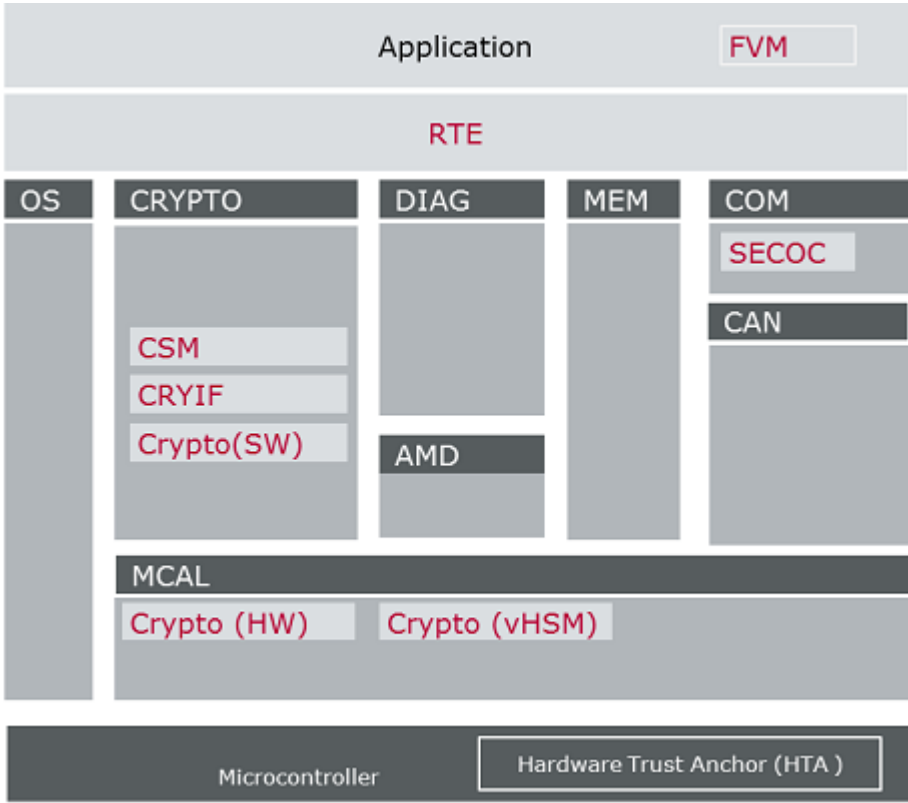
Type	Description	Change ID
	<p>The breaking change affects projects only that shall be able to access flash data that has been written with the previous version of the Fee (Small Sector).</p> <p>The old Fee flash data are no longer compatible with the flash format of the new Fee. The new Fee reports existing data to be inconsistent.</p> <p>Existing flash data cannot be read by the new Fee.</p> <p>A manual update strategy is required such as:</p> <ul style="list-style-type: none"><li>&gt; use the old Fee (from the old SIP) to retrieve stored data from the ECU and store that data externally</li><li>&gt; update the SIP with the new Fee</li><li>&gt; download the old data with the new Fee</li></ul> <p>A build in update mechanism is not provided.</p>	
Extension	<p>The CRC compare mechanism defined by AUTOSAR has been introduced in NvM. It may reduce the need to store data to NV memory in case no modifications have been made.</p> <p>Note: Since CRC might be insufficient to detect changes (different data, same CRC) using this feature might result in losing most recent data.</p>	FEAT-2914

**Rte**

Type	Description	Change ID
Extension	The Rte now supports scaling (offset and factor) of data that is connected with a group signal.	FEAT-2998
Information	The MICROSAR RTE has been certified for safety related projects up to ASIL D.	FEAT-2768

Type	Description	Change ID
	<div data-bbox="443 308 683 432">  </div> <div data-bbox="443 564 683 603"> <p>The manufacturer may use the mark:</p> </div> <div data-bbox="443 619 683 858">  </div> <div data-bbox="443 906 683 986"> <p>Revision 1.0 August 21, 2017 Surveillance Audit Due August 31, 2020</p> </div> <div data-bbox="510 1337 616 1401">  </div> <div data-bbox="465 1417 660 1473"> <p>ANSI Accredited Program PRODUCT CERTIFICATION #1004</p> </div> <div data-bbox="790 300 1238 419"> <p>Certificate / Certificat Zertifikat / 合格証</p> </div> <div data-bbox="790 443 1193 475"> <p>Vector 1512053 P0030 C003</p> </div> <div data-bbox="857 491 1149 515"> <p><i>exida</i> hereby confirms that the:</p> </div> <div data-bbox="790 531 1205 651"> <p><b>Vector MICROSAR SafeRTE</b> <b>Vector Informatik GmbH</b> <b>Stuttgart, Germany</b></p> </div> <div data-bbox="723 707 1261 754"> <p>has been assessed per the relevant requirements regarding software development and verification of:</p> </div> <div data-bbox="745 770 1272 866"> <p><b>ISO 26262 : 2011 Parts 2, 4, 6, 7, 8 and 9</b> <b>(to the extent applicable)</b> <b>Systematic Integrity: ASIL D</b></p> </div> <div data-bbox="723 898 947 922"> <p><b>Safety related function:</b></p> </div> <div data-bbox="723 930 1305 994"> <p>The Vector MICROSAR SafeRTE V4.14.00 supports the generation and execution of safety-related software by its listed safety features (see reverse).</p> </div> <div data-bbox="723 1010 958 1034"> <p><b>Application restrictions:</b></p> </div> <div data-bbox="723 1042 1249 1082"> <p>The MICROSAR SafeRTE V4.14.00 shall be used per the Safety Manual requirements.</p> </div> <div data-bbox="734 1217 936 1417">  </div> <div data-bbox="947 1233 1305 1321"> <p> Evaluating Assessors</p> </div> <div data-bbox="947 1345 1272 1425"> <p> Certifying Assessor</p> </div> <div data-bbox="857 1449 981 1473"> <p>Page 1 of 2</p> </div>	

## Security

Type	Description	Change ID
Information	<p>The AR4.3 based CRYPTO stack is now available as ASIL. The stack includes the Crypto (SW), CryIf, Csm and, SecOC. Hardware based CRYPTO drivers are available on request.</p> <p>Please note: Some algorithms realized by CRYPTO (SW) are not fully verified yet and will be completed in the following release cycles.</p>  <p>Note: The AR4.3 based CRYPTO stack has not yet been rolled out to all programs.</p>	FEAT-2447 FEAT-2500 FEAT-2502 FEAT-2767
Information	Crypto (SW) has now a better runtime performance due to the pre calculation of Sub Key and Roundkeys.	FEAT-2509

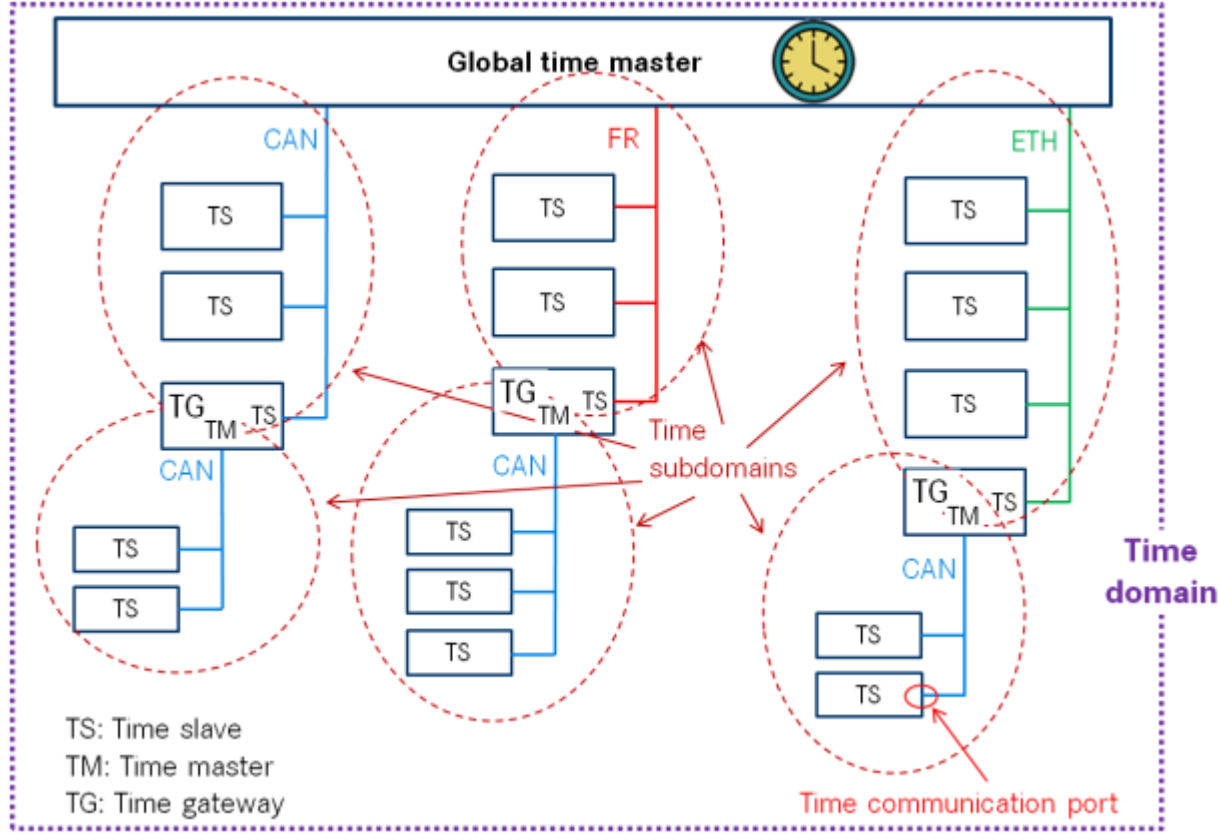
Type	Description	Change ID
Information	<p>The BSWMD of CRYPTO stack modules have been reworked to be compliant with the final AR4.3 definition. No manual migration effort is to be expected.</p> <p>Note: The AR4.3 based CRYPTO stack has not yet been rolled out to all programs.</p>	FEAT-3044

## Time Synchronization

Type	Description	Change ID
Breaking Change	<p>The time synchronization stack (CanTsyn, FrTsyn, EthTsyn and StbM) is now realized and released (QM) based on AR4.3 architecture specification. Currently the modules are still AR4.2 compliant on the network.</p> <p>Note: Not all AR4.3 features have been implemented yet. These will be completed in R20 including the possibility to switch the network compatibility between AR4.3 and AR4.2.</p> <p>Overview MICROSAR Time Synchronization Stack:</p>	<p>FEAT-2457</p> <p>FEAT-2461</p> <p>FEAT-2466</p> <p>FEAT-2473</p> <p>FEAT-2474</p>



Type	Description	Change ID
	<div><div><div>E2ePw</div><div>Application</div></div><div><div>SchM</div><div>Rte</div></div><div><div><div><div>OS</div><div>Os</div></div><div><div>SYS</div><div>BswM</div><div>ComM</div><div>Csm</div><div>Cry (Sw)</div><div>Det</div><div>EcuM</div><div>StbM</div><div>Tm</div><div>WdgIf</div><div>WdgM</div></div><div><div>DIAG</div><div>Dcm</div><div>Dem</div><div>FiM</div><div>J1939Dcm</div><div>vDrm</div><div>AMD</div><div>vDbg</div><div>Dlt</div><div>vRtm</div><div>Xcp</div></div><div><div>MEM</div><div>Ea</div><div>Fee</div><div>MemIf</div><div>NvM</div></div><div><div>COM</div><div><div>Com</div><div>ComXf</div></div><div><div>LdCom</div><div>SomelpXf</div></div><div><div>IpduM</div><div>E2eXf</div></div><div><div>Nm</div><div>SecOC</div></div><div><div>PduR</div></div></div><div><div>IO</div><div>vDioHwAb</div></div><div><div>LIBS</div><div>Cal (Cpl)</div><div>Crc</div><div>E2e</div><div>Complex Driver</div></div></div><div><div><div>CAN</div><div>J1939Tp</div><div>J1939Nm</div><div>J1939Rm</div><div>CanXcp</div><div>CanTp</div><div>CanNm</div><div>CanSM</div><div>CanTSyn</div><div>CanIf</div></div><div><div>LIN</div><div>vLinXcp</div><div>vLinTp</div><div>LinNm</div><div>LinSM</div><div>LinIf</div></div><div><div>FR</div><div>FrXcp</div><div>FrTp</div><div>FrArTp</div><div>FrNm</div><div>FrSM</div><div>FrTSyn</div><div>FrIf</div></div><div><div>ETH</div><div>EthXcp</div><div>UdpNm</div><div>Sd</div><div>DoIP</div><div>SoAd</div><div>vEtm</div><div>vTls</div><div>Tcplp</div><div>EthSM</div><div>EthTSyn</div><div>vEthFw</div><div>EthIf</div></div><div><div>V2G</div><div>vCanCcCdm</div><div>vCanCcGbt</div><div>vDns</div><div>vExi</div><div>vHttp</div><div>vSec</div><div>vXmlSecurity</div><div>AVB</div><div>vAvTp</div><div>vSrp</div><div>vPtp<sup>2</sup></div></div></div><div><div><div>MCAL</div><div><div>AdcDrv</div><div>CanDrv</div><div>CorTst</div><div>DioDrv</div></div><div><div>EepDrv</div><div>EthDrv</div><div>EthSwtDrv</div><div>FlsDrv</div></div><div><div>FlsTst</div><div>FrDrv</div><div>GptDrv</div><div>IcuDrv</div></div><div><div>vIlCDrv</div><div>LinDrv</div><div>McuDrv</div><div>OcuDrv</div></div><div><div>PortDrv</div><div>PwmDrv</div><div>RamTst</div><div>vCry (Hw)</div></div><div><div>SpiDrv</div><div>WdgDrv</div></div></div></div><div><div>EXT</div><div><div>CanTrcv</div><div>DrvExt<sup>1</sup></div><div>EthTrcv</div><div>FrTrcv</div></div><div><div>LinTrcv</div><div>vSbc</div></div></div></div><div><div>Microcontroller</div><div><div>Vector Standard Software</div><div>3rd Party Software</div><div>Global Time Support Modules</div><div>Affected by Global Time Support</div></div><div><div><div><sup>1</sup>Includes ExtAdc, EepExt, FlsExt, EthSwtDrvExt, EthDrvExt and WdgExt</div><div><sup>2</sup>Functionality represented in EthTSyn and StbM</div></div></div></div></div>	
Terms (Source: AUTOSAR SWS STBM):		

Type	Description	Change ID
	 <p>TS: Time slave TM: Time master TG: Time gateway</p>	
Extension	Time master nodes: Multiple timing domains are not supported by CanTsyn and FrTsyn within a single PDU. The past limitation was removed.	FEAT-2517

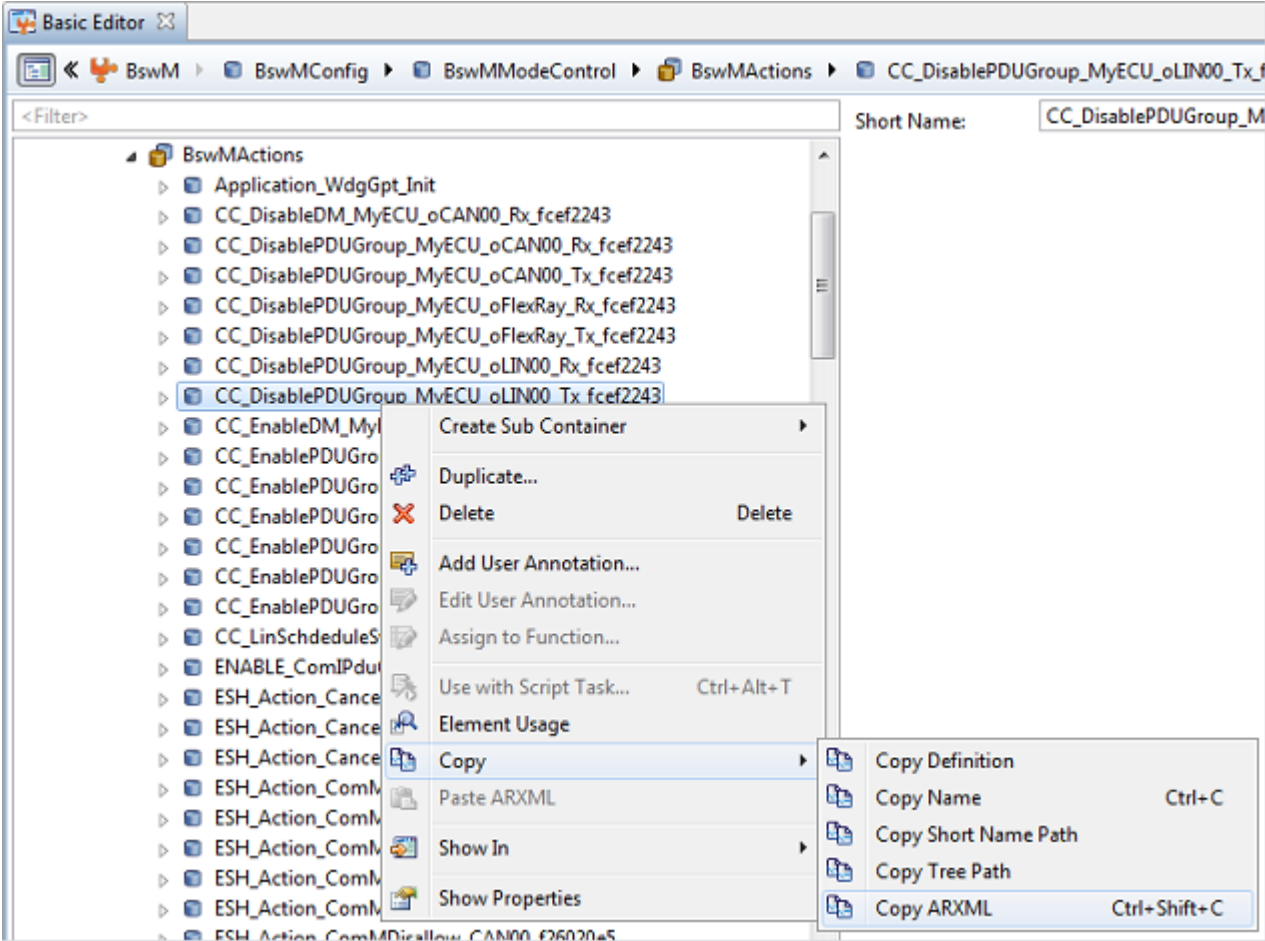
## V2G

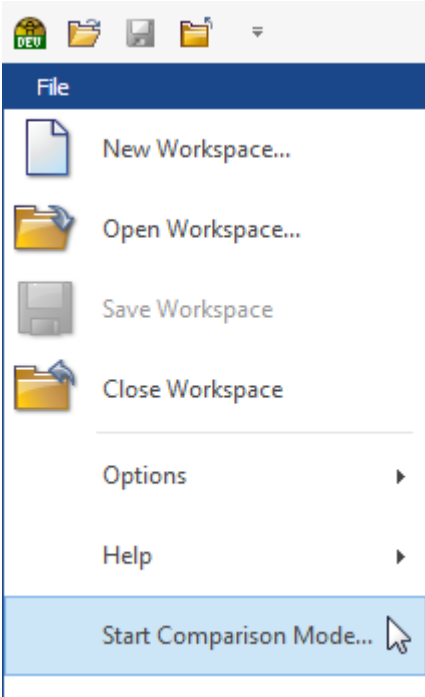
Type	Description	Change ID
Extension	The charging standard GB/T 27930 is now supported by a new MICROSAR component vCanCcGbt.	FEAT-2534
Extension	The charging standard CHAdeMO is now supported by a new MICROSAR component vCanCcCdm. Initially v1.1 of	FEAT-2752

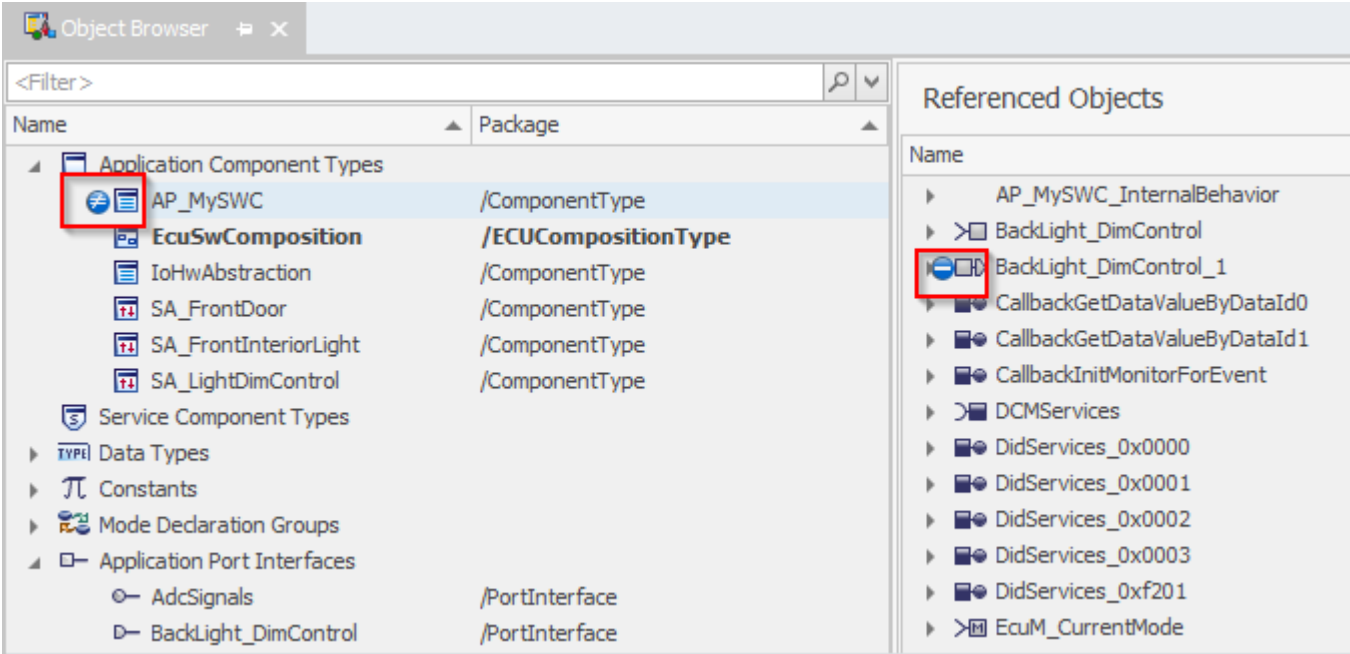
Type	Description	Change ID
	the standard has been realized.	

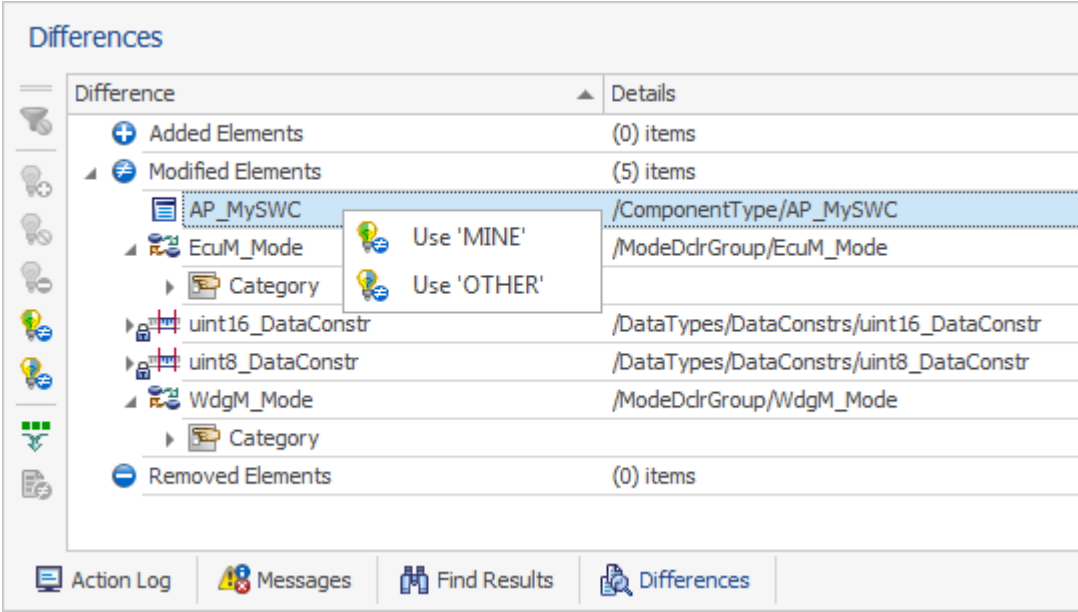
**Tooling**

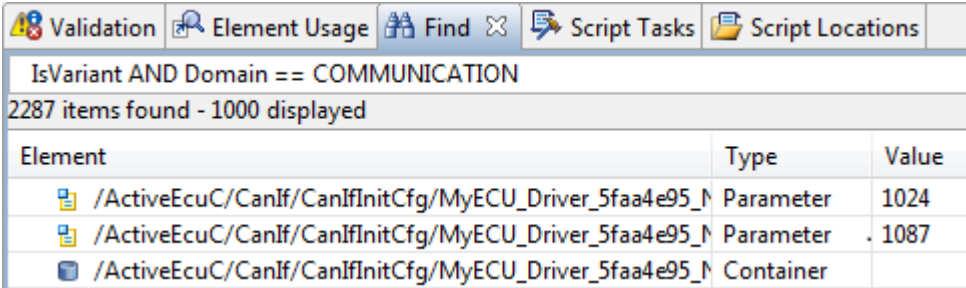
Type	Description	Change ID
Extension	DaVinci Configurator Pro supports copy and past operation of configuration data. This allows easy to use configuration data exchange e.g. between different projects or configuration trees.	FEAT-2481

Type	Description	Change ID
	 <p><b>Additional Information</b></p> <p><a href="#">Copy and paste of configuration elements</a></p> <p><a href="#">Copy grids to CSV format</a></p>	

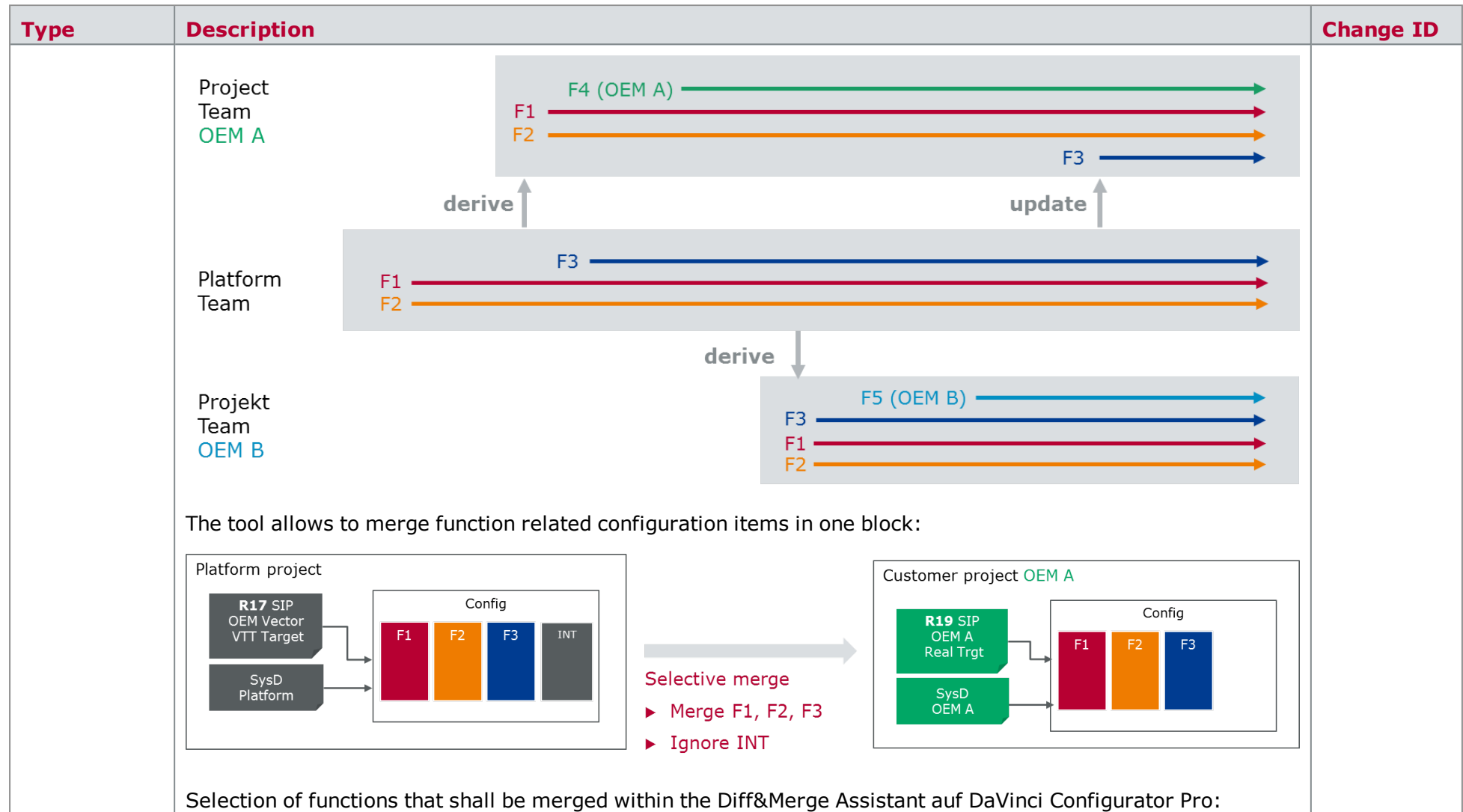
Type	Description	Change ID
Extension	<p>DaVinci Developer has an improved diff- and merge functionality that allows selective merge decisions. The UI has been reworked to give a user experience similar to the diff- and merge functionality of DaVinci Configurator Pro.</p> <p>Launch the difference mode:</p>  <p>Highlevel difference illustration in Object Browser using icons:</p>	FEAT-2494

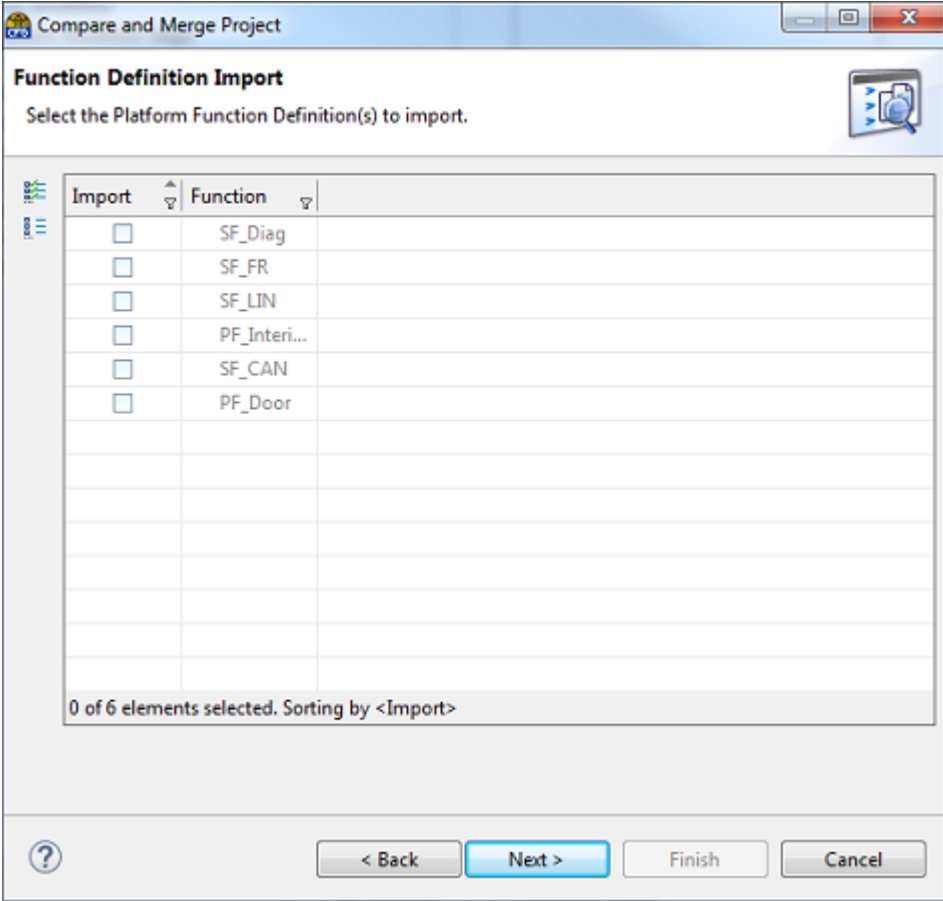
Type	Description	Change ID
	<div><p>The Differences View shows more details and allows resolving differences in the toolbar:</p></div>	

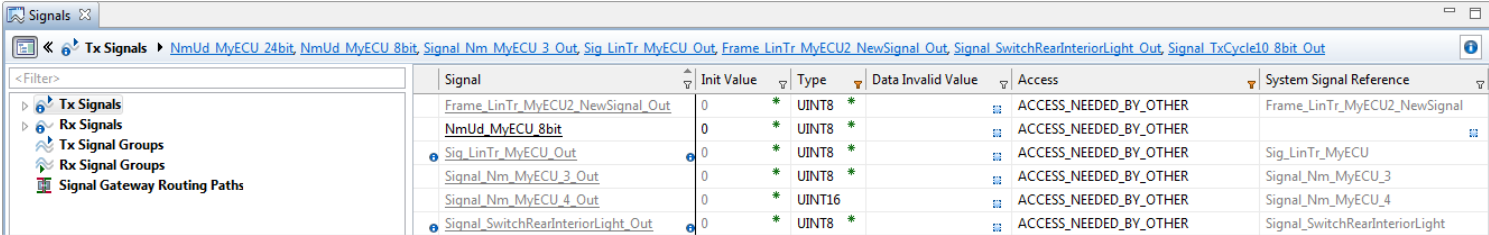
Type	Description	Change ID
		
Extension	<p>The configuration report provided by DaVinci Configurator Pro can now be created via command line.</p> <div data-bbox="421 970 996 1433"> <p><b>DaVinci Configurator Report</b></p> <p>CanLinFrlpMemWd V1.0 by vissvh</p> <p>0 Modules, 0 Containers, 0 Parameters</p> <p>Sep 29, 2017 2:35 PM</p> <p><b>Module Overview</b></p> <p><a href="#">Adc</a></p> <p><a href="#">AdcConfigSet</a></p> <p><a href="#">AdcConfigSet.AdcHwUnit</a></p> <p><a href="#">AdcConfigSet.AdcHwUnit.AdcChannel</a></p> </div>	FEAT-2553

Type	Description	Change ID
Extension	Central display option in DaVinci Configurator Pro to reference parameters either with full path or only as short name	FEAT-2553
Extension	<p>The Find View of DaVinci Configurator Pro now provides a search criterion "IsVariant" that allows searching for elements that are actually different in the configured variants.</p>  <p>Use the Help dialogue to find out more about the different query possibilities. Alternatively you can hit "CTRL + SPACE" in the query editor to get a context sensitive list of possible options.</p>	FEAT-2553
Extension	Product line approach supported by DaVinci Configurator Pro and DaVinci Developer: SWCs and ECUC containers can be assigned to platform functions. Selective diff/merge enables simple take-over of complete platform functions from a baseline project to individual customer projects.	FEAT-2785





Type	Description	Change ID
		
Extension	<p>The UI of DaVinci Configurator has been improved:</p> <ul style="list-style-type: none"> <li>&gt; Basic editor: the expansion and selection state of the tree is restored after switching the displayed variant</li> <li>&gt; Grids now supports multiple filtered columns</li> </ul>	FEAT-2791

Type	Description	Change ID
	 <p>&gt; If a new element is created in the tree this new element is selected automatically</p>	
Extension	The CAN baudrate configuration has been improved for devices that result in many possible register settings.	FEAT-2879
Extension	<p>DaVinci Configurator Pro.WF now provides more powerful automation interface APIs to access and modify the task- and data mapping as well as the creation of component prototypes.</p> <p>This feature allows automation scripts to be created with less effort as the APIs abstract from the complex AR data structure.</p>	FEAT-2942
Extension	When migrating a project from one SIP to another the derivative selection is now migrated.	FEAT-3011
Extension	<p>DaVinci Configurator Pro now provides the possibility to split a SystemTemplate with prebuild variance into non variant configuration files that can be used in the process to setup a new project.</p> <p>The variant split is available as command line option of DaVinci Configurator Pro (DVCfgCmd.exe): using --exportPreBuildVariants &lt;VARIANTS&gt;</p> <p>Find out the full command and more details in the Help dialogue.</p>	FEAT-3095
Extension	DaVinci Configurator Pro now blocks the possibility to configure PublishedInformation (defined in the BSWMD) as it is defined by AUTOSAR. This feature is currently limited to non MICROSAR modules.	STORY-1021
Information	DaVinci Developer has been realized as a 64-bit application to support large projects with more available memory.	FEAT-2944