

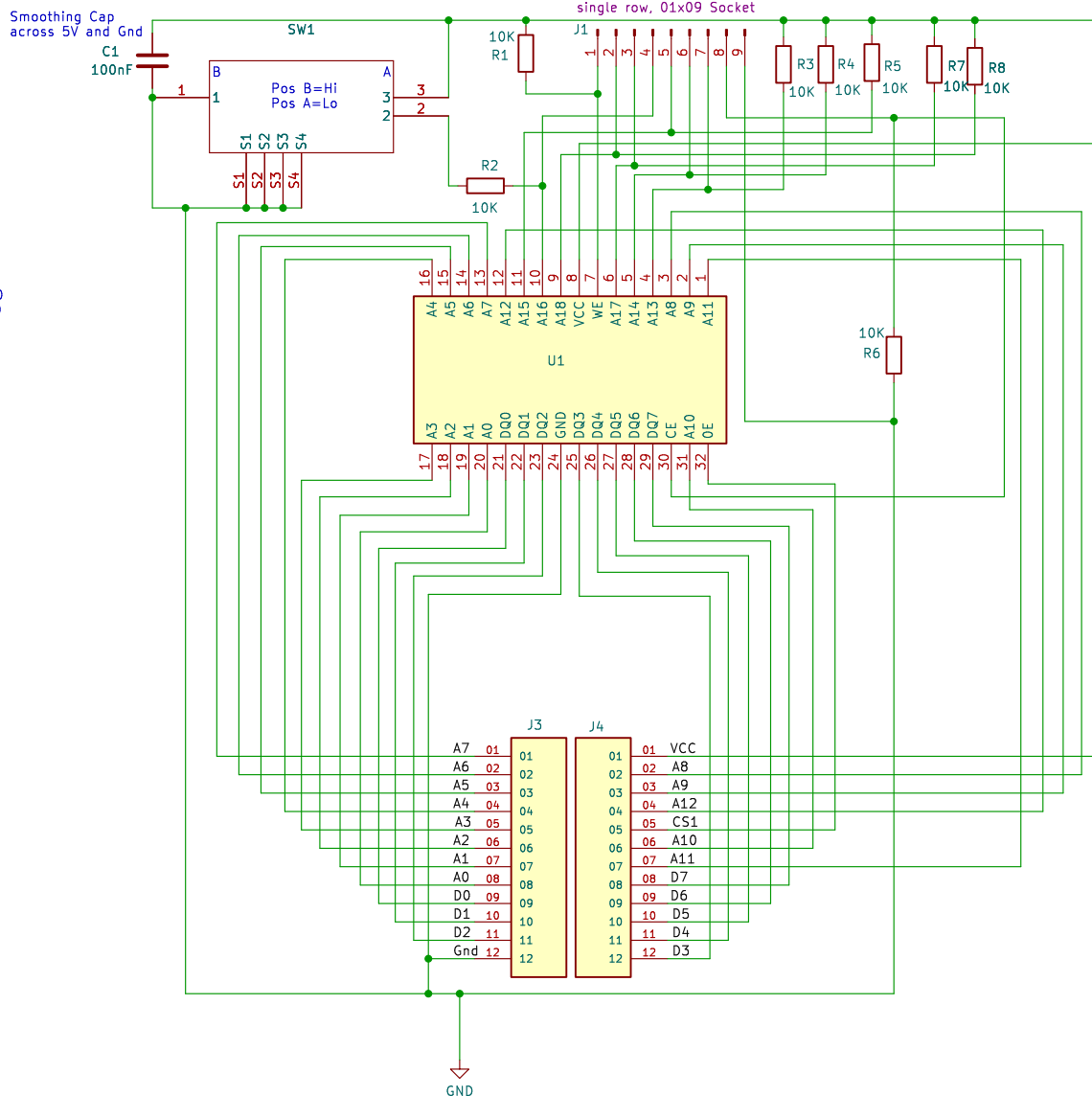
J1 pin 1 -WE pulled high in operation by 10K resistor R1 on ROM board.  
 J1 pin 2 A18 pulled high in operation by 10K resistor R8 on ROM board.  
 J1 pin 3 A17 pulled high in operation by 10K resistor R7 on ROM board.  
 J1 pin 4 A16 selects ROM to use, pulled high or low in operation by switch and 10K resistor R2 on ROM board. Includes switching of A16 if internal switch set to position B.  
 J1 pin 5 A15 pulled high in operation by 10K resistor R5 on ROM board.  
 J1 pin 6 A14 pulled high in operation by 10K resistor R4 on ROM board.  
 J1 pin 7 A13 pulled high in operation by 10K resistor R3 on ROM board. If connected with a wire link to address A13 on IF1 PCB allows use of 16K ROM (Ian Collier).  
 J1 pin 8 CE pulled low in operation by 10K resistor R6 on ROM board.  
 J1 Pin 9 0V Allows pulling down of address lines J1 Pin 1 to Pin 8 by external switches but can be left unconnected if the switches are externally connected to ground.

To program,  
 Connect to Adapter PCB and link J1 on Adapter PCB and J1 on ROM PCB.  
 Pins 1-1,2-2,3-3,4-4,5-5,6-6,7-7 and 8-8. J1 Pin 9 is not connected.  
 Set switch on ROM PCB to position B

- 1 - Select correct EEPROM in programmer and Erase as normal. 39SF040 /39SF020 /39SF010
- 2 - To use default switched A16 load 1st ROM into programmer at 0x6E0000/0x2E0000/0X0E0000
- 3 - To use default switched A16 load 2nd ROM into programmer at 0x7E0000/0X3E0000/0X1E0000 without clearing previous ROM.
- 4 - Load other ROM's into programmer as required with the IF1 addressing based on the position of external switches.  
 A18 switches every 0x40000 (SST39SF040 Only)  
 A17 switches every 0x20000 (SST39SF020 and 040 Only)  
 A16 switches every 0x10000 if internal switch set to position B.  
 A15 switches every 0x08000  
 A14 switches every 0x04000  
 A13 switches every 0x02000 (Do not connect to switch if using 16K images as this needs to be connected to A13 on the IF1)

A mix of switches is allowed and will increase the number of ROM positions available.

- 5 - program as normal."



Correct ROM addresses for programming  
 DO NOT EXTERNALLY SWITCH A13 IF USING 16K IMAGES

Add 0V connection to J1 allowing external switching of A14/A15/A16/A17 and A18.

**Andy Potter**

Sheet: /

File: ZX Interface 1 ROM 39SF010.kicad\_sch

**Title: ZX Interface 1 ROM Replacement SST39SF0x0**

Size: A4

Date: 2025-10-26

Rev: 1.2

KiCad E.D.A. 9.0.5

Id: 1/1