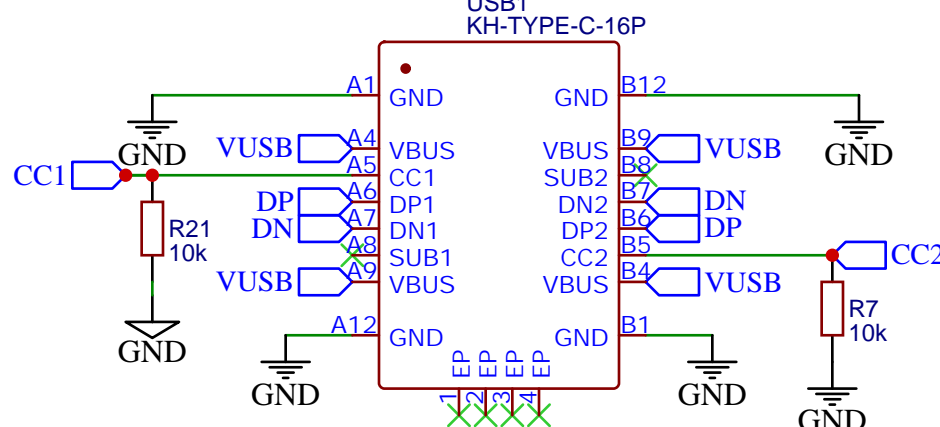
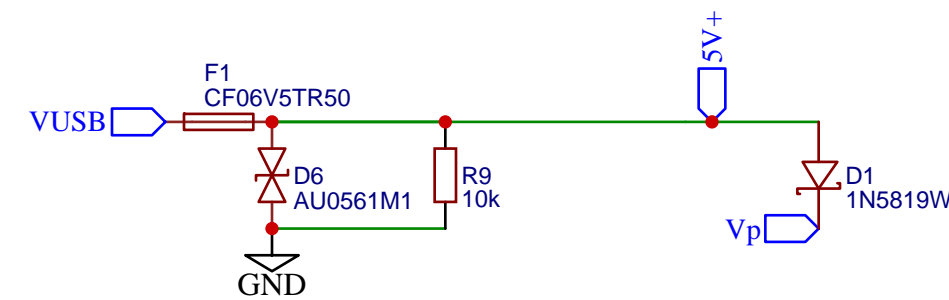
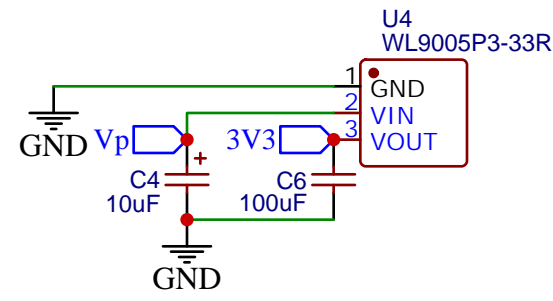


[illegible]

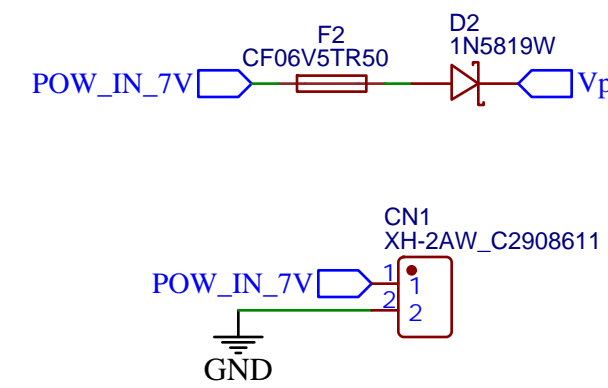
# Vp to 3.3V

The diagram shows a circuit for converting a voltage  $V_p$  to 3.3V. A green wire labeled  $V_p$  is connected to a 100nF capacitor ( $C_4$ ) to ground and a 3V3 voltage divider. The 3V3 divider consists of two resistors in series connected to the green wire. The output of the divider is connected to the VIN pin of a buffer (U4, WL9005P3-33R). The buffer's GND pin is connected to ground, and its VOUT pin is connected to a 100uF capacitor ( $C_6$ ) to ground. The output of the buffer is also connected to the green wire.

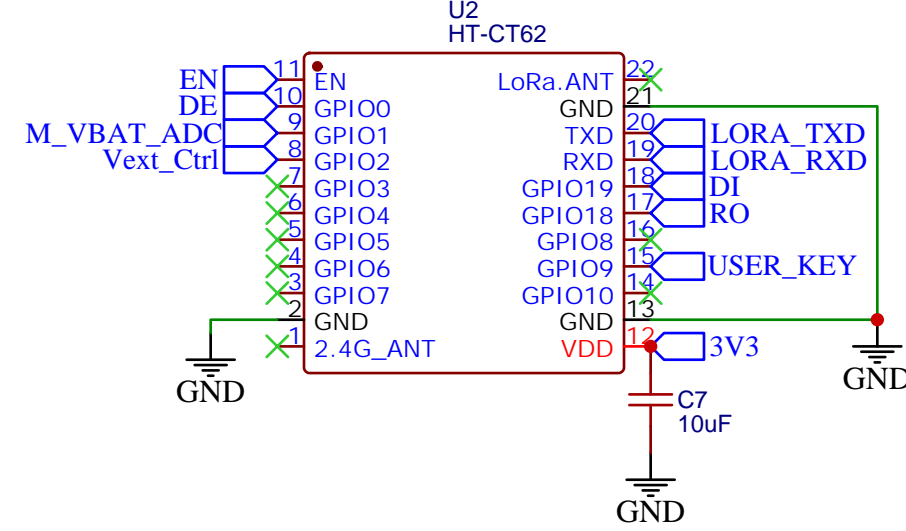
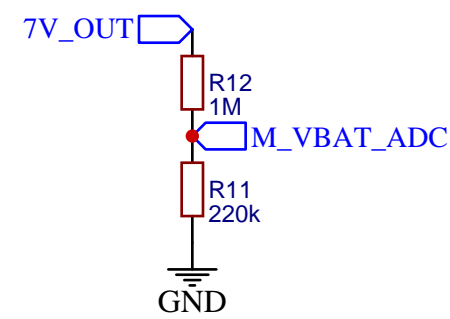


# Power In 7V

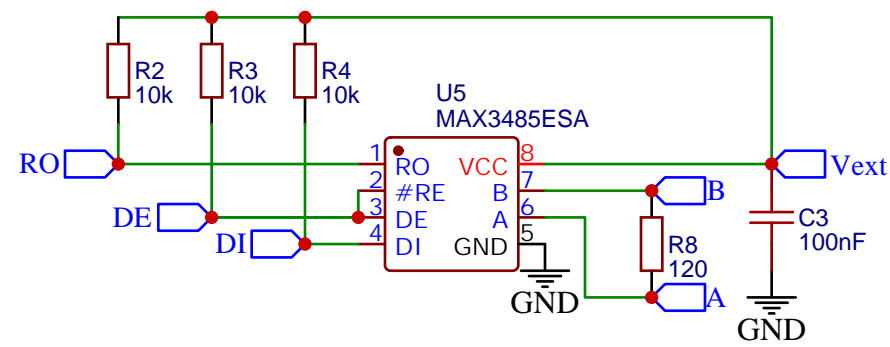
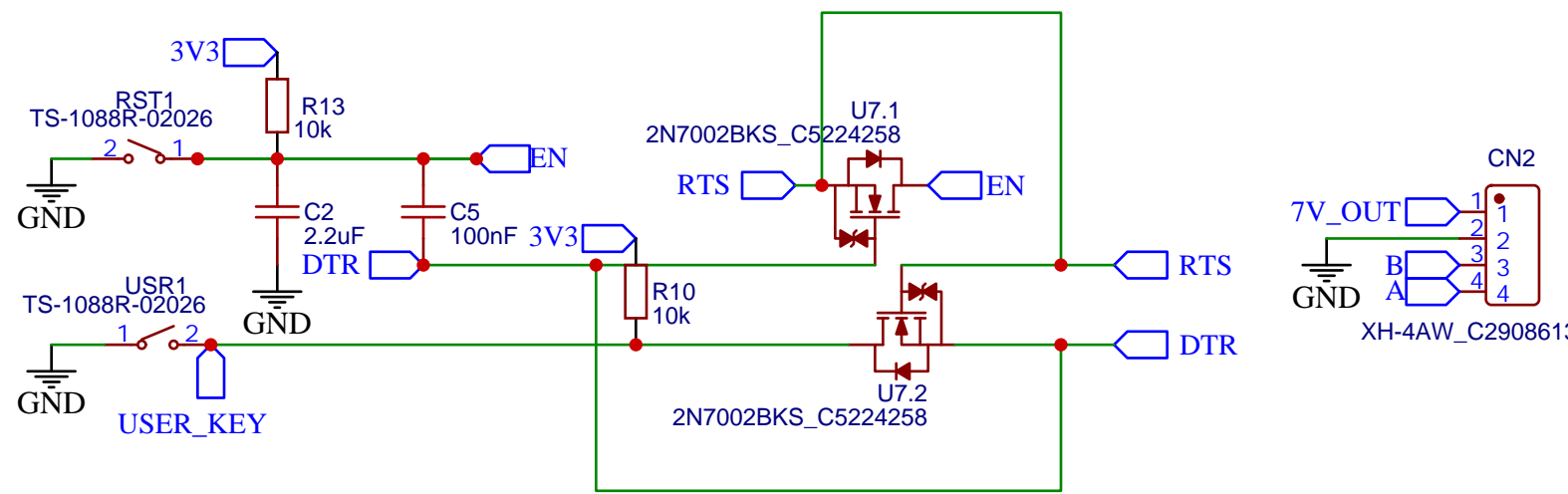
The diagram illustrates a power input circuit. It starts with a 7V input (POW\_IN\_7V) connected to a 0.6V 5W resistor (CF06V5TR50). This is followed by a 1N5819W diode (D2) in series with a 7V output (Vp). A ground connection (GND) is also shown.



# LORA CT62



# RS485

[illegible]

## EXT 7V Ctrl

Q4  
AO3401A\_C347476

7V\_OUT

Vp

R5  
10k

Q5  
AO3400A

Vext\_Ctrl

R6  
10k

GND

Q2  
AO3401A\_C347476

3V3

Vext

R14  
10k

R1  
1k

Vext\_Ctrl

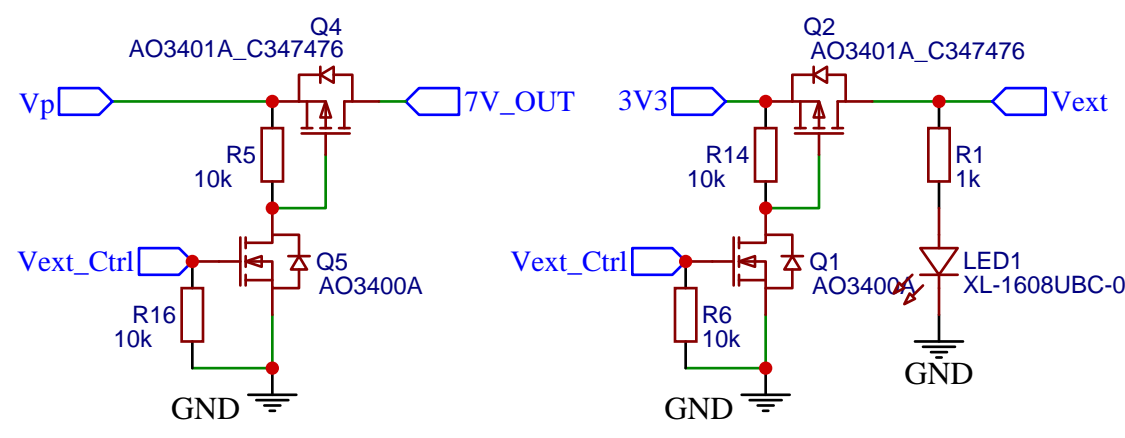
R6  
10k

GND

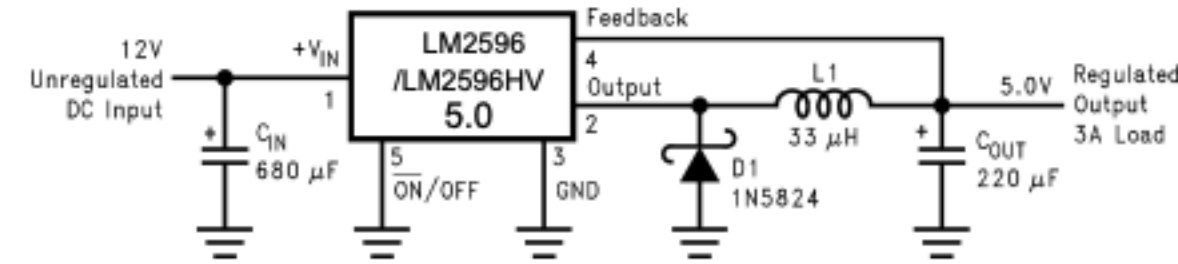
LED1  
XL1608UBC-04

GND

Q4 on when EXT\_7V\_Ctrl = HIGH



Q4 on when EXT\_7V\_Ctrl = HIGH



HT-CT62

