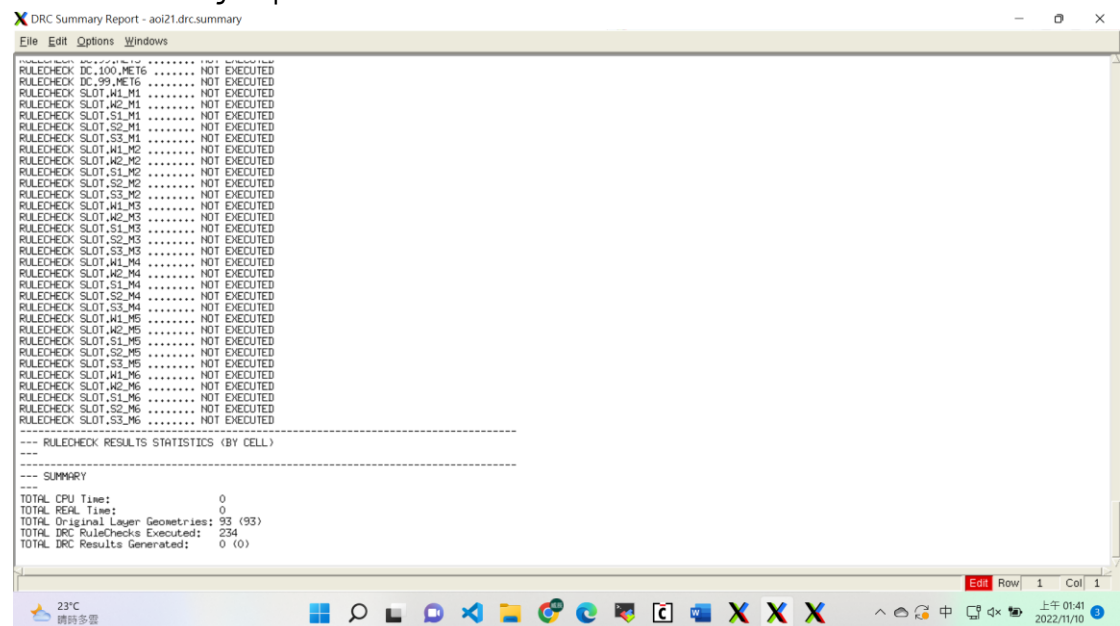
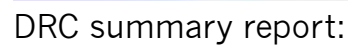
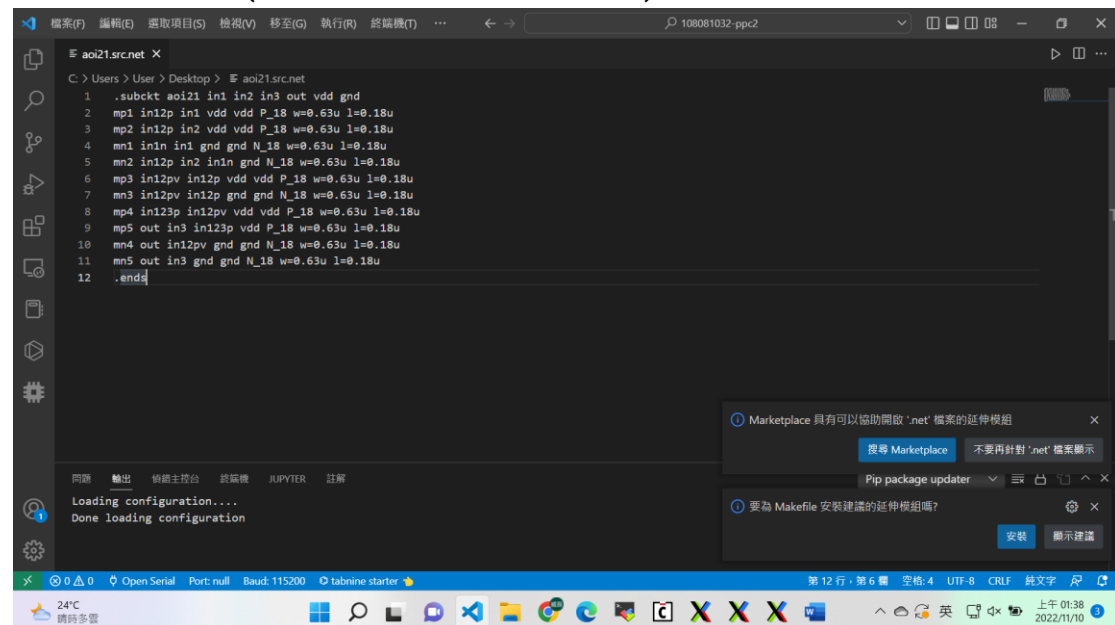


Layout with rulers:

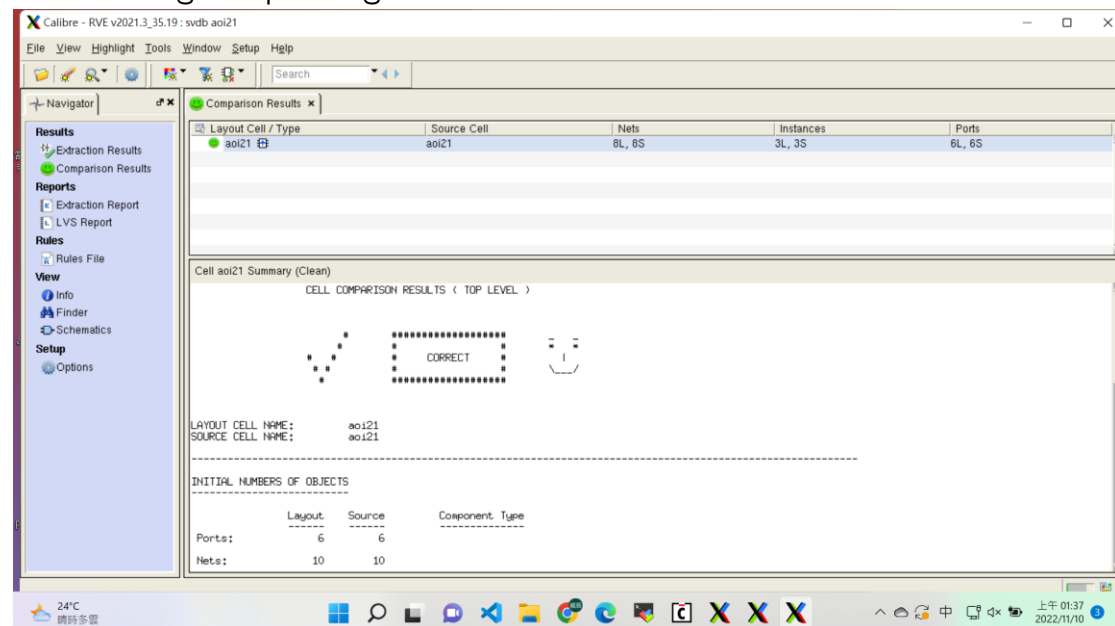


LVS schematic (screenshot of the text file):



```
1 .subckt aoi21 in1 in2 in3 out vdd gnd
2 mp1 in12p in1 vdd vdd P_18 w=0.63u l=0.18u
3 mp2 in12p in2 vdd vdd P_18 w=0.63u l=0.18u
4 mn1 in1n in1 gnd gnd N_18 w=0.63u l=0.18u
5 mn2 in12p in2 in1n gnd N_18 w=0.63u l=0.18u
6 mp3 in12pv in12p vdd vdd P_18 w=0.63u l=0.18u
7 mn3 in12pv in12p gnd gnd N_18 w=0.63u l=0.18u
8 mp4 in123p in12pv vdd vdd P_18 w=0.63u l=0.18u
9 mp5 out in3 in123p vdd P_18 w=0.63u l=0.18u
10 mn4 out in12pv gnd gnd N_18 w=0.63u l=0.18u
11 mn5 out in3 gnd gnd N_18 w=0.63u l=0.18u
12 .end
```

The message of passing LVS:



What else did you do to enhance your layout quality?

用 A 鍵去一個個對邊和點，item 間距和大小盡量固定，連接 metal 的 contact 在 vdd、gnd 上盡量多，增大電流

What have you learned from this homework?

我覺得 layout 有很多細節要顧，尤其是 item 間距要慢慢試，而且一開始空白時會不知道要從哪裡下手，但後來發現從小到大會最好畫，速度變越來越快。再來就是 schematic file 有一些地方要注意很容易打錯，總之這個作業讓我細心程度大增。

What problem(s) have you encountered in this homework?

一開始完全不知道這個軟體要怎用和 schematic file 要如何寫，信好也都查的到，甚至繪圖 layout 還有影片帶著做，還有這個作業要花不少心思去修改，會覺得挺麻煩的，但總之已經完成了 XD。