

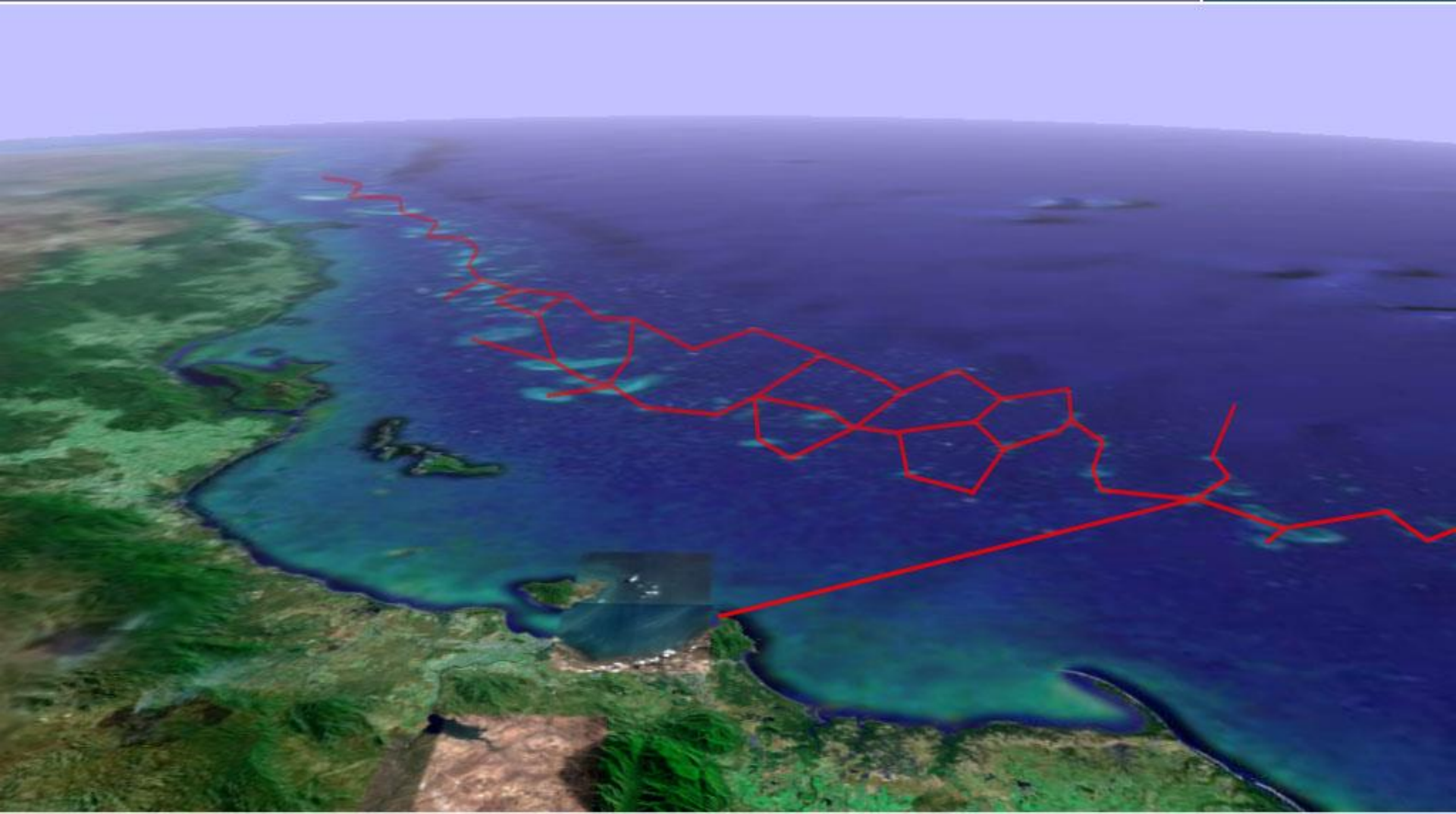
Remote Ad Hoc Sensor Networks



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Remote Ad Hoc Sensor Networks



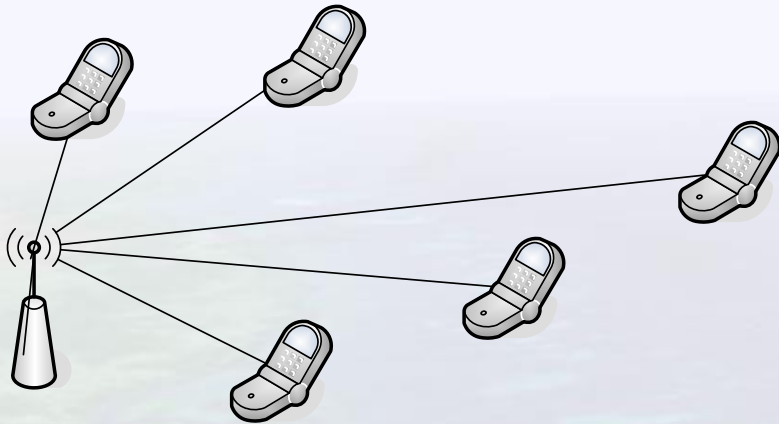
Introduction

Project Overview

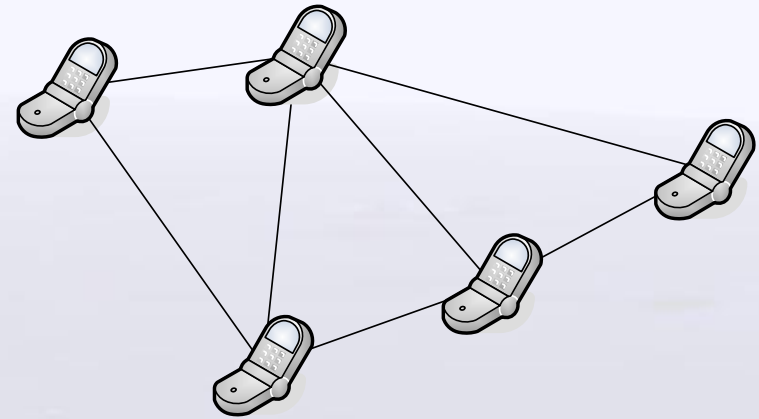


- Australia is a huuuuge country
- Remote and inaccessible locations
- Challenging communication scenarios
- An alternative approach: Ad Hoc Networking

What is an Ad-Hoc Network?



Cellular Network



Ad Hoc Network

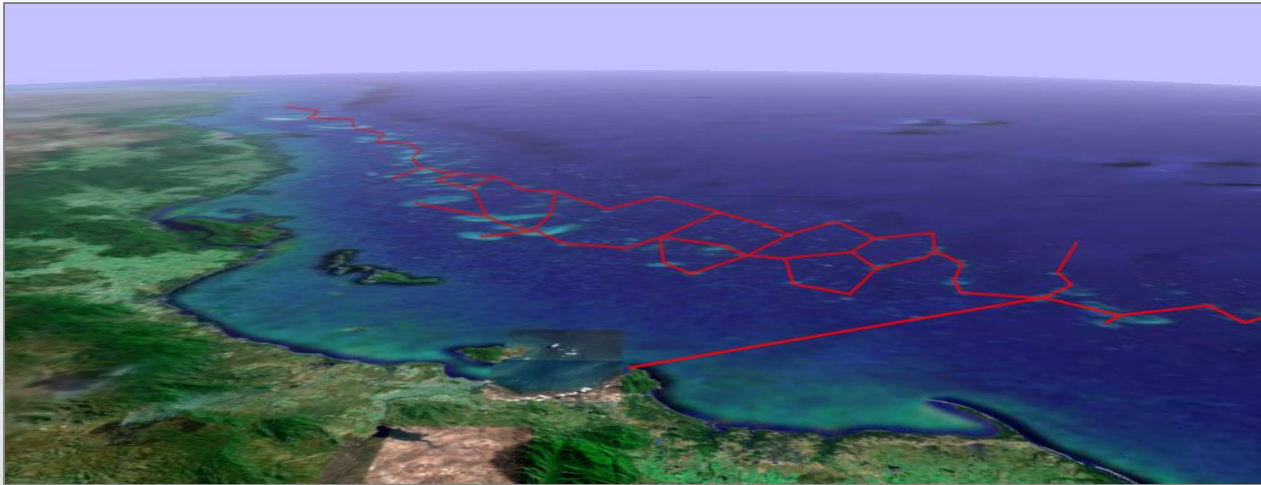
- Peer to peer network
- Distributed infrastructure
- Automatic and adaptive configuration
- Energy efficient

Typical Ad Hoc Sensor Networks

- Network Conditions
 - Dense, well connected topologies
 - Low traffic rates
- Tiny sensor nodes
 - Data collection
 - Data processing
 - Low power radios
- Long battery life



Crossbow “Mica” Motes



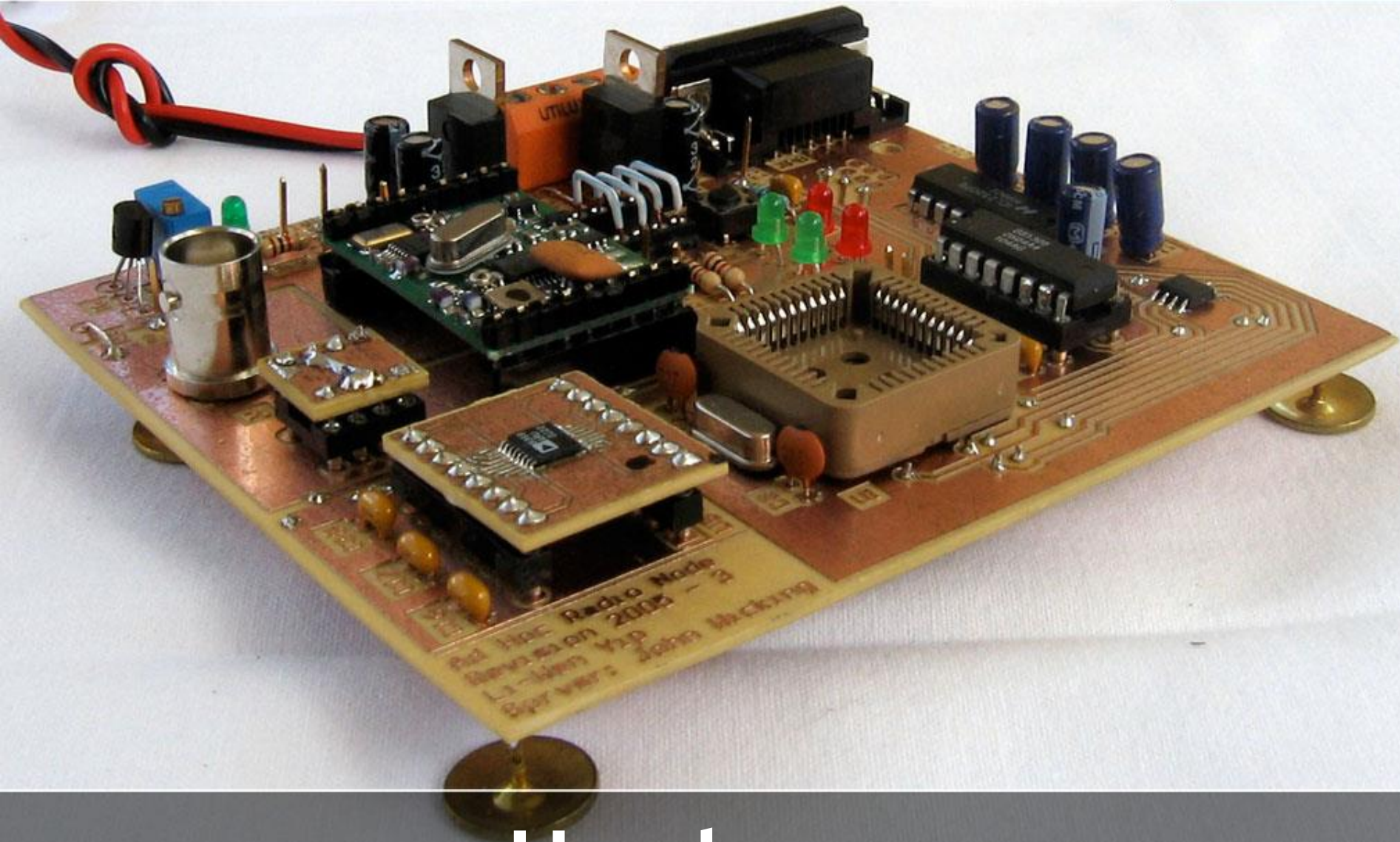
- Size
 - Large geographical areas
 - Remote and inaccessible locations
- Network Conditions
 - Sparse, minimally connected topologies
 - Large link distances (up to 10km)
 - Exposure to environmental conditions

- Data collection
 - Great Barrier Reef monitoring stations
 - Water trough monitoring on cattle properties
- Rapidly deployable communications
 - Natural disasters
 - Battlefields



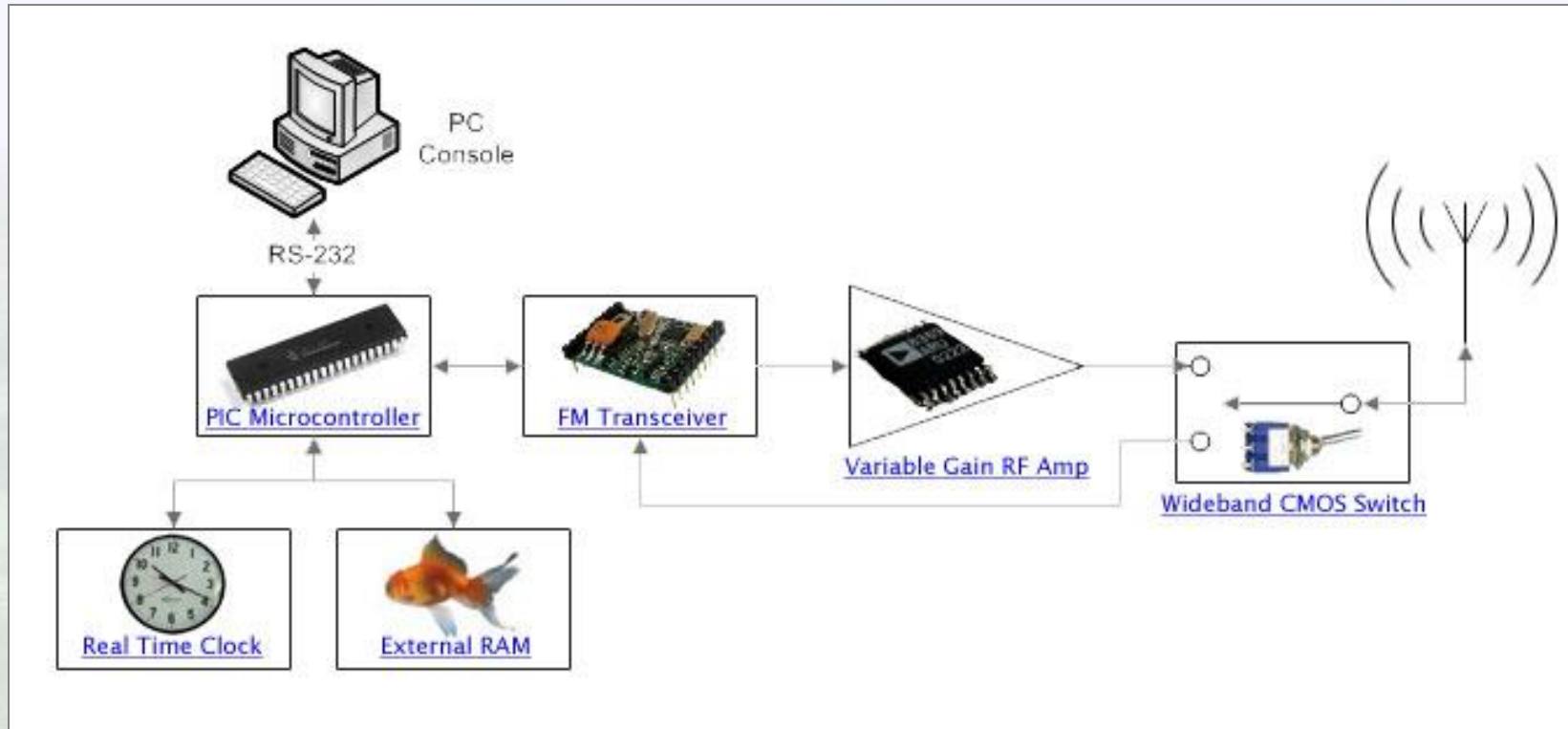
Davies Reef
Remote Weather Station

Remote Ad Hoc Sensor Networks



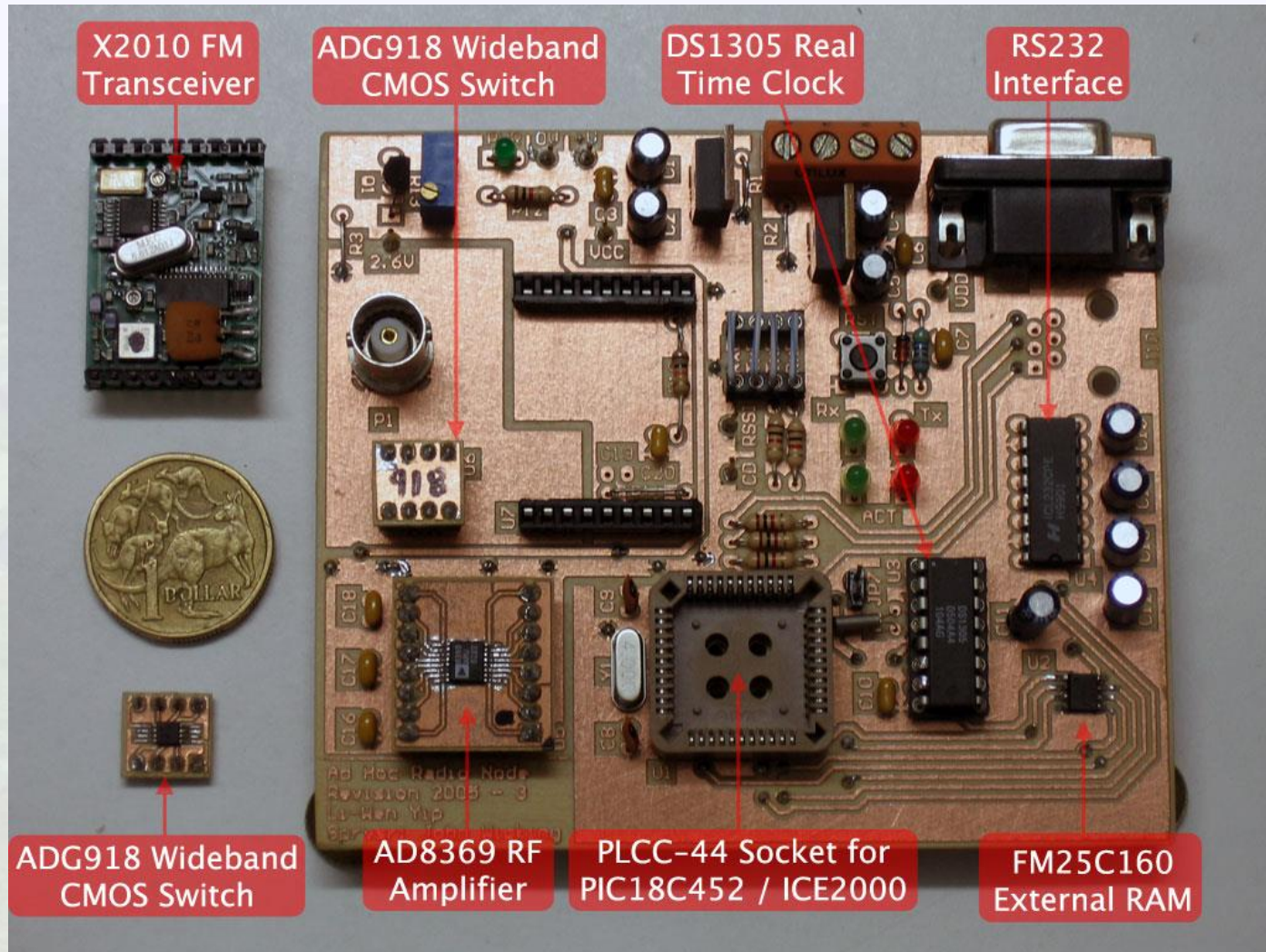
Hardware

Hardware Overview

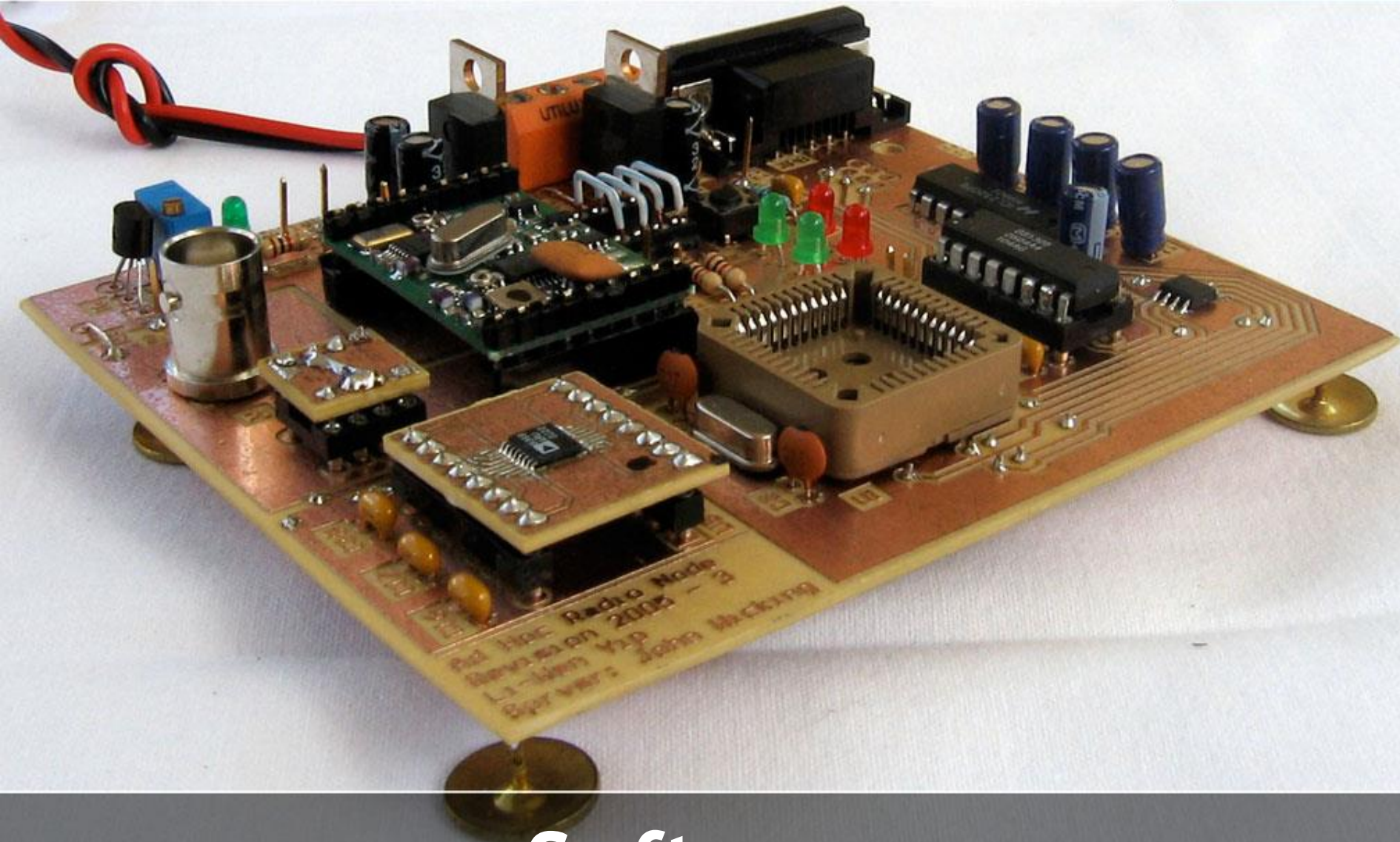


- Microcontroller based (PIC18C452)
- Programmable output power
- Cache routing information
- Schedule sleep periods
- CMOS Switch only consumes 5 μ A

Here's one I prepared earlier...



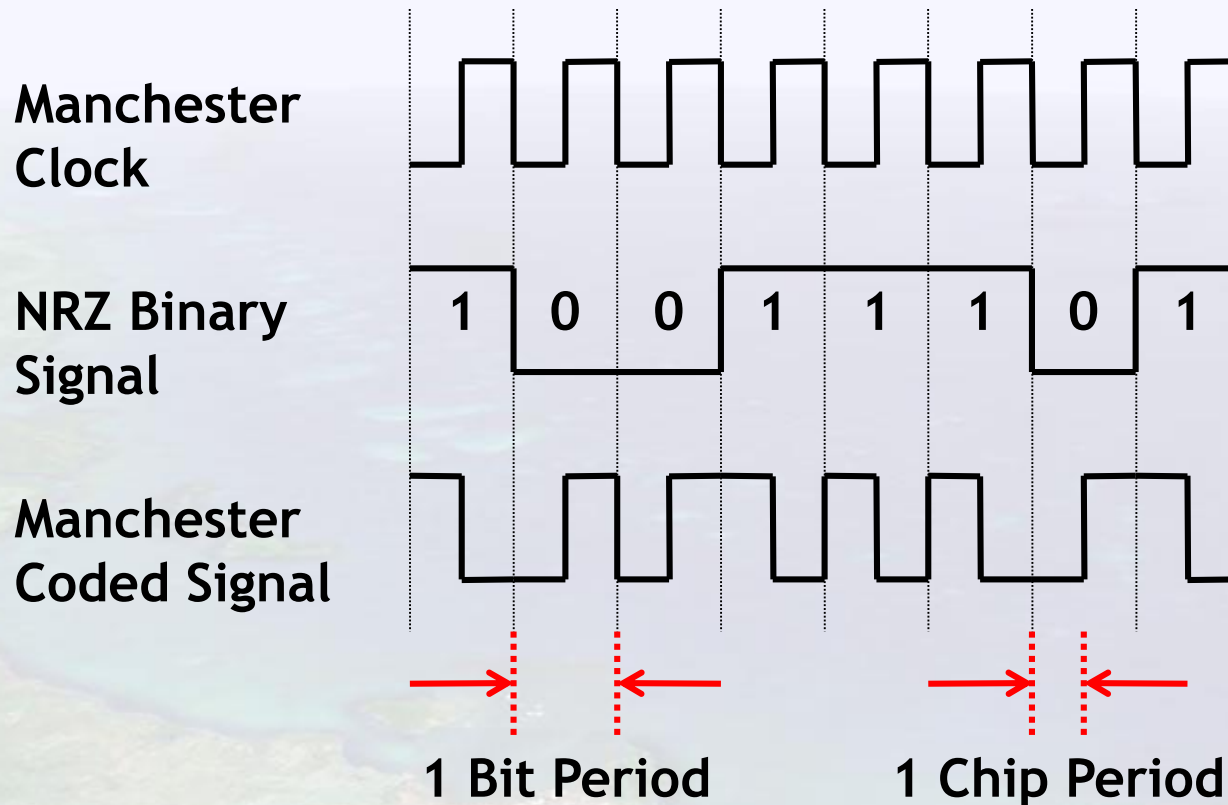
Remote Ad Hoc Sensor Networks



Software

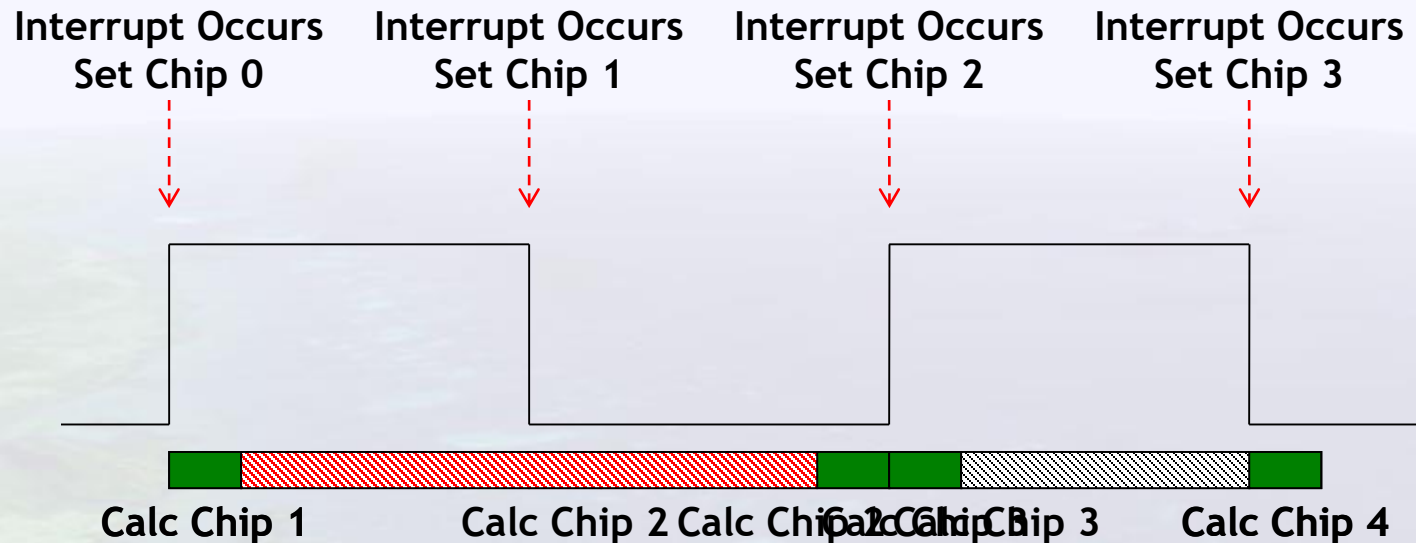
- **Manchester Encoding/Decoding**
- Power Saving Techniques
- Dynamic Address Allocation

Manchester Encoding / Decoding



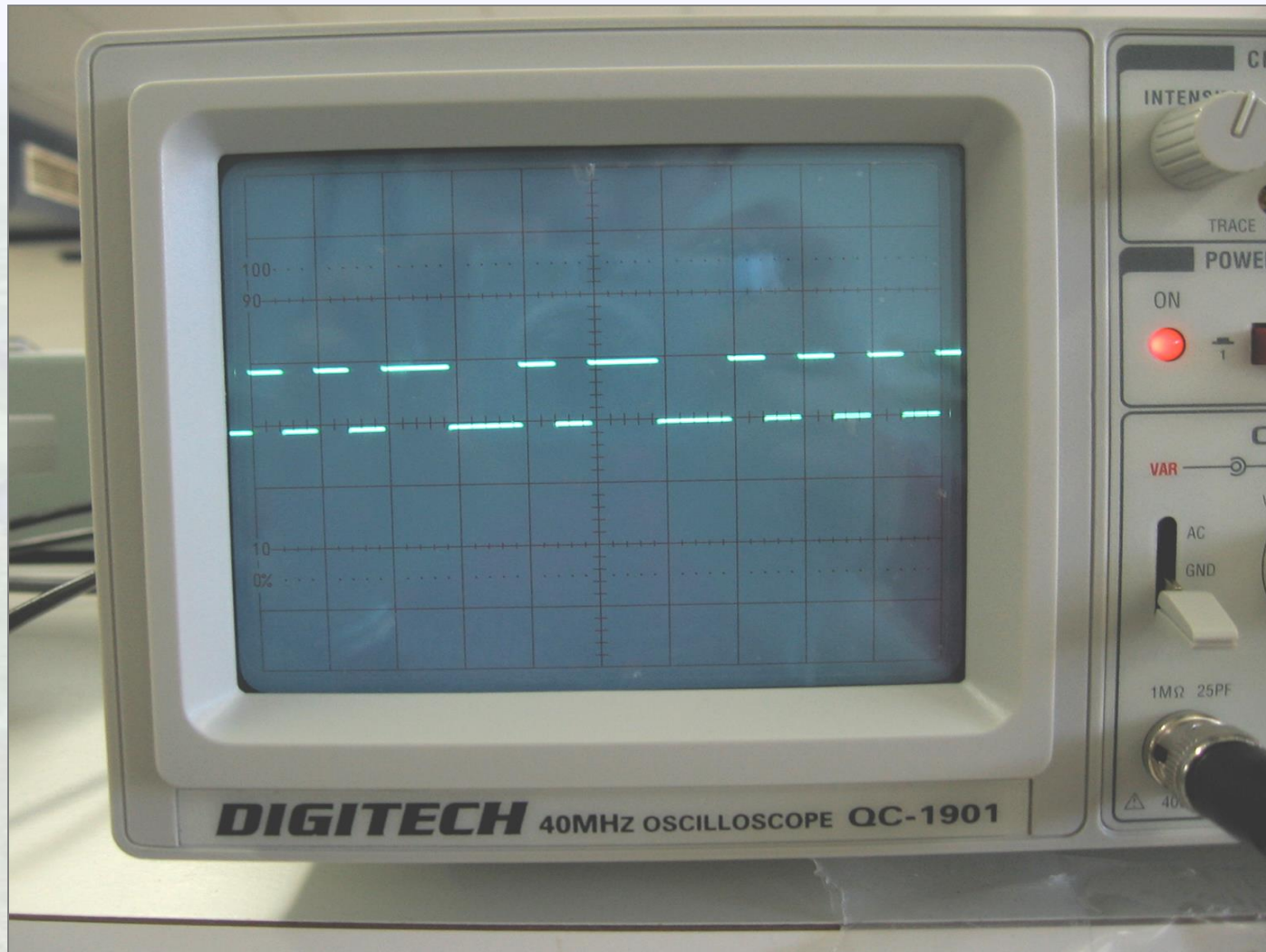
- 1 -> 10; 0 -> 01
- Required for the X2010 FM Transceiver

Manchester Encoding

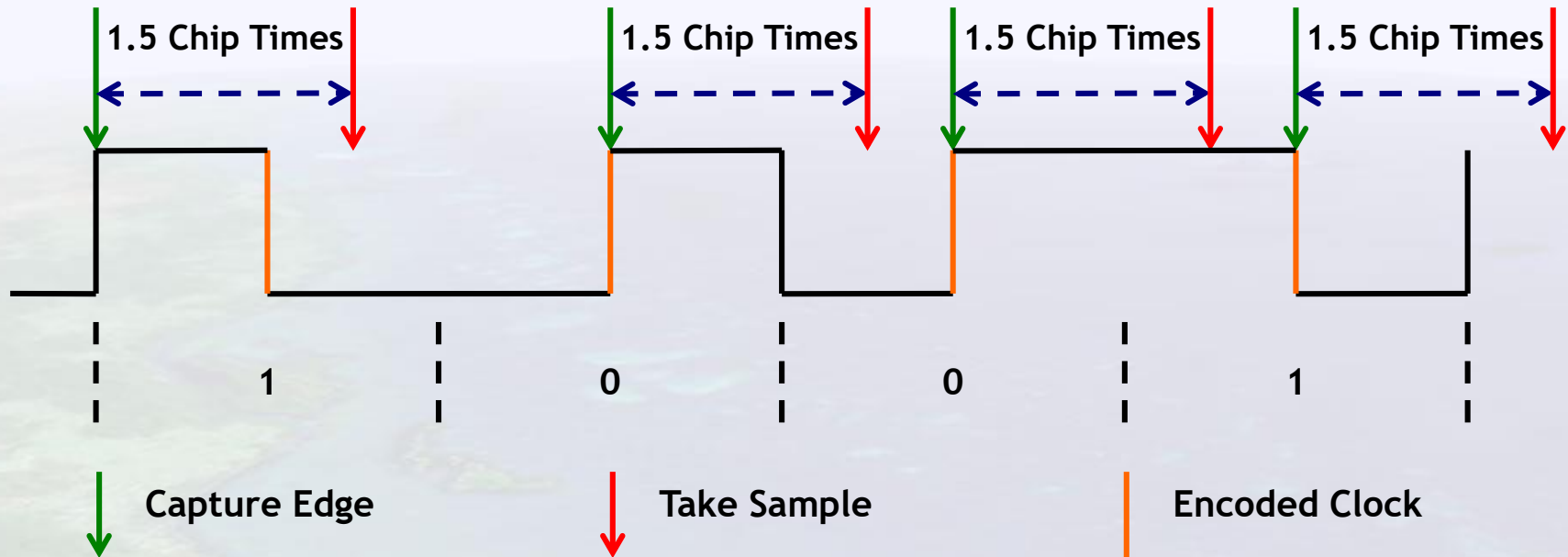


- Each change is calculated in advance and scheduled
- Not dependent on calculation time
- Allows for some slack
- Perform data fetching under interrupt

Manchester Encoding



Manchester Decoding



- Previous method involved start and stop bits
- Synchronise to the encoded clock
- Locks as soon as the data bit changes
- Extremely efficient - runs at 9.6 kbps

Manchester Decoding

MPLAB IDE v7.00

File Edit View Project Debugger Programmer Tools Configure Window Help

Checksum: 0x5270

Watch

Symbol Name	Value	Decimal
TMR3	0x3C9F	15519
CCPR1	0x3BB2	15282
txbyte	0x20	32
bitcount	0x00	0
shiftreg_l	0x8B	139
shiftreg_h	0xDA	218
WREG	0x13	19
rx_flags	0x03	3
rxbuf	0x13	19
bytecount	0x00	0

MPLAB IDE Editor*

Main.ASM Buffers.inc swstack.inc Buffers v5.asm MediaAccess.asm connections.inc

```
1203      tstfsz crchigh      ; Is the high byte zero?
1204      bra      _CRC_BAD   ; NO - CRC is bad.
1205      tstfsz  crcloiw      ; Is the low byte zero?
1206      bra      _CRC_BAD   ; NO - CRC is bad.
1207
1208      ; The CRC was good - notify the network layer that we have received a
1209      _CRC_GOOD:
1210      bsf      packet_received ; Set the packet received flag.
1211      return    ; Go pikachu!
1212
1213      ; The CRC was bad - wait for a new frame.
1214      _CRC_BAD:
1215      clrf     rx_flags      ; Clear all flags.
1216      clrf     bitcount     ; Reset the bit counter.
1217      return    ; Turns the antenna into a mushroom.
1218
```

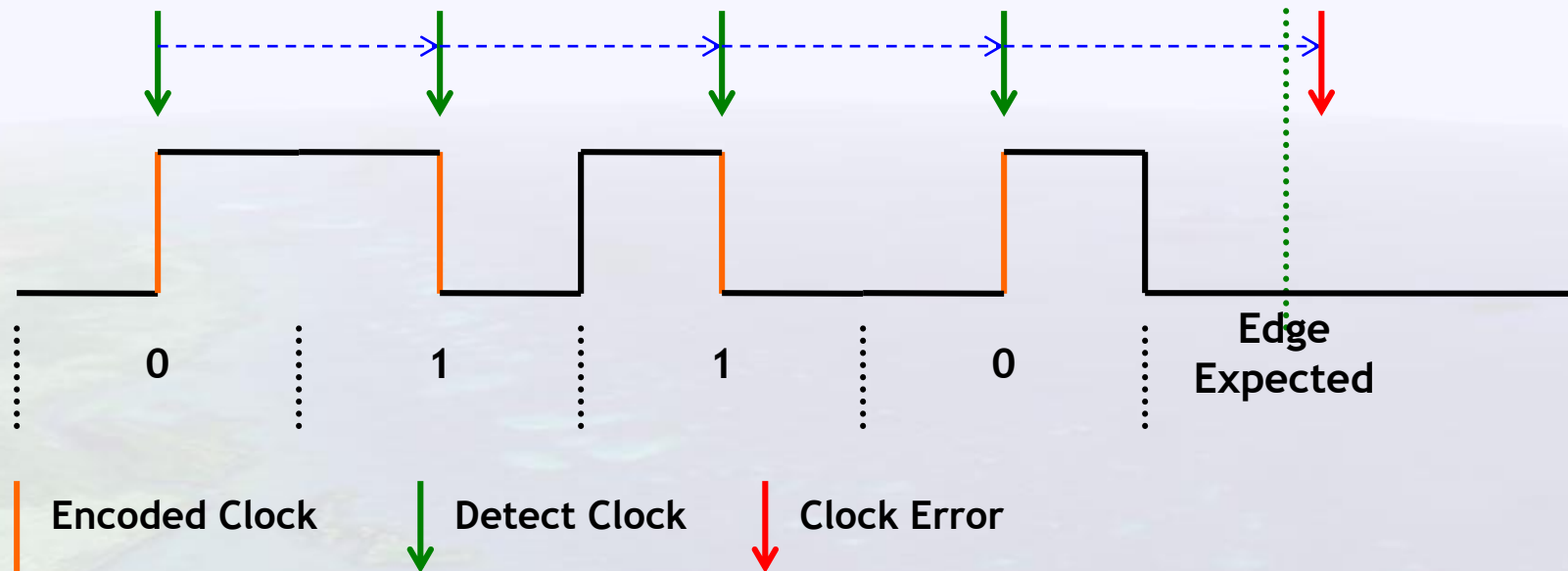
File Registers

Address	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	ASCII
0050	00	88	DD	F7	10	20	D7	B9	19	30	7D	EA	A0	0A	37	EE0)...7.
0060	01	24	76	7F	12	02	FF	71	68	84	ED	FF	40	00	CC	B7	.\$v....q h...@...
0070	00	04	7F	FF	10	00	7F	DF	08	20	5F	FF	2A	00	4B	EF_*.K.
0080	13	13	54	68	65	20	41	6E	73	77	65	72	20	49	73	20	..The Answer Is
0090	34	32	21	8B	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	42!.....
00A0	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA
00B0	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA	DA
00C0	12	12	00	11	22	33	44	55	66	77	88	99	AA	BB	CC	DD"3DU fw.....
00D0	FF	FF	FF	20	20	41	F7	FF	04	81	BE	F7	90	70	7D	DF	... &.....n).

Hex Symbolic

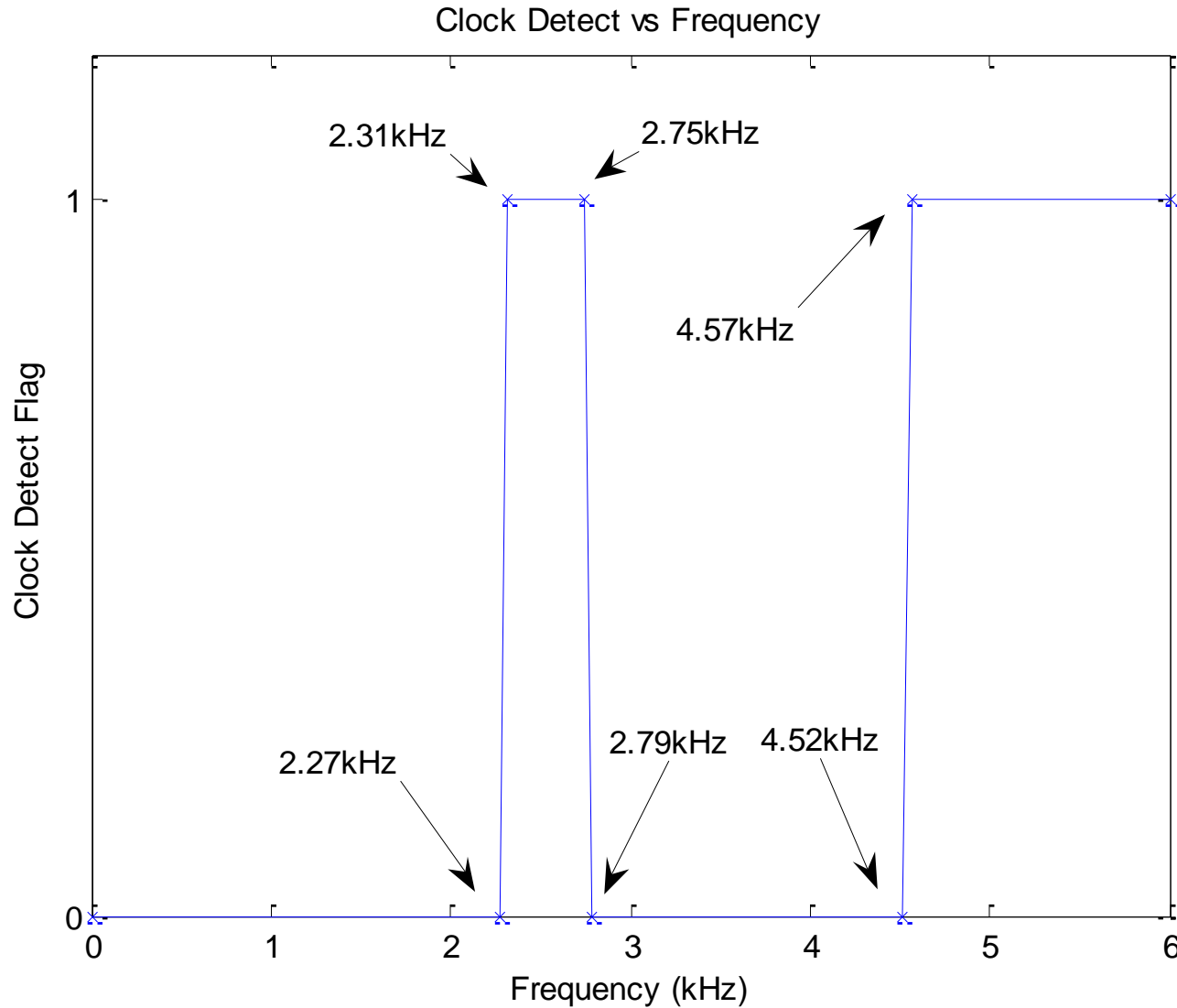
MPLAB ICE 2000 PIC18C452 pc:0x51e W:0x13 n ov z dc c Brk On 4 MHz bank 0 Ln 1221, Col 31 INS WR

Clock Detection



- Detect if a valid signal is present
- Check that edges occur regularly
- Timer overflows -> clock error

Clock Detection



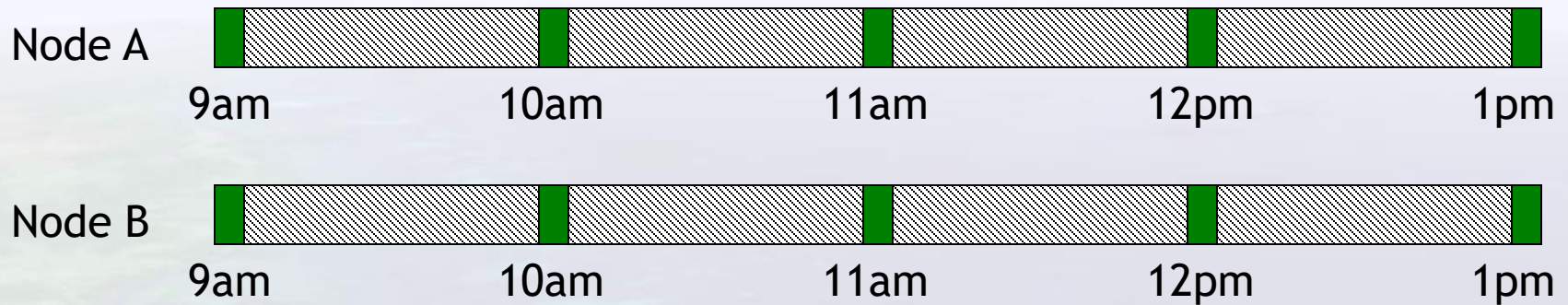
- Manchester Encoding/Decoding
- **Power Saving Techniques**
- Dynamic Address Allocation

Scheduled Rendezvous



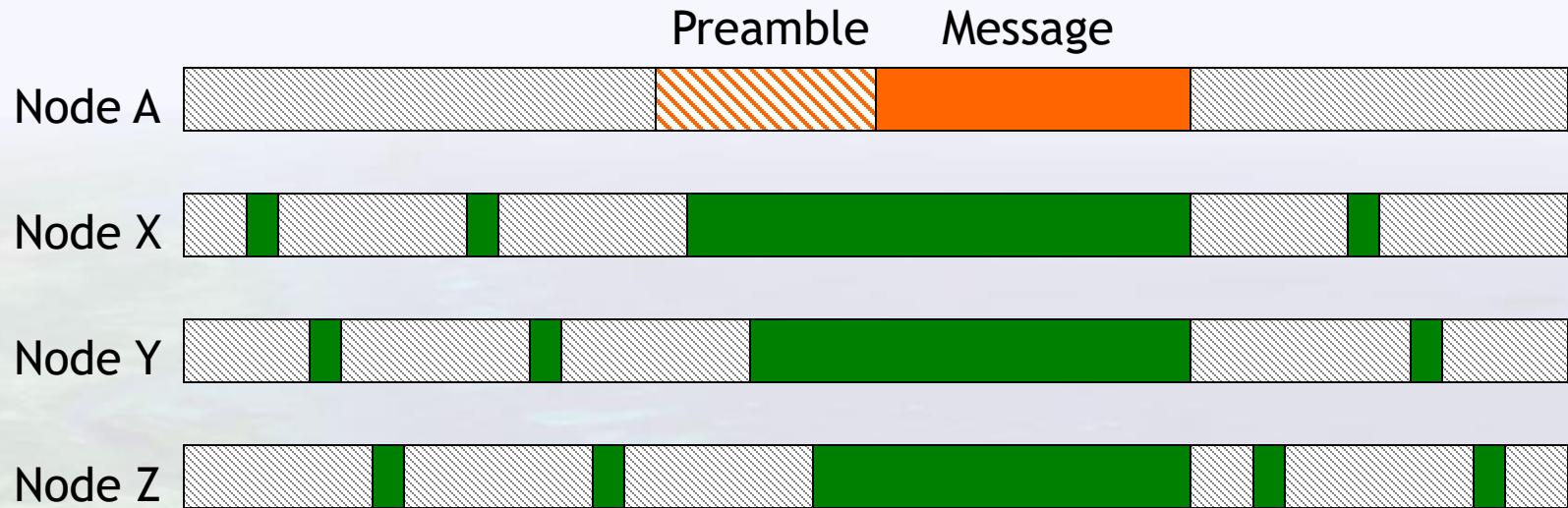
- Nodes are awake for 5 minutes every hour
- Suits periodic bulk data transfer
- Common in data collection applications

Synchronisation



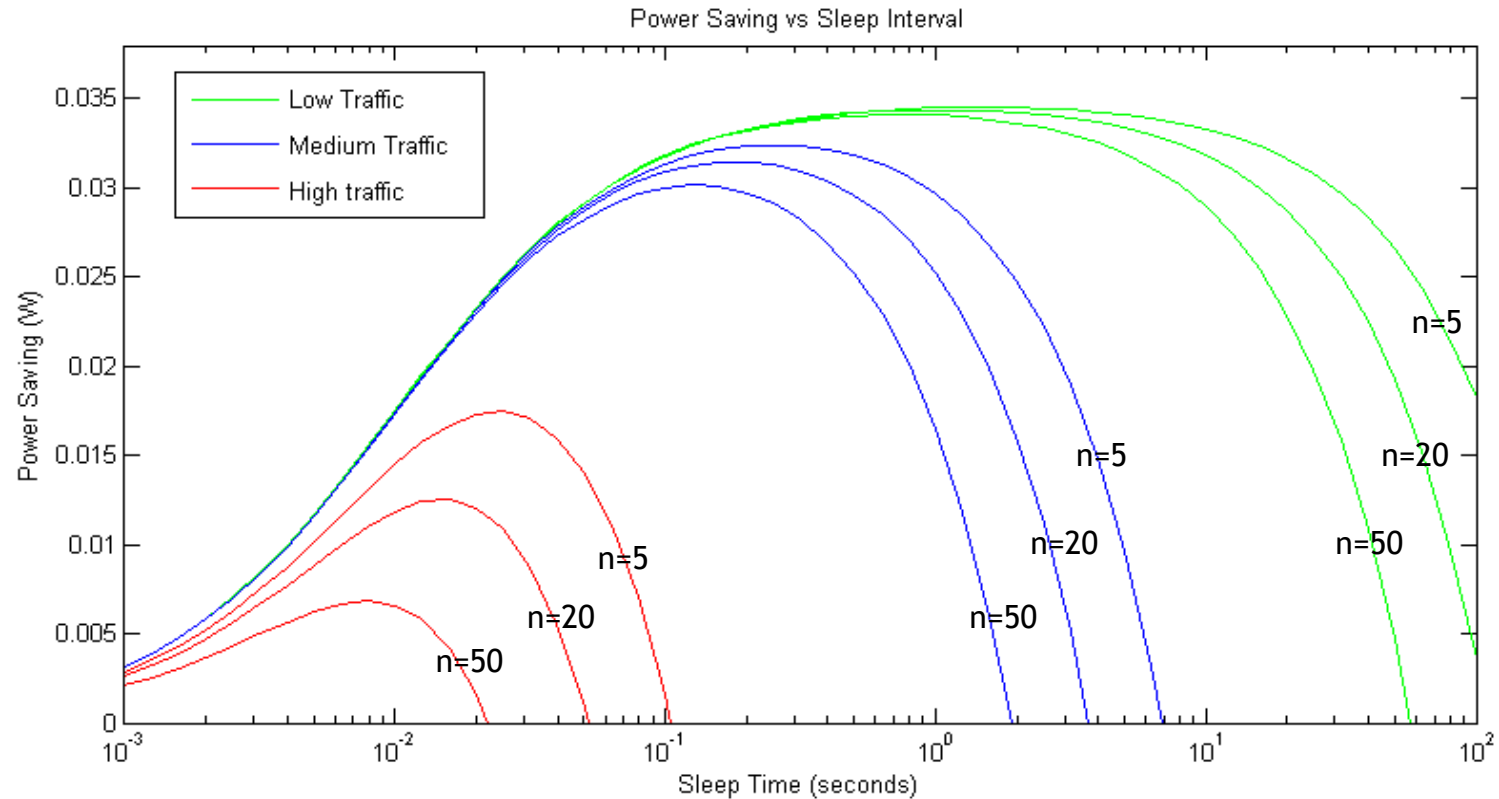
- Distributed synchronisation
 - Large error tolerance
- Clock Drift
 - Phase Detection
- No connectivity between rendezvous periods
 - Preamble sampling

Preamble Sampling

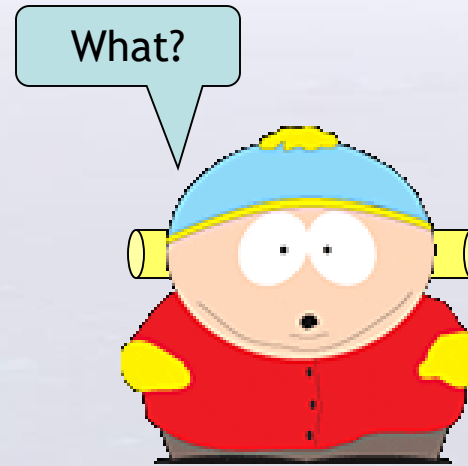


- Use preamble to wake up other nodes
- Frequent brief samples
- Keep listening if there is any activity
- Preamble length = sampling delay
 - Save power while listening
 - Use more power when transmitting

Optimal Sampling Frequency



- Optimum sampling frequency depends on node density and traffic level
- Very effective for low density and low traffic
- Power savings up to 99%



- Periods of localised increased traffic levels
- Dynamically adjust the sampling frequency when increased traffic is anticipated
- Reduced chance of collisions

- Scheduled Rendezvous:
 - Scheduling implemented with RTC chip
 - Synchronise two nodes with 1 sec error
- Preamble Sampling:
 - Implemented and verified
 - Maintains connectivity
 - Power consumption proportional to duty cycle
- Dynamic Optimization
 - Basic operation verified
 - Future work - use network and application layer information for more intelligence

- Manchester Encoding/Decoding
- Power Saving Techniques
- **Dynamic Address Allocation**

- Traditional methods are centralised
 - e.g. DHCP Server
- Goals:
 - Dynamically allocate unique addresses
 - Cope with network dynamics
- Three ad hoc addressing techniques:
 - Centralised (elected leader)
 - Decentralised (distributed agreement)
 - Mutually exclusive address spaces

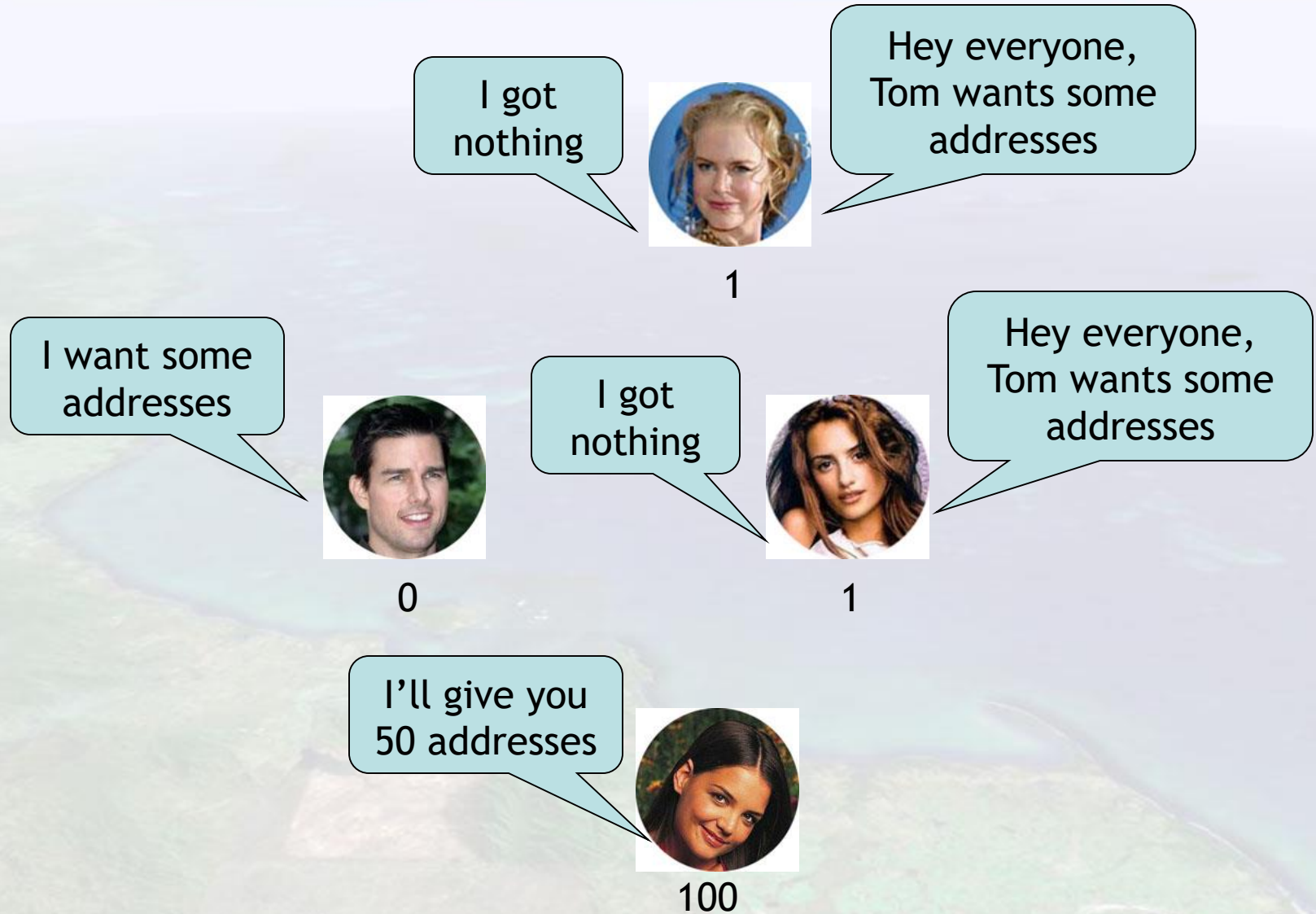
Mutually Exclusive Address Spaces

- Every node has a *set* of mutually exclusive addresses
- Every node can give some of its addresses away
- Very low overhead
- New protocol based upon two existing protocols

Here's how it works:



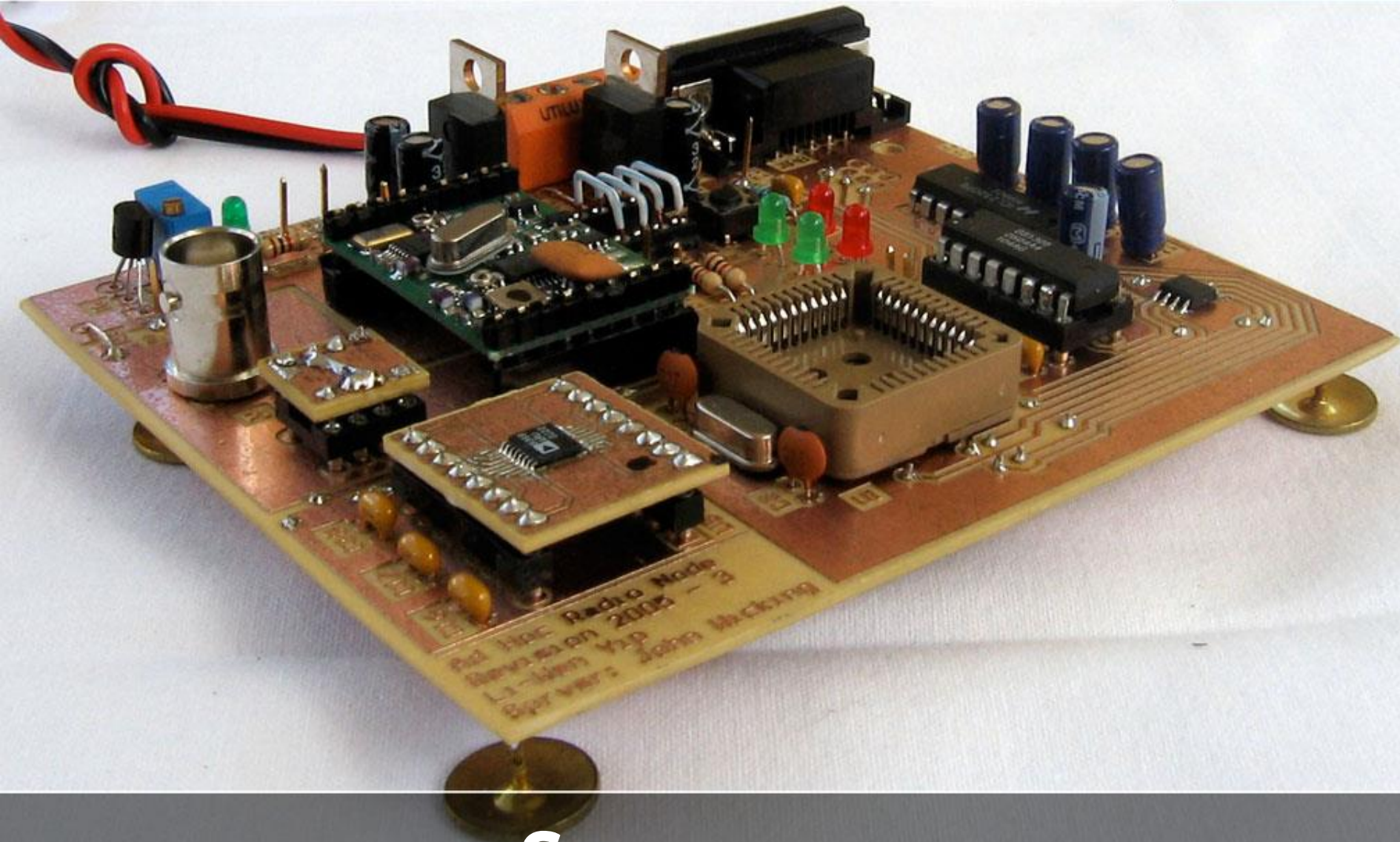
Address Depletion



- Partially implemented for ns-2 network simulator
- Didn't work
- Poor documentation and support
- Simulation required to quantify results

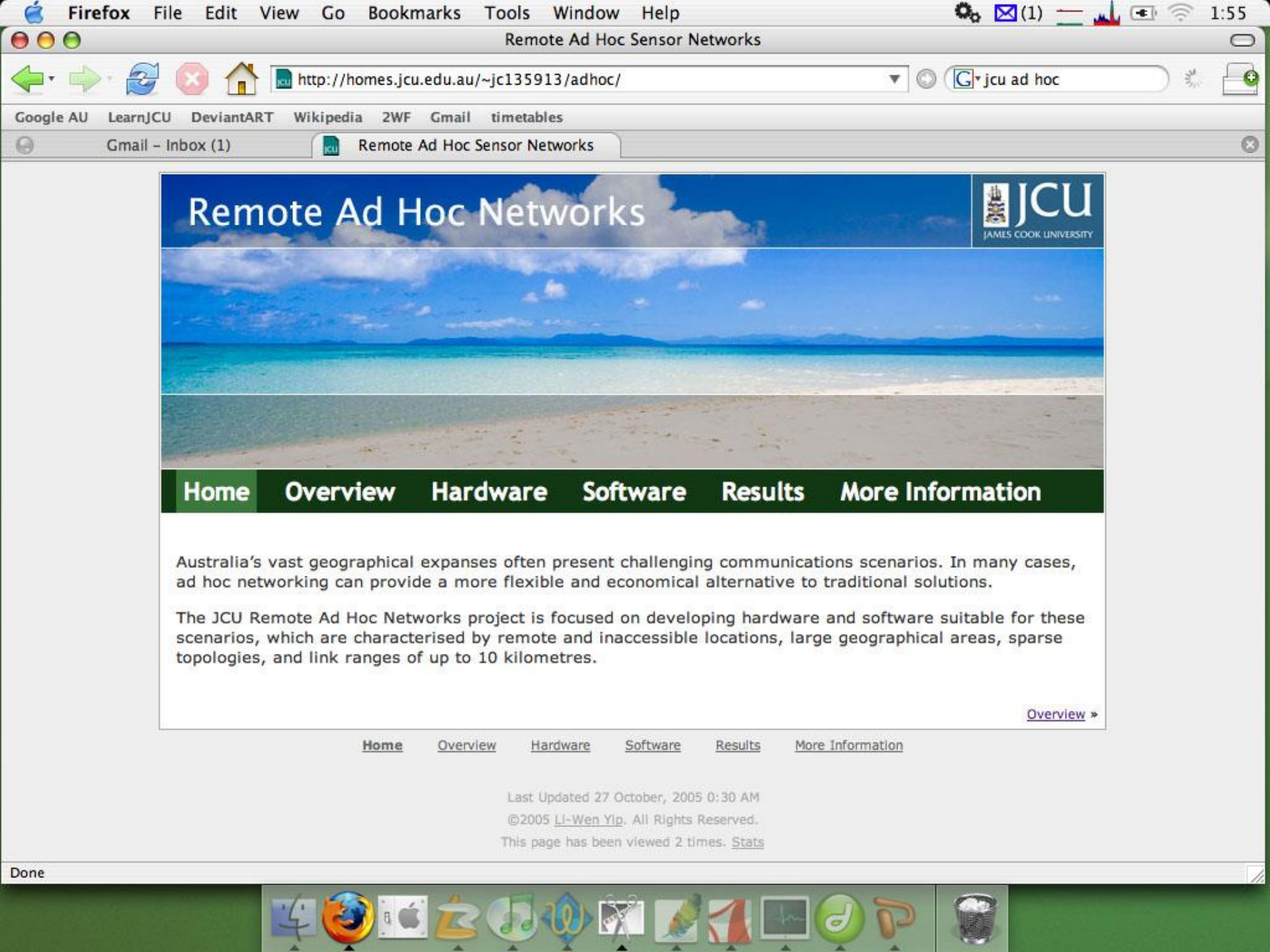


Remote Ad Hoc Sensor Networks



Summary

- Hardware improved
 - More compact PCB design
 - Bypass switch - 500mA reduced to 5uA
- Fast, efficient, reliable Manchester codec
 - Fully working mate!
- Power save protocol working
 - Theoretical 99% power saving
 - Requires simulation to quantify results
 - Identified areas for future improvement
- Dynamic address allocation developed
 - Implementation needs to be completed
 - Requires simulation to verify operation



Remote Ad Hoc Networks



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Australia's vast geographical expanses often present challenging communications scenarios. In many cases, ad hoc networking can provide a more flexible and economical alternative to traditional solutions.

The JCU Remote Ad Hoc Networks project is focused on developing hardware and software suitable for these scenarios, which are characterised by remote and inaccessible locations, large geographical areas, sparse topologies, and link ranges of up to 10 kilometres.

[Overview »](#)

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