

Computer Architecture and Design Practical 2 Lab Sheet
October 2019

Name	Matric No.
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Part (a) – Dependency Checking and Stalling

<input type="checkbox"/> Baseline CPI measurement = <input type="checkbox"/> Mandlebrot demo working, execution time =
Demonstrator Signature
Demonstrator Comments

Part (b) – Forwarding Speculative Results

<input type="checkbox"/> Optimized CPI measurement = <input type="checkbox"/> Mandlebrot demo working, execution time =
Demonstrator Signature
Demonstrator Comments

Part (c) – Optimizing the Critical Path

<input type="checkbox"/> Optimized CPI measurement = <input type="checkbox"/> Optimized F_{max} setting = <input type="checkbox"/> Mandlebrot demo working, execution time =
Demonstrator Signature
Demonstrator Comments

**Attach this lab sheet as the cover page to your report before handing to ITO before the deadline.
Please ensure you include listings of your Verilog code for each part.**