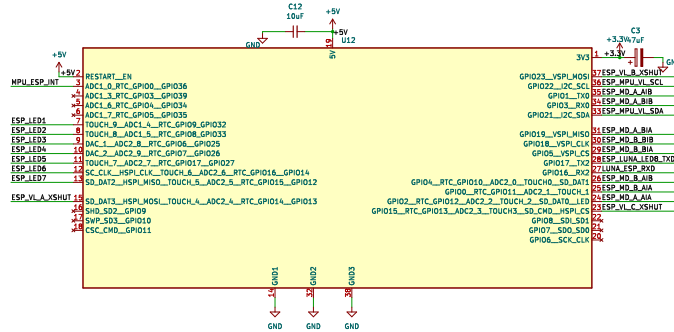
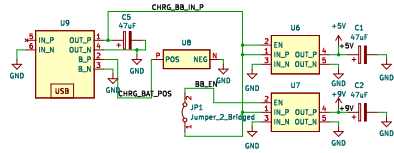


Diagram illustrating the test point connections for the CHRG_BAT_POS pin:

- TP1: TestPoint_3.3V
- TP2: TestPoint_5V
- TP3: TestPoint_VCC1
- TP4: TestPoint_GND
- TP5: TestPoint_Battery_POS
- TP6: TestPoint_Battery_NEG



The diagram illustrates the ESP system architecture, showing a vertical stack of eight LED modules (ESP_LED1 to ESP_LED8) and a final module (ESP_LED8_TXD). Each module consists of an LED (D1-D8), a CD1414W diode, and a resistor (R1-R8). The modules are connected in a series-like fashion, with the output of one module feeding into the next. The power supply for each module is +3.3V.