

MJPEG Encoder

Version 01.00.16

Release Notes

July 2014

Build ID: 01.00.16.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Please refer Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

- This release is dependent on the following components.
 - Code Composer Studio (CCSv4) version 4.2.0.09000
 - Code Generation Tool version 4.5.1 for HDVICP2 processor
 - Code Generation Tool version 5.0.3 for Media Controller processor
 - Framework Component version 3.20.00.22
 - HDVICP2 API (01.00.00.23)
 - HDVICP2 CSP (Containing CSL) version 00.05.02.00
- This release is validated on DM816x REV-A2 DDR2 EVM using following components.
 - BIOS version 6.30.02.42
 - Codec Engine (CE) version 3.20.00.16
 - XDC tools version 3.20.04.68



Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00111677	MJPEG Encoder cropping issue with luma as 8-bit container and chroma as 16- bit container when input buffers are TILER mode	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.15

Release Notes

October 2013

Build ID: 01.00.15.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Please refer Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

- This release is dependent on the following components.
 - Code Composer Studio (CCSv4) version 4.2.0.09000
 - Code Generation Tool version 4.5.1 for HDVICP2 processor
 - Code Generation Tool version 5.0.3 for Media Controller processor
 - Framework Component version 3.20.00.22
 - HDVICP2 API (01.00.00.23)
 - HDVICP2 CSP (Containing CSL) version 00.05.02.00
- This release is validated on DM816x REV-A2 DDR2 EVM using following components.
 - BIOS version 6.30.02.42
 - Codec Engine (CE) version 3.20.00.16
 - XDC tools version 3.20.04.68



Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00104740	MJPEG Encoder output color was wrong in privacy masking	IVAHD_001
SDOCM00104741	MJPEG Encoder reports error when bottomRight.x/y are set to image width and image height	IVAHD_001
SDOCM00104742	MJPEG Encoder has observed offset between defined privacy mask region and output privacy mask region	IVAHD_001
SDOCM00104743	MJPEG Encoder does not honor coordinates of ROI regions of non-multiple of 16 image height when privacy mask is enable	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.14

Release Notes

August 2013

Build ID: 01.00.14.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Added support for encoding images of resolutions up to 16384x8192 for YUV420SP, YUV422ILE and YUV422IBE and resolutions up to 8192x8192 for YUV444 and YUV400

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1 for HDVICP2 processor
- Code Generation Tool version 5.0.3 for Media Controller processor
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.21)



- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release

- None

Known Issues

- None



MJPEG Encoder

Version 01.00.13

Release Notes

May 2013

Build ID: 01.00.13.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Added error check for “non-multiple of 16” inputBufDesc->imagePitch

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1 for HDVICP2 processor
- Code Generation Tool version 5.0.3 for Media Controller processor
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.21)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00



- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00101136	inputBufDesc->imagePitch is not respected by codec	IVAHD_001
SDOCM00101200	MJPEG Encoder returns error when outBufs buffer pointer and size are NULL when output datasync is IVIDEO_FIXEDLENGTH mode	IVAHD_001
SDOCM00101201	dataSyncHandle is not set to correct value by MJPEG encoder for data sync callback APIs	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.12

Release Notes

April 2013

Build ID: 01.00.12.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- MJPEG Encoder supports YUV 422ILE(UYVY) input chroma format.

Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1 for HDVICP2 processor
- Code Generation Tool version 5.0.3 for Media Controller processor
- Framework Component version 3.20.00.22



- HDVICP2 API (01.00.00.21)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release

- None

Known Issues

- None



MJPEG Encoder

Version 01.00.11

Release Notes

February 2013

Build ID: 01.00.11.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- MJPEG Encoder Media Controller library is compiled with Code Generation tools v5.0.3 to produce ELF compliant binaries
- Migration of HDVICP API to 01.00.00.21

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1 for HDVICP2 processor
- Code Generation Tool version 5.0.3 for Media Controller processor
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.21)



- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release

- None

Known Issues

- None



MJPEG Encoder

Version 01.00.10

Release Notes

January 2013

Build ID: 01.00.10.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Please refer Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00



- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00098370	MJPEG Encoder fails to get updated values from codecParams of DDR in case of SAMECODEC. Also, dynamic change in frameRate doesn't reset the Rate Control Module.	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.09

Release Notes

December 2012

Build ID: 01.00.09.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Please refer Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00



- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00098186	MJPEG encoder doesn't honour dynamic change of initQP, qpMax, qpMin and rcAlgo parameters in rate control structure	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.08

Release Notes

November 2012

Build ID: 01.00.08.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42



- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00097302	inputBufDesc->imageRegion. and inputBufDesc->activeFrameRegion are not respected by codec	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.07

Release Notes

November 2012

Build ID: 01.00.07.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Support for Rate Control – CBR and VBR for all input YUV chroma formats

Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16



— XDC tools version 3.20.04.68

Fixed In This Release

- None

Known Issues

- None



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MJPEG Encoder

Version 01.00.06

Release Notes

September 2012

Build ID: 01.00.06.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Support for Privacy Masking
- Support for Rate Control – CBR for input chroma format YUV 420 Semi-Planar only

Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68



Fixed In This Release

- None

Known Issues

- None

MJPEG Encoder

Version 01.00.05

Release Notes

July 2012

Build ID: 01.00.05.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68



Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00093787	Change the order of marker position in JPEG header	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.04

Release Notes

June 2012

Build ID: 01.00.04.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68



Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00092970	MJPEG Encoder misbehaves if SL2 memory is kept in buffered region of media controller	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.03

Release Notes

March 2012

Build ID: 01.00.03.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68



Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00088956	More clarifications in Data sheet	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.02

Release Notes

February 2012

Build ID: 01.00.02.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68



Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00088897	Linker errors observed for default static and dynamic MJPEG encoder instance objects	IVAHD_001
SDOCM00088903	In data sheet the frame rate used is not mentioned for performance table	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.01

Release Notes

December 2011

Build ID: 01.00.01.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Added support for encoding images of resolutions up to 4320x4096

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68



Fixed In This Release

SDOCM ID	Description	Configuration
SDOCM00084257	JPEG instance objects (JPEGVENC_TI_PARAM) that defines default parameter values of create time params, has only base params	IVAHD_001
SDOCM00084256	Default parameter values for JPEG instance objects are not exported in the interface file.	IVAHD_001

Known Issues

- None

MJPEG Encoder

Version 01.00.00

Release Notes

December 2010

Build ID: 01.00.00.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Added support for error recovery and error robustness in case of data sync errors
- Added support for data sync when markers are enabled
- Migration to tools mentioned under "Validation Information" below

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MJPEG Encoder which runs on M3 and IVAHD based SoC	Code Composer Studio [User Guide] [Data Sheet]

This release is validated on the following:

- Code Composer Studio Version 4.2.0.09000
- IVAHD Simulation CSP version 1.1.5
- Netra CSP (Simulation) version 0.7.1
- DM816x DDR2 EVM REV-B
- OMAP4 ES1.0 Hardware Platform
- Code Generation Tool version 4.5.1
- Framework Components version 3.20.00.22 GA
- XDAIS version 7.20.00.07 GA
- XDC tools version 3.20.04.68 GA
- HDVICP2.0 CSL Version 00.05.02
- HDVICP2.0 API library version 01.00.00.19



- BIOS version 6.30.02.42 GA
- CE version 3.20.00.16 GA

Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00075245	Visual artifacts observed in output when encoding a 444 planar 4Kx4K image with TILER 8 memory for input chroma buffer	IVAHD_001
SDOCM00075246	Stability testing has been done for limited test cases	IVAHD_001
SDOCM00075247	Sub frame level data exchange with TILER buffers has been tested for limited test cases	IVAHD_001

Known Issues

Defect ID	Description	Applicable Release Configuration
SDOCM00075244	Restart Marker insertion fails for high RST intervals on simulator but works fine on DM816x EVM REV-B	IVAHD_001