

# MPEG4/H.263 Decoder

## Version 01.00.16

### Release Notes

### December 2017

#### Build ID: 01.00.16.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer Fixed In This Release section

#### Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOC's	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is dependent on the following components –
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Tool version 5.0.3 for Media Controller Processor and 4.5.1 for HDVICP2 processor
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.23)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



#### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00343913	Issue with FLUSH during Dynamic Resolution change.	IVAHD_001

#### Known Issues

- None

# MPEG4/H.263 Decoder

## Version 01.00.15

### Release Notes

### April 2015

#### Build ID: 01.00.15.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer Fixed In This Release section

#### Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is dependent on the following components –
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Tool version 5.0.3 for Media Controller Processor and 4.5.1 for HDVICP2 processor
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.23)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00115792	Video Blockiness observed in dual-instance decoding case	IVAHD_001

### Known Issues

- None

# **MPEG4/H.263 Decoder**

## **Version 01.00.14**

### **Release Notes**

### **November 2013**

#### **Build ID: 01.00.14.00**

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### **New In This Release**

- Please refer Fixed In This Release section

#### **Validation Information**

Release Configuration	Description	Validation Platform
IVAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is dependent on the following components –
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Tool version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.22)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00300818	Video Decode: Fix Video decoder testing bugs	IVAHD_001

### Known Issues

- None

# MPEG4/H.263 Decoder

## Version 01.00.13

### Release Notes

### August 2013

#### Build ID: 01.00.13.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer Fixed In This Release section

#### Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is dependent on the following components –
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Tool version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.22)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00103095	Hang is seen at OMX/system when MPEG4 decoder is fed with stream with multiple skip frames	IVAHD_001

### Known Issues

- None



# MPEG4/H.263 Decoder

## Version 01.00.12

### Release Notes

### June 2013

#### Build ID: 01.00.12.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer to the fixed in this release section for details.

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSV4) version 4.2.0.09000
  - Code Generation Tool version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00294564	Divide by zero exception in MPEG4 codec control call for get status	IVAHD_001
OMAPS00294164	H.263 hardware codec returns wrong error code for ANNEX D streams	IVAHD_001

### Known Issues

- None

# MPEG4/H.263 Decoder

## Version 01.00.11

### Release Notes

### October 2012

#### Build ID: 01.00.11.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer to the fixed in this release section for details.

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Tool version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



## Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00281195	Codec returns error if output buffers provided are mix of RAW and TILEDPAGE	IVAHD_001

## Known Issues

- None

# MPEG4/H.263 Decoder

## Version 01.00.10

### Release Notes

### July 2012

#### Build ID: 01.00.10.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Added feature to support decoding of only Intra frames with less create time memory. Please refer Datasheet for gain in memory in this feature.

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Tool version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### **Fixed In This Release**

- None

### **Known Issues**

- None



# MPEG4/H.263 Decoder

## Version 01.00.09

### Release Notes

### June 2012

#### Build ID: 01.00.09.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer to the fixed in this release section for details.

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Tool version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00093580	In case of H263++ streams, frames without UFEP are not decoded properly. ECD3 throws out error.	IVAHD_001
OMAPS00273469	MPEG4 decoder is not populating frame rate of status params correctly	IVAHD_001

### Known Issues

- None



# MPEG4/H.263 Decoder

## Version 01.00.08

### Release Notes

### June 2012

#### Build ID: 01.00.08.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer to the fixed in this release section for details.

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Toll version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00093154	Posted write problem from M3. Hang on Media Controller can be observed, if SL2 is accessed through buffered region from Media Controller.	IVAHD_001
SDOCM00093428	Uninitialized parameter - "interlaced" in header structure.	IVAHD_001

### Known Issues

None

# MPEG4/H.263 Decoder

## Version 01.00.07

### Release Notes

### May 2012

#### Build ID: 01.00.07.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Added support for enhanced out loop Deblocking Feature

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Toll version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00089567	More clarifications in data sheet	IWAHD_001

### Known Issues

None

# MPEG4/H.263 Decoder

## Version 01.00.06

### Release Notes

### March 2012

#### Build ID: 01.00.06.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer the fixed in this release section for details. Please refer the data sheet for detail feature list.

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Toll version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00262600	MPEG4 Corrupted clip - Occurs sorry cannot player pop-up	IVAHD_001
SDOCM00090501	Codec freeing single buffer instead of two buffers when display delay set to decode order	

### Known Issues

Defect ID	Description	Applicable Release Configuration
SDOCM00089567	More clarifications in data sheet.	IVAHD_001

# MPEG4/H.263 Decoder

## Version 01.00.05

### Release Notes

### February 2012

#### Build ID: 01.00.05.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer the fixed in this release section for details. Please refer the data sheet for detail feature list.

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Toll version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00262190	VOL header corrupted clip stops playing and showing pop up message video cant played.	IVAHD_001

### Known Issues

Defect ID	Description	Applicable Release Configuration
SDOCM00089567	More clarifications in data sheet.	IVAHD_001





# MPEG4/H.263 Decoder

## Version 01.00.04

### Release Notes

### January 2012

#### Build ID: 01.00.04.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer the fixed in this release section for details. Please refer the data sheet for detail feature list.

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Toll version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00259817	A block noise is displayed on the lower right of the picture.	IVAHD_001

### Known Issues

None



# MPEG4/H.263 Decoder

## Version 01.00.03

### Release Notes

### November 2011

#### Build ID: 01.00.03.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### New In This Release

- Please refer the fixed in this release section for details. Please refer the data sheet for detail feature list.

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Toll version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



### Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00252022	MPEG-4 outArg's display buffer descriptors don't match the display buffer.	IVAHD_001
OMAPS00255460	MPEG-4 Dec Codec Lib causing corruption after seek	IVAHD_001

### Known Issues

None



# **MPEG4/H.263 Decoder**

## **Version 01.00.03**

### **Release Notes**

### **November 2011**

#### **Build ID: 01.00.03.00**

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issue](#)

#### **New In This Release**

- Please refer the fixed in this release section for details. Please refer the data sheet for detail feature list.

#### **Validation Information**

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Decoder, which runs on HDVICP2 and Media Controller Based SOCs	DM81x REV-A2 DDR2 EVM <a href="#">[Datasheet]</a> <a href="#">[User Guide]</a>

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
  - Code Composer Studio (CCSv4) version 4.2.0.09000
  - Code Generation Toll version 4.5.1
  - Framework Component version 3.20.00.22
  - HDVICP2 API (01.00.00.19)
  - HDVICP2 CSP (Containing CSL) version 00.05.00.20
  - BIOS version 6.30.02.42
  - Codec Engine (CE) version 3.20.00.16
  - XDC tool version 3.20.04.68



## Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00250564	Mpeg4 de-blocking feature does not work: green blocks are shown all along the video clip incase codec gets internal buffers allocated in tiler region.	IVAHD_001
OMAPS00252437	Codec report fatal error while playing the H.263 movie clip	IVAHD_001
SDOCM00083334	MPEG4 Decoder is having issue while transferring the static table to SI2	IVAHD_001
SDOCM00083458	In MPEG4 Decoder some of erroneous stream decoded output is having block ness.	IVAHD_001
SDOCM00084258	MPEG4 Decoder on HDVCIP2 is not having support for 2K-2K error concealment even though having support for decoding of 2Kx2K.	IVAHD_001
SDOCM00084955	MPEG4/H263 decoder returning error as unsupported Parameters while changing the display width through control call.	IVAHD_001
SDOCM00085157	Concealment of some of erroneous streams are not happening correctly in case of MPEG4/H.263 decoder	IVAHD_001
SDOCM00085158	Dynamic resolution change is not supported in MPEG4/H.263 decoder	IVAHD_001
SDOCM00085159	Drop of first Frame (I Frame) in sequence is not getting reported by H.263 codec	IVAHD_001
SDOCM00085160	Copying of MB-status Info from SL2 Memory to DDR may create issue	IVAHD_001
SDOCM00085269	MPEG4/H.263 decoder is wrongly decoding the erroneous spark stream	IVAHD_001
SDOCM00085268	MPEG4/H263 decoder should have common test app as other codec's	IVAHD_001
SDOCM00083990	make batch file may not work on windows 7	IVAHD_001
SDOCM00082283	H.263 stream having height more then 800 pixels are not Getting decoded correctly by MPEG4 Decoder, codec reporting error for non-erroneous streams.	IVAHD_001
SDOCM00082097	Hard fault observed with the ice crusher register in HDVCIP_Acquire function, while running H263 erroneous Stream.	IVAHD_001
SDOCM00082275	An erroneous H263 stream throws a FATAL error when a valid Start H263 code not found in stream.	IVAHD_001
SDOCM00082335	MPEG4 Decoder returns error for non-corrupted Gob's for Erroneous H.263 streams.	IVAHD_001
SDOCM00082539	MPEG4 decoder is tested with only limited number of positive And negative test cases.	IVAHD_001

Defect ID	Description	Applicable Release Configuration
SDOCM00082712	MPEG4 Decoder is corrupting output buffer for erroneous Streams.	IVAHD_001
SDOCM00081514	With outloopDeBlocking = DEBLOCK_ENABLE, for a error stream the display buffer will have filtered output but not concealed for the erroneous frame, but for subsequent frame(s) The concealed output will be used for temporal prediction.	IVAHD_001
OMAPS00242800	H263 video clip does not play after few seconds	IVAHD_001
OMAPS00242676	Video playback fails, show "video format is not supported", 1080p and 720p mp4 clip.	IVAHD_001

#### Known Issues

- None