


1			2			3			4			
A	版本号	修订日期	修订记录									
	V1.0.0.0	2024-03-04	初始版本									
	V1.0.1.0	2024-03-15	下载口修正									
	V1.0.2.0	2024-03-20	更改引脚									
	V1.0.3.0	2024-04-10	TX加上拉,更改LED灯									
B												
C												
D												
1			2			3			4			
<div><div><div>版权所有，保留一切权利。 本公司致力于产品的不断改善和功能升级，内容如有改动，恕不另行通知。</div><div>在此文件中，仅以版本号前两位(发行版)，划分版本之间的区别。</div></div><div><div><div>Title: MCXVision</div><div>Subtitle: Revision</div><div>Size: A4</div></div><div><div>Author: 猫肆管</div><div>Revision: V1.0.3.0</div><div>Date: 2024/4/12</div></div><div><div>Time: 10:25:44</div><div>Sheet: 1</div><div>of: 8</div></div></div><div></div></div>												

1

2

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A

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B

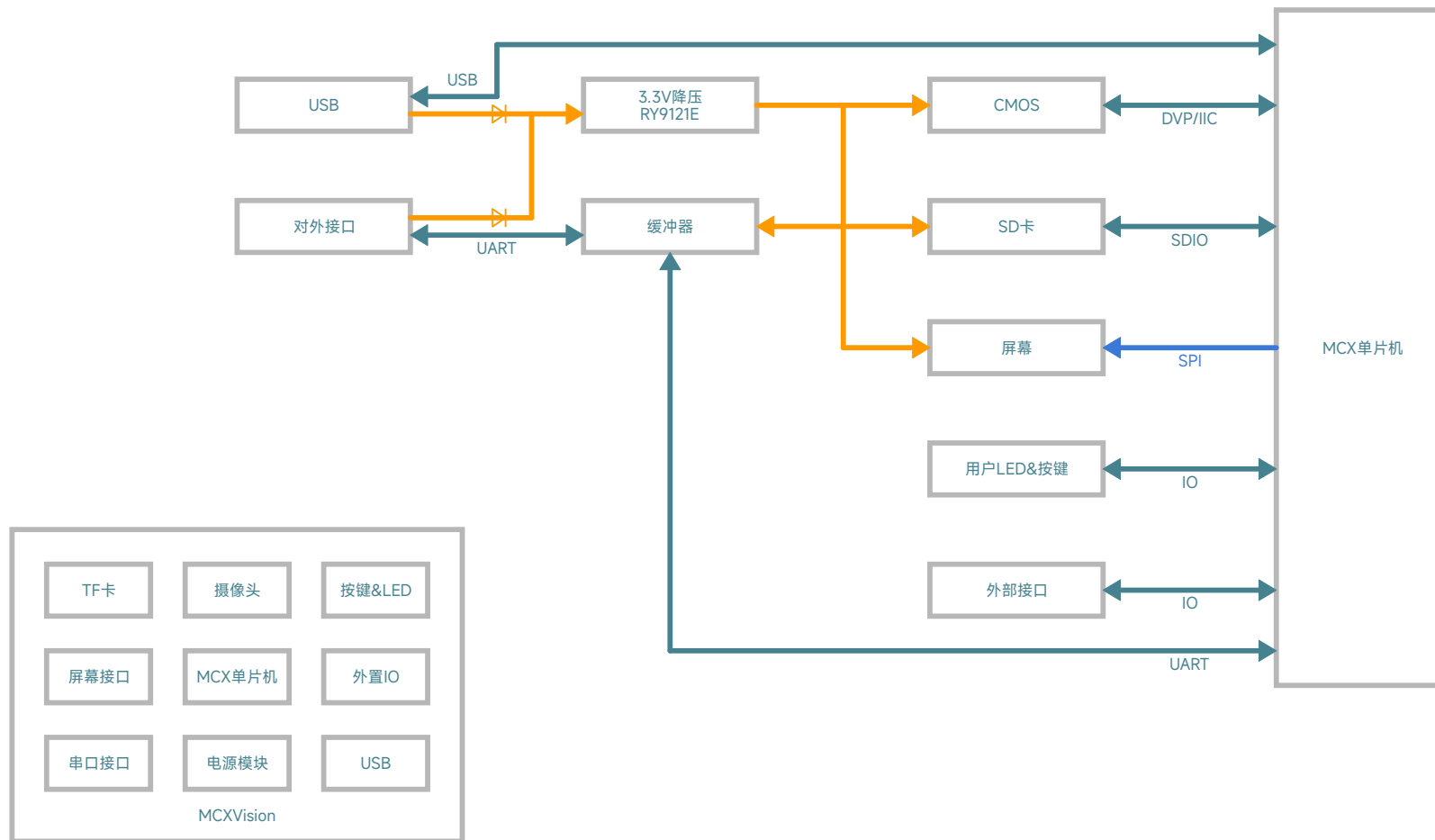
B

C

C

D

D



图例:



Title: MCXVision

Author: 猫肆管

Subtitle: BlockDiagram

Revision: V1.0.3.0

Size: A4

Date: 2024/4/12

Time: 10:25:44

Sheet: 2

of: 8

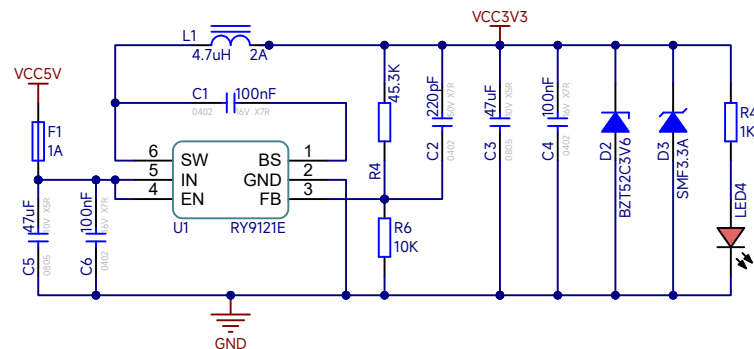
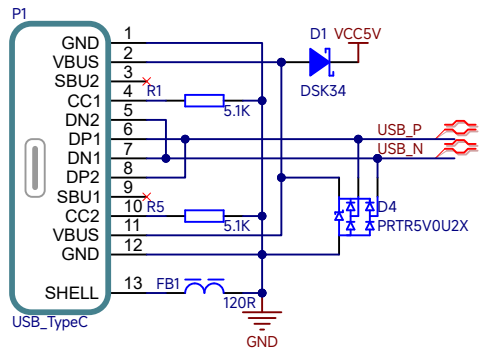


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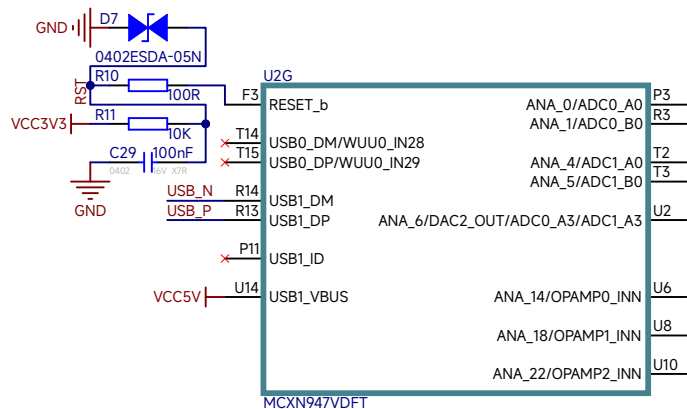
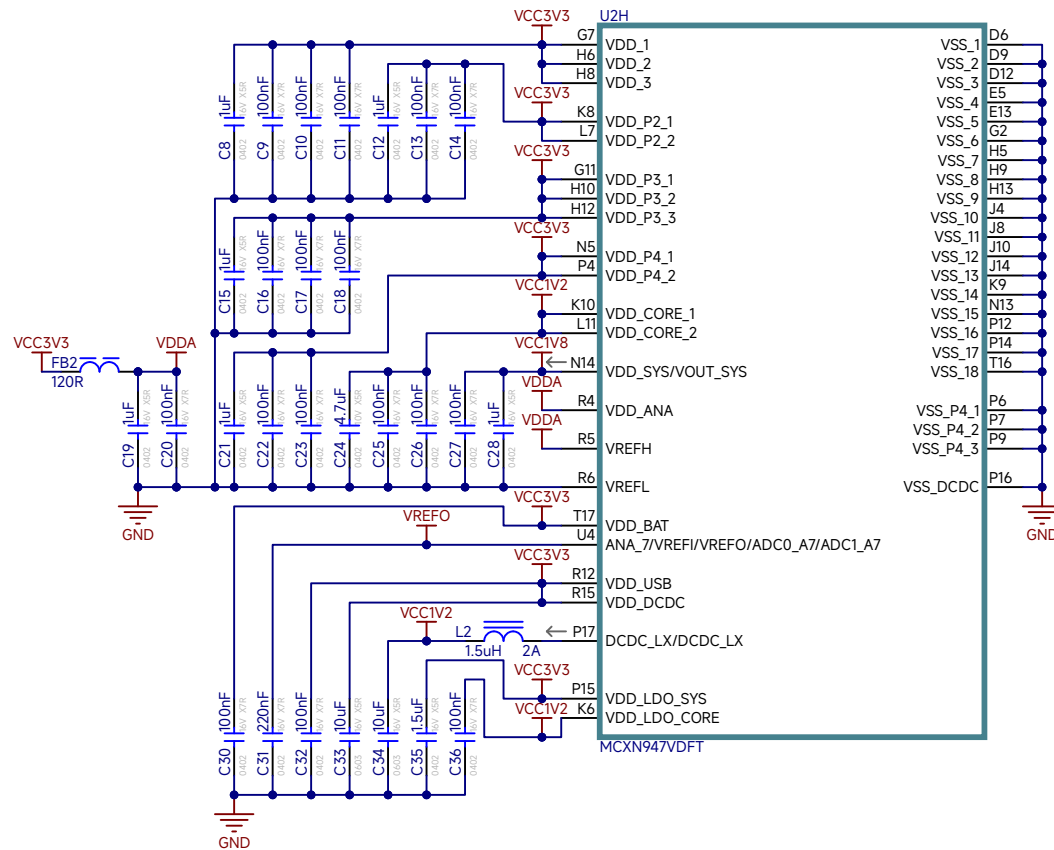
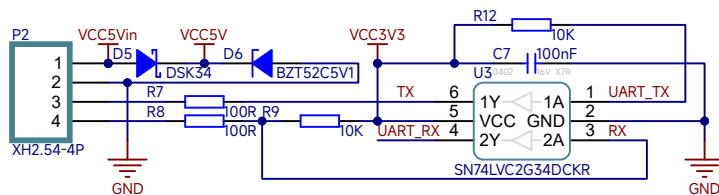
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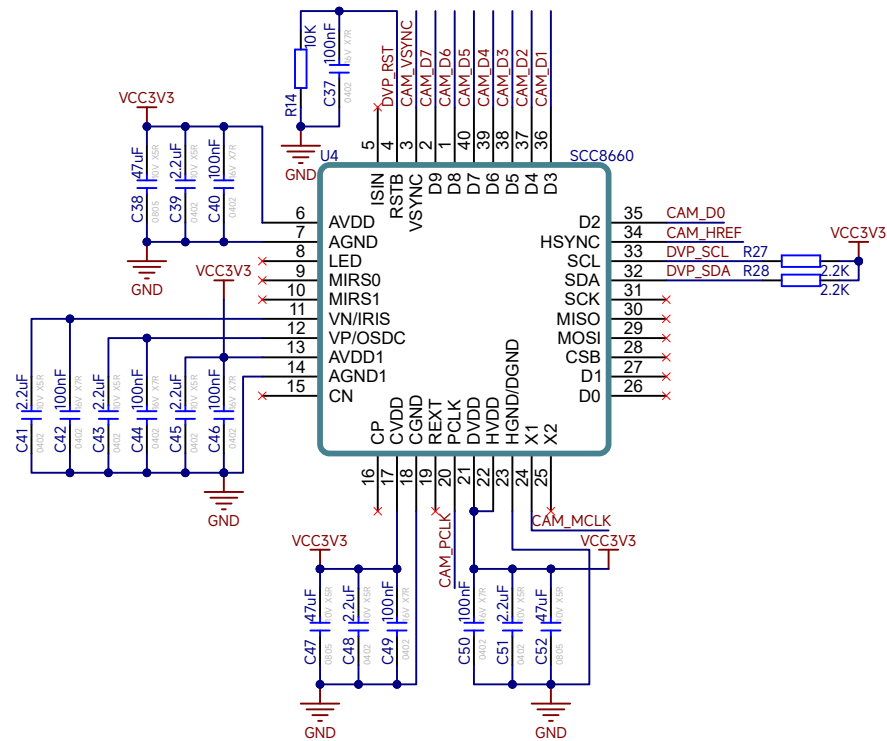
4



VDD:3.3V
VDD_P2:3.3V
VDD_P3:3.3V
VDD_P4:3.3V
VDD_CORE:1.0-1.2V(1.2V)
VDD_SYS/VOUT_SYS:1.8/2.5V(Out)
VDD_ANA:3.3V
VREFH:3.3V
VREFL:0V
VDD_BAT:3.3V
VREF0:220nF tie to GND
VDD_USB:3.3V
VDD_DCDC:3.3V
DCDC_LX:SW
VDD_LDO_SYS:3.3V
VDD_LDO_CORE:VDD_CORE



Title: MCXVision				Author: 猫肆管		
Subtitle: Power				Revision: V1.0.3.0		
Size: A4	Date: 2024/4/12	Time: 10:25:44	Sheet: 3	of: 8		



CAM_D0	R15		DVP_D0
CAM_D1	R16		100R DVP_D1
CAM_D2	R17		100R DVP_D2
CAM_D3	R18		100R DVP_D3
CAM_D4	R19		100R DVP_D4
CAM_D5	R20		100R DVP_D5
CAM_D6	R21		100R DVP_D6
CAM_D7	R22		100R DVP_D7
CAM_PCLK	R23		100R DVP_PCLK
CAM_HREF	R24		100R DVP_HREF
CAM_VSYNC	R25		100R DVP_VSYNC
CAM_MCLK	R26		100R DVP_MCLK
			100R

U2A

VCC3V3  
10K  
R2  
100nF  
C58  
GND

JTAG\_TMS A17  
JTAG\_TCLK A16  
JTAG\_TDO B16  
JTAG\_TDI B15  
DVP\_VSYNC B14  
DVP\_PCLK A14

ISP  
R3  
100R  
C14  
C13  
C12  
A12  
B12  
D11  
F12  
E11  
G13  
B10  
A10  
C10

DVP\_HREF  
P0.16  
P0.17  
P0.18

LCD\_RES  
LCD\_DC  
LCD\_MOSI  
LCD\_SCLK  
LCD\_CS  
LCD\_BL

P0\_0/TMS/SWDIO/FC1\_P0/CT\_INP0  
P0\_1/TCLK/SWCLK/FC1\_P1/CT\_INP1  
P0\_2/TDO/SWO/FC1\_P2/CT0\_MAT0/UTICK\_CAP0/I3C0\_PUR  
P0\_3/CMP1\_IN1/TDI/FC1\_P3/CT0\_MAT1/UTICK\_CAP1/HSCMP0\_OUT  
P0\_4/TSIO\_CH8/EWM0\_IN/FC0\_P0/FC1\_P4/CT0\_MAT2/UTICK\_CAP2/HSCMP1\_OUT/PDM0\_CLK/WUU0\_IN0  
P0\_5/TSIO\_CH9/EWM0\_OUT\_b/FC0\_P1/FC1\_P5/CT0\_MAT3/UTICK\_CAP3/PDM0\_DATA0  
P0\_6/TSIO\_CH10/ISPMODE\_N/FC0\_P2/FC1\_P6/CT\_INP2/HSCMP2\_OUT/PDM0\_DATA1/CLKOUT  
P0\_7/CMP2\_IN1/FC0\_P3/CT\_INP3/WUU0\_IN1  
P0\_8/ADC0\_B8/FC0\_P4/CT\_INP0/FLEXIO0\_D0  
P0\_9/ADC0\_B9/FC0\_P5/CT\_INP1/FLEXIO0\_D1  
P0\_10/ADC0\_B10/FC0\_P6/CT0\_MAT0/FLEXIO0\_D2  
P0\_11/ADC0\_B11/CT0\_MAT1/FLEXIO0\_D3/HSCMP2\_OUT  
P0\_12/ADC0\_B12/NVM\_TM0/FC1\_P4/FC0\_P0/CT0\_MAT2/FLEXIO0\_D4  
P0\_13/ADC0\_B13/NVM\_TM1/FC1\_P5/FC0\_P1/CT0\_MAT3/FLEXIO0\_D5  
P0\_14/ADC0\_B14/NVM\_TM2/FC1\_P6/FC0\_P2/CT\_INP2/UTICK\_CAP0/FLEXIO0\_D6  
P0\_15/ADC0\_B15/NVM\_TM3/FC0\_P3/CT\_INP3/UTICK\_CAP1/FLEXIO0\_D7  
P0\_16/TSIO\_CH11/ADC0\_A8/FC0\_P0/CT0\_MAT0/UTICK\_CAP2/FLEXIO0\_D0/PDM0\_CLK/I3C0\_SDA/TP\_HRESP/WUU0\_IN2  
P0\_17/TSIO\_CH12/ADC0\_A9/FC0\_P1/CT0\_MAT1/UTICK\_CAP3/FLEXIO0\_D1/PDM0\_DATA0/I3C0\_SCL/TP\_HREADY  
P0\_18/TSIO\_CH13/ADC0\_A10/EWM0\_IN/FC0\_P2/CT0\_MAT2/FLEXIO0\_D2/HSCMP0\_OUT/PDM0\_DATA1  
P0\_19/TSIO\_CH14/ADC0\_A11/EWM0\_OUT\_b/FC0\_P3/CT0\_MAT3/FLEXIO0\_D3/HSCMP1\_OUT/WUU0\_IN3  
P0\_20/TSIO\_CH15/ADC0\_A12/FC0\_P4/FC1\_P0/CT\_INP0/FLEXIO0\_D4/I3C0\_SDA/WUU0\_IN4  
P0\_21/TSIO\_CH16/ADC0\_A13/FC0\_P5/FC1\_P1/CT\_INP1/FLEXIO0\_D5/I3C0\_SCL  
P0\_22/ADC0\_A14/CMP1\_IN2/EWM0\_IN/FC0\_P6/FC1\_P2/CT\_INP2/FLEXIO0\_D6/I3C0\_PUR  
P0\_23/ADC0\_A15/CMP2\_IN2/EWM0\_OUT\_b/FC1\_P3/CT\_INP3/FLEXIO0\_D7/WUU0\_IN5  
P0\_24/ADC0\_B16/FC1\_P0/CT0\_MAT0  
P0\_25/ADC0\_B17/FC1\_P1/CT0\_MAT1  
P0\_26/ADC0\_B18/FC1\_P2/CT0\_MAT2  
P0\_27/ADC0\_B19/FC1\_P3/CT0\_MAT3  
P0\_28/ADC0\_B20/FC1\_P4/FC0\_P4/CT\_INP0  
P0\_29/ADC0\_B21/FC1\_P5/FC0\_P5/CT\_INP1  
P0\_30/ADC0\_B22/FC1\_P6/FC0\_P6/CT\_INP2  
P0\_31/ADC0\_B23/CT\_INP3

MCXN947VDFT

U2B

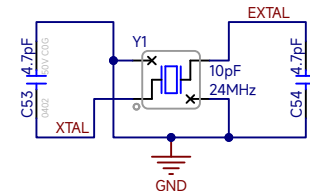
P1.0 C6  
P1.1 C5  
P1.2 C4  
P1.3 B4  
DVP\_D0 A4  
DVP\_D1 B3  
DVP\_D2 B2  
DVP\_D3 A2  
DBG\_RX A1  
DBG\_TX B1  
DVP\_D6 C3  
DVP\_D7 D3

UART\_RX F6  
UART\_TX F4  
DVP\_RST G4

XTAL F1  
EXTAL F2

P1\_0/TSIO\_CH0/ADC0\_A16/CMP0\_IN0/TRIG\_IN0/FC3\_P0/FC4\_P4/CT\_INP4/SCT0\_OUT6/FLEXIO0\_D8/SAI1\_TX\_BCLK/WUU0\_IN6/LPTMR0\_ALT3  
P1\_1/TSIO\_CH1/ADC0\_A17/CMP1\_IN0/TRIG\_IN1/FC3\_P1/FC4\_P5/CT\_INP5/SCT0\_OUT7/FLEXIO0\_D9/SAI1\_TX\_FS/NVM\_SDO  
P1\_2/TSIO\_CH2/ADC0\_A18/CMP2\_IN0/TRIG\_OUT0/FC3\_P2/FC4\_P6/CT1\_MAT0/SCT0\_IN6/FLEXIO0\_D10/ENET0\_MDC/SAI1\_TXD0/CAN0\_TXD0/USB0\_OBE\_TXDATA\_b\_TEST/ADC1\_CONV\_CLK  
P1\_3/TSIO\_CH3/ADC0\_A19/CMP0\_IN1/TRIG\_OUT1/FC3\_P3/CT1\_MAT1/SCT0\_IN7/FLEXIO0\_D11/ENET0\_MDIO/SAI1\_RXD0/CAN0\_RXD/TP\_HTRANS/WUU0\_IN7  
P1\_4/TSIO\_CH4/ADC0\_A20/CMP0\_IN2/FREQME\_CLK\_IN0/FC3\_P4/FC5\_P0/CT1\_MAT2/SCT0\_OUT0/FLEXIO0\_D12/EZH\_PIO0/ENET0\_TX\_CLK/SAI0\_TXD1/USB0\_IND\_RXDM\_TEST/ADC1\_CONV\_DATA0/WUU0\_IN8  
P1\_5/TSIO\_CH5/ADC0\_A21/CMP0\_IN3/FREQME\_CLK\_IN1/FC3\_P5/FC5\_P1/CT1\_MAT3/SCT0\_OUT1/FLEXIO0\_D13/EZH\_PIO1/ENET0\_TXEN/SAI0\_RXD1/USB0\_IND\_RXDP\_TEST/ADC1\_CONV\_DATA1  
P1\_6/TSIO\_CH6/ADC0\_A22/TRIG\_IN2/FC3\_P6/FC5\_P2/CT\_INP6/SCT0\_IN0/FLEXIO0\_D14/EZH\_PIO2/ENET0\_TXD0/SAI1\_RX\_BCLK/CAN1\_TXD/USB0\_IND\_RXDIFF\_TEST/ADC1\_CONV\_DATA2  
P1\_7/TSIO\_CH7/ADC0\_A23/TRIG\_OUT2/FC5\_P3/CT\_INP7/SCT0\_IN1/FLEXIO0\_D15/EZH\_PIO3/PLU\_CLK/ENET0\_TXD1/SAI1\_RX\_FS/CAN1\_RXD/USB0\_DO\_TXDATA\_TEST/ADC1\_CONV\_DATA3/WUU0\_IN9  
P1\_8/TSIO\_CH17/ADC1\_A8/TRACE\_DATA0/FC4\_P0/FC5\_P4/CT\_INP8/SCT0\_OUT2/FLEXIO0\_D16/EZH\_PIO4/PLU\_OUT0/ENET0\_TXD2/I3C1\_SDA/TP\_DATA0/WUU0\_IN10/LPTMR1\_ALT3  
P1\_9/TSIO\_CH18/ADC1\_A9/TRACE\_DATA1/FC4\_P1/FC5\_P5/CT\_INP9/SCT0\_OUT3/FLEXIO0\_D17/EZH\_PIO5/PLU\_OUT1/ENET0\_TXD3/I3C1\_SCL/TP\_DATA1  
P1\_10/TSIO\_CH19/ADC1\_A10/TRACE\_DATA2/FC4\_P2/FC5\_P6/CT2\_MAT0/SCT0\_IN2/FLEXIO0\_D18/EZH\_PIO6/PLU\_IN0/ENET0\_TXER/CAN0\_TXD/TP\_DATA2  
P1\_11/TSIO\_CH20/ADC1\_A11/TRACE\_DATA3/FC4\_P3/CT2\_MAT1/SCT0\_IN3/FLEXIO0\_D19/EZH\_PIO7/PLU\_IN1/ENET0\_RX\_CLK/I3C1\_PUR/CAN0\_RXD/TP\_DATA3/WUU0\_IN11  
P1\_12/TSIO\_CH21/ADC1\_A12/TRACE\_CLK/FC4\_P4/FC3\_P0/CT2\_MAT2/SCT0\_OUT4/FLEXIO0\_D20/EZH\_PIO8/PLU\_OUT2/ENET0\_RXER/CAN1\_RXD/USB0\_PUE\_PULLDOWN\_DP\_TEST/ADC1\_CONV\_DATA4/WUU0\_IN12  
P1\_13/TSIO\_CH22/ADC1\_A13/TRIG\_IN3/FC4\_P5/FC3\_P1/CT2\_MAT3/SCT0\_OUT5/FLEXIO0\_D21/EZH\_PIO9/PLU\_OUT3/ENET0\_RXDV/CAN1\_TXD/USB0\_OBE\_TXBIAS\_b\_TEST/ADC1\_CONV\_DATA5  
P1\_14/TSIO\_CH23/ADC1\_A14/FC4\_P6/FC3\_P2/CT\_INP10/SCT0\_IN4/FLEXIO0\_D22/EZH\_PIO10/PLU\_IN2/ENET0\_RXD0/USB0\_DO\_TXSZERO\_TEST/ADC1\_CONV\_DATA6  
P1\_15/TSIO\_CH24/ADC1\_A15/FC3\_P3/CT\_INP11/SCT0\_IN5/FLEXIO0\_D23/EZH\_PIO11/PLU\_IN3/ENET0\_RXD1/I3C1\_PUR/USB0\_SRE\_LOWSPEED\_TEST/ADC1\_CONV\_DATA7/WUU0\_IN13  
P1\_16/ADC1\_A16/FC5\_P0/FC3\_P4/CT\_INP12/SCT0\_OUT6/FLEXIO0\_D24/EZH\_PIO12/PLU\_OUT4/ENET0\_RXD2/I3C1\_SDA/WUU0\_IN14  
P1\_17/ADC1\_A17/FC5\_P1/FC3\_P5/CT\_INP13/SCT0\_OUT7/FLEXIO0\_D25/EZH\_PIO13/PLU\_OUT5/ENET0\_RXD3/I3C1\_SCL  
P1\_18/ADC1\_A18/FREQME\_CLK\_IN0/FC5\_P2/FC3\_P6/CT3\_MAT0/SCT0\_IN6/FLEXIO0\_D26/EZH\_PIO14/PLU\_IN4/ENET0\_COL/CAN0\_TXD  
P1\_19/ADC1\_A19/FREQME\_CLK\_IN1/FC5\_P3/CT3\_MAT1/SCT0\_IN7/FLEXIO0\_D27/EZH\_PIO15/PLU\_IN5/ENET0\_CRS/CAN0\_RXD/WUU0\_IN15  
P1\_20/ADC1\_A20/CMP1\_IN3/TRIG\_IN2/FC5\_P4/FC4\_P0/CT3\_MAT2/SCT0\_OUT8/FLEXIO0\_D28/EZH\_PIO16/PLU\_OUT6/ENET0\_MDC/CAN1\_TXD  
P1\_21/ADC1\_A21/CMP2\_IN3/TRIG\_OUT2/FC5\_P5/FC4\_P1/CT3\_MAT3/SCT0\_OUT9/FLEXIO0\_D29/EZH\_PIO17/PLU\_OUT7/ENET0\_MDIO/SAI1\_MCLK/CAN1\_RXD  
P1\_22/ADC1\_A22/TRIG\_IN3/FC5\_P6/FC4\_P2/CT\_INP14/SCT0\_OUT4/FLEXIO0\_D30/EZH\_PIO18  
P1\_23/ADC1\_A23/FC4\_P3/CT\_INP15/SCT0\_OUT5/FLEXIO0\_D31/EZH\_PIO19  
P1\_30/XTAL48M/TRIG\_OUT3/CT\_INP16/SCT0\_OUT8/SAI0\_MCLK  
P1\_31/EXTAL48M/TRIG\_IN4/CT\_INP17/SCT0\_OUT9

MCXN947VDFT



Title: MCXVision

Author: 猫肆管

Subtitle: MCU-GPIO0&amp;1

Revision: V1.0.3.0

Size: A4

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U2C

H2	P2_0/TRIG_IN5/FC9_P6/SDHC0_D5/SCT0_IN0/PWM1_A3/FLEXIO0_D8/EZH_PIO20/FLEXSPI0_B_SS1_b/SAI0_RX_BCLK/ESPI0_RST
H1	P2_1/TRACE_CLK/SDHC0_D4/SCT0_IN1/PWM1_B3/FLEXIO0_D9/EZH_PIO21/FLEXSPI0_B_DQS/SINC0_MCLK_OUT0/SAI0_RX_FS/ESPI0_NOTIFY
H3	P2_2/CLKOUT/FC9_P3/SDHC0_D1/SCT0_OUT0/PWM1_A2/FLEXIO0_D10/EZH_PIO22/FLEXSPI0_B_SS0_b/SINC0_MCLK0/SAI0_TXD0/ESPI0_CSn/USB0_IBE_RXDIFF_b_TEST/WUU0_IN16
J3	P2_3/FC9_P1/SDHC0_D0/SCT0_OUT1/PWM1_B2/FLEXIO0_D11/EZH_PIO23/FLEXSPI0_B_SCLK/SINC0_MBIT0/SAI0_RXD0/ESPI0_CLK/USB0_PUE_PULLDOWN_DM_TEST
K3	P2_4/FC9_P0/SDHC0_CLK/SCT0_OUT2/PWM1_A1/FLEXIO0_D12/EZH_PIO24/FLEXSPI0_B_DATA0/SINC0_MCLK1/SAI0_RXD1/ESPI0_DATA0/USB0_PUE_PULLUP_DP_TEST/WUU0_IN17
K1	P2_5/TRIG_OUT3/FC9_P2/SDHC0_CMD/SCT0_OUT3/PWM1_B1/FLEXIO0_D13/EZH_PIO25/FLEXSPI0_B_DATA1/SINC0_MBIT1/SAI0_TXD1/ESPI0_DATA1/USB0_SRE_LOWSPEED_TEST
K2	P2_6/TRIG_IN4/FC9_P4/SDHC0_D3/SCT0_OUT4/PWM1_A0/FLEXIO0_D14/EZH_PIO26/FLEXSPI0_B_DATA2/SINC0_MCLK2/SAI0_TX_BCLK/ESPI0_DATA2/USB0_PUIMPEL_PULLUP_DP_TEST
L2	P2_7/TRIG_IN5/FC9_P5/SDHC0_D2/SCT0_OUT5/PWM1_B0/FLEXIO0_D15/EZH_PIO27/FLEXSPI0_B_DATA3/SINC0_MBIT2/SAI0_TX_FS/ESPI0_DATA3/USB1_TSTO_PLL_OUT_DIV
M2	P2_8/TRACE_DATA0/SDHC0_D7/SCT0_IN2/PWM1_X0/FLEXIO0_D16/EZH_PIO28/FLEXSPI0_B_DATA4/SINC0_MCLK3/SAI1_TXD0
M1	P2_9/TRACE_DATA1/SDHC0_D6/SCT0_IN3/PWM1_X1/FLEXIO0_D17/EZH_PIO29/FLEXSPI0_B_DATA5/SINC0_MBIT3/SAI1_RXD0
M3	P2_10/TRACE_DATA2/SCT0_IN4/PWM1_X2/FLEXIO0_D18/EZH_PIO30/FLEXSPI0_B_DATA6/SINC0_MCLK4/SAI1_RXD1
N4	P2_11/TRACE_DATA3/SCT0_IN5/PWM1_X3/FLEXIO0_D19/EZH_PIO30/FLEXSPI0_B_DATA7/SINC0_MBIT4/SAI1_TXD1

MCXN947VDFT

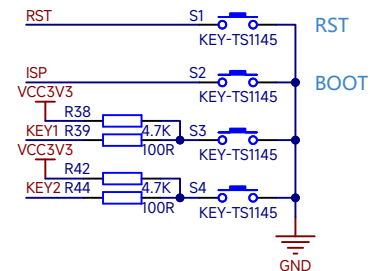
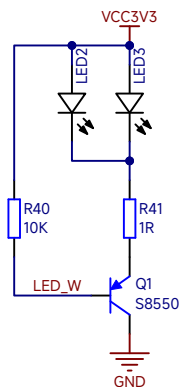
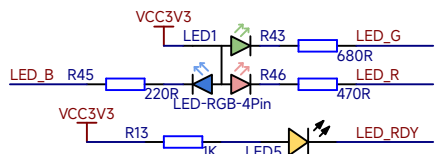
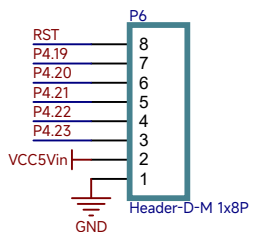
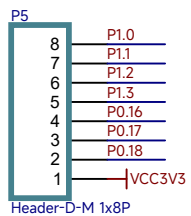
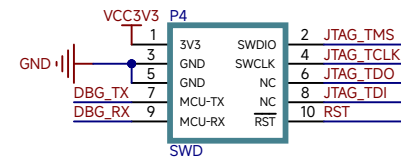
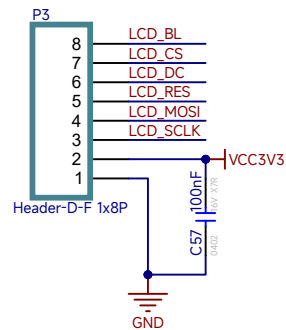
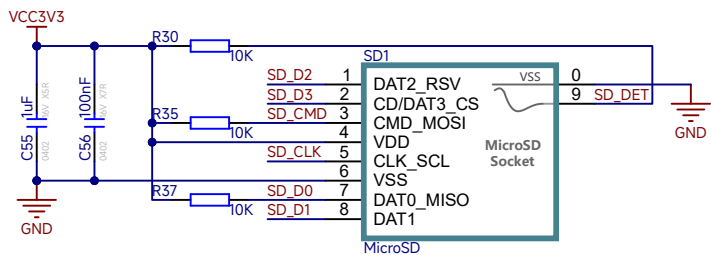
U2D

B17	P3_0/TRIG_IN0/FC7_P3/CT_INP16/PWM0_A0/FLEXIO0_D8/EZH_PIO0/FLEXSPI0_A_SS0_b/PF_QSPI_CS0_DIS/TP_HWRITE/WUU0_IN22
C15	P3_1/TRIG_IN1/FC6_P0/FC7_P6/CT_INP17/PWM0_B0/FLEXIO0_D9/EZH_PIO1/FLEXSPI0_A_SS1_b/PF_QSPI_CS1_DIS/FREQME_CLK_OUT0
D15	P3_2/FC7_P0/CT4_MAT0/PWM0_X0/FLEXIO0_D10/EZH_PIO2/SIM1_PD
D16	P3_3/FC7_P1/CT4_MAT1/PWM0_X1/FLEXIO0_D11/EZH_PIO3/SIM1_RST
F14	P3_4/FC7_P2/CT_INP18/PWM0_X2/FLEXIO0_D12/EZH_PIO4/SIM1_CLK
G14	P3_5/FC7_P3/CT_INP19/PWM0_X3/FLEXIO0_D13/EZH_PIO5/SIM1_IO
D17	P3_6/CLKOUT/FC6_P1/CT4_MAT2/PWM0_A1/FLEXIO0_D14/EZH_PIO6/FLEXSPI0_A_DQS/SIM1_VCCEN/SAI1_MCLK/PF_QSPI_CS_n/FREQME_CLK_OUT1/ADC0_CONV_CLK
D14	P3_7/FC6_P6/FC7_P1/CT4_MAT3/PWM0_B1/FLEXIO0_D15/EZH_PIO7/FLEXSPI0_A_SCLK/SIM0_VCCEN/SAI0_MCLK/PF_QSPI_SCKIN/TP_LOAD_EN
E14	P3_8/FC6_P4/FC7_P0/CT_INP4/PWM0_A2/FLEXIO0_D16/EZH_PIO8/FLEXSPI0_A_DATA0/SIM0_PD/SAI0_TX_BCLK/PF_QSPI_DATA0/TP_ADDR0/WUU0_IN23
F15	P3_9/FC6_P5/FC7_P2/CT_INP5/PWM0_B2/FLEXIO0_D17/EZH_PIO9/FLEXSPI0_A_DATA1/SIM0_RST/SAI0_TX_FS/PF_QSPI_DATA1/TP_ADDR1
F17	P3_10/FC6_P2/FC7_P4/CT1_MAT0/PWM0_A3/FLEXIO0_D18/EZH_PIO10/FLEXSPI0_A_DATA2/SIM0_CLK/SAI0_TXD0/PF_QSPI_DATA2/USB1_TSTL_TX_HS_MODE/TP_ADDR2
F16	P3_11/FC6_P3/FC7_P5/CT1_MAT1/PWM0_B3/FLEXIO0_D19/EZH_PIO11/FLEXSPI0_A_DATA3/SIM0_IO/SAI0_RXD0/PF_QSPI_DATA3/USB1_TSTL_TX_LS_EN/TP_ADDR3/WUU0_IN24
G16	P3_12/FC7_P4/FC6_P4/CT1_MAT2/PWM1_A0/FLEXIO0_D20/EZH_PIO12/FLEXSPI0_A_DATA4/SAI0_RXD1/USB1_TSTL_TX_ENAB/ADC0_CONV_DATA0
H16	P3_13/FC7_P5/FC6_P5/CT1_MAT3/PWM1_B0/FLEXIO0_D21/EZH_PIO13/FLEXSPI0_A_DATA5/SAI0_TXD1/PF_SPL_CS0_n/USB1_TSTL_TX_HIZ/ADC0_CONV_DATA1
H17	P3_14/FC8_P0/CT_INP6/PWM1_A1/FLEXIO0_D22/EZH_PIO14/FLEXSPI0_A_DATA6/SAI0_RX_BCLK/PF_SPL_DATA/USB1_TSTL_TX_DP/ADC0_CONV_DATA2/WUU0_IN25
H15	P3_15/FC8_P1/CT_INP7/PWM1_B1/FLEXIO0_D23/EZH_PIO15/FLEXSPI0_A_DATA7/SAI0_RX_FS/PF_SPL_SCKIN/USB1_TSTL_TX_DN/ADC0_CONV_DATA3
J15	P3_16/FC8_P2/CT_INP8/PWM1_A2/FLEXIO0_D24/EZH_PIO16/SIM0_CLK/SAI1_TX_BCLK/USB1_TSTO_RX_FS/RXD/ADC0_CONV_DATA4
K15	P3_17/FC8_P3/CT_INP9/PWM1_B2/FLEXIO0_D25/EZH_PIO17/SIM0_IO/SAI1_TX_FS/USB1_TSTO_RX_HS_RXD/ADC0_CONV_DATA5/WUU0_IN26
K16	P3_18/FC6_P6/CT2_MAT0/PWM1_X0/FLEXIO0_D26/EZH_PIO18/SAI1_RX_BCLK
K17	P3_19/FC7_P6/CT2_MAT1/PWM1_X1/FLEXIO0_D27/EZH_PIO19/SAI1_RX_FS
M17	P3_20/TRIG_OUT0/FC8_P4/FC6_P0/CT2_MAT2/PWM1_A3/FLEXIO0_D28/EZH_PIO20/SIM0_PD/SAI1_TXD0/PF_SPL_CS0_DIS_n/USB1_TSTO_RX_DISCON_DET/ADC0_CONV_DATA6/WUU0_IN27
L16	P3_21/TRIG_OUT1/FC8_P5/FC6_P1/CT2_MAT3/PWM1_B3/FLEXIO0_D29/EZH_PIO21/SIM0_RST/SAI1_RXD0/PF_SPL_CS1_DIS_n/USB1_TSTO_RX_SQUELCH/ADC0_CONV_DATA7
M16	P3_22/FC8_P6/FC6_P2/CT_INP10/PWM1_X2/FLEXIO0_D30/EZH_PIO22/SIM0_VCCEN/SAI1_RXD1
M15	P3_23/FC6_P3/CT_INP11/PWM1_X3/FLEXIO0_D31/EZH_PIO23/SAI1_TXD1

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	U2E
P1	P4_0/TRIG_IN6/FC2_P0/CT_INP16/EZH_PIO24/PLU_IN0/SINC0_MCLK3/WUU0_IN18
P2	P4_1/TRIG_IN7/FC2_P1/CT_INP17/EZH_PIO25/PLU_IN1
KEY1	P4_2/DAC0_OUT/ADC0_A4/ADC1_A4/CMP0_IN4N/CMP1_IN4N/TRIG_IN6/FC2_P2/CT_INP12/EZH_PIO26/PLU_IN2/SINC0_MBIT3
KEY2	P4_3/DAC1_OUT/ADC0_B4/ADC1_B4/CMP0_IN5N/CMP1_IN5N/CMP2_IN5N/TRIG_IN7/FC2_P3/CT_INP13/EZH_PIO27/PLU_IN3/WUU0_IN19
M6	P4_4/FC2_P4/CT_INP14/EZH_PIO28/PLU_IN4/SINC0_MCLK4
M8	P4_5/FC2_P5/CT_INP15/EZH_PIO29/PLU_IN5/SINC0_MBIT4
N7	P4_6/TRIG_OUT4/FC2_P6/CT_INP18/EZH_PIO30/PLU_CLK
LED_RDY	P4_7/CT_INP19/EZH_PIO31
T6	P4_12/OPAMP0_INP0/ADC0_A5/ADC1_A5/USB0_VBUS_DET/FC2_P0/CT4_MAT0/FLEXIO0_D20/PLU_OUT0/SINC0_MCLK0/CAN0_RXD/MTR_START_BURNIN/WUU0_IN20
T7	P4_13/OPAMP0_INP1/ADC0_B5/ADC1_B5/TRIG_IN8/FC2_P1/USB1_ID/CT4_MAT1/FLEXIO0_D21/PLU_OUT1/SINC0_MBIT0/CAN0_TXD
N8	P4_14/CT4_MAT2/FLEXIO0_D22/PLU_OUT2
T8	P4_15/OPAMP0_OUT/ADC0_A1/CMP0_IN4P/TRIG_OUT4/USB1_VBUS_DIG/CT4_MAT3/FLEXIO0_D23/PLU_OUT3/SINC0_MCLK_OUT0/CAN1_RXD/WUU0_IN21
R8	P4_16/OPAMP1_INP0/ADC0_A6/FC2_P2/USB1_OTG_PWR/CT3_MAT0/FLEXIO0_D24/PLU_OUT4/SINC0_MCLK1/CAN1_TXD
R9	P4_17/OPAMP1_INP1/ADC0_B6/TRIG_IN9/FC2_P3/USB1_OTG_OC/CT3_MAT1/FLEXIO0_D25/PLU_OUT5/SINC0_MBIT1
N10	P4_18/CT3_MAT2/FLEXIO0_D26/PLU_OUT6
P4.19	P4_19/OPAMP1_OUT/ADC0_B1/CMP1_IN4P/TRIG_OUT5/CT3_MAT3/FLEXIO0_D27/PLU_OUT7/SINC0_MCLK_OUT1
P4.20	P4_20/OPAMP2_INP0/ADC1_A6/TRIG_IN8/FC2_P4/CT2_MAT0/FLEXIO0_D28/SINC0_MCLK2
P4.21	P4_21/OPAMP2_INP1/ADC1_B6/TRIG_IN9/FC2_P5/CT2_MAT1/FLEXIO0_D29/SINC0_MBIT2
P4.22	P4_22/CT2_MAT2/FLEXIO0_D30
P4.23	P4_23/OPAMP2_OUT/ADC0_A2/ADC0_B2/ADC1_B3/CMP2_IN4P/TRIG_OUT5/FC2_P6/CT2_MAT3/FLEXIO0_D31/SINC0_MCLK_OUT2
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	U2F
U16	P5_0/EXTAL32K/ADC1_B8/TRIG_IN10/LPTMR0_ALT2
U17	P5_1/XTAL32K/ADC1_B9/TRIG_OUT6/LPTMR1_ALT2
M10	P5_2/ADC1_B10/VBAT_WAKEUP_b/SPC_LPREQ/TAMPER0
N11	P5_3/ADC1_B11/TRIG_IN11/RTC_CLKOUT/TAMPER1
M12	P5_4/ADC1_B12/TRIG_OUT7/SPC_LPREQ/TAMPER2
K12	P5_5/ADC1_B13/TRIG_IN10/LPTMR0_ALT2/TAMPER3
K13	P5_6/ADC1_B14/TRIG_OUT6/LPTMR1_ALT2/TAMPER4
L13	P5_7/ADC1_B15/TRIG_IN11/TAMPER5
L14	P5_8/ADC1_B16/TRIG_OUT7/TAMPER6
M14	P5_9/ADC1_B17/TAMPER7
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