Zhuoran Li

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SUMMARY

Studying Electrical Engineering at ETH Zurich. With an academic background on communications engineering and analog/mixed-signal/RF IC design. Currently focusing on RF front-end circuit design.

EDUCATION

MSc. Electrical Engineering & Information Technology Eidgenössische Technische Hochschule Zürich (ETHz)

Zürich, Switzerland Sept 2023 - Present

BE. Communications Engineering Southeast University (SEU)

Nanjing, China Sept 2019 – Jul 2023

ACADEMIC EXPERIENCE

Master Thesis Project Integrated Systems Laboratory, ETHz

Zürich, Switzerland Mar 2025 – Present

- Project title: Design and Demonstration of RFIC and BFIC
- Research content: to measure and demonstrate the performance of a CMOS beam-forming receiver and to design an LNA.
- Supervisor: Prof. Dr. Hua Wang

Teaching Assistant Integrated Systems Laboratory, ETHz

Zürich, Switzerland Mar 2025 – Jul 2025

Course: 227-0111-00L Radio-Frequency Electronics I

Semester Project Integrated Systems Laboratory, ETHz

Zürich, Switzerland Sept 2024 – Jan 2025

- Project title: Advanced High-Performance GaAs RF/mm-Wave Front-End Integrated Circuit Designs and Testing for Next-Generation Satellite Communications
- Research content: to design a V-band GaAs-based power amplifier used for satellite communications.
- Supervisor: Prof. Dr. Hua Wang

Teaching Assistant Integrated Systems Laboratory, ETHz

Zürich, Switzerland Sept 2024 – Jan 2025

Course: 227-0116-00G VLSI 1: HDL Based Design for FPGAs

Semester Project Integrated Systems Laboratory, ETHz

Zürich, Switzerland Feb 2024 – Jun 2024

- Project title: An RF-DAC in 22nm CMOS-SOI
- Research content: designed a digital-to-analog interface based on the switched-capacitor power amplifier (SPCA) architecture for satellite communication transceivers. A digital decoding and mixing module, a switched-capacitor interface, and a matching network are included.
- Supervisor: Dr. Thomas Burger

Bachelor Thesis Project

State Key Laboratory of Mobile Communications, SEU

Nanjing, China Dec 2022 - Jun 2023

- Project title: CMOS High-Speed 8-bit Flash ADC Design
- Research content: designed a parallel ADC with 4x time-domain interpolation technique.
- Supervisor: Prof. Dr. Lianming Li

Summer Internship

Nanjing, China

State Key Laboratory of Mobile Communications, SEU

Jul 2022 - Sept 2022

Internship content: CMOS operational amplifier design

Student Research Training Project

Nanjing, China Nov 2020 - Nov 2021

Display R&D Center, SEU

- Project title: Research on Underwater Wireless Optical Communication
- Publication: Adaptive beam shaping for enhanced underwater wireless optical communication. Nie J, Tian L, Wang H, Chen L, Li Z, Yue S, Zhang Z, Yang H. Opt Express. 2021 Aug
- Supervisor: Dr. Haining Yang

HONORS & AWARDS

Second Prize of the Rescue Agent Simulation Competition 2021 RoboCup China Open by Chinese Association of Automation Tianjin, China May 2021

Nanjing, China Second Prize Aug 2020

Jiangsu "Internet +" University Student Innovation and Entrepreneurship Competition

EXTRACURRICULAR

Football Team Captain

Nanjing, China

School of Information Science and Engineering, SEU

Sep 2022 - May 2023

Department Director

Nanjing, China

Culture Department of Student Council, School of Electronic Science and Engineering, SEU Sep 2020

CAD SKILLS

- Front-end and back-end analog/RF IC design (Cadence Virtuoso, Keysight ADS, ...)
- Digital IC design (Verilog programming, Vivado, Synopsys, Inovous, ...)
- Mathematic analysis (MATLAB, ...)
- General programming (C++/C, Python)

LANGUAGE SKILLS

- English (working proficiency)
- Chinese (mother tongue)
- German (A2)