



- SHEETS
- 1. BLOCK DIAGRAM
 - 2. MICROPROCESSOR
 - 3. FPGA 1
 - 4. FPGA 2
 - 5. SINE DACS
 - 6. MONITOR DACS
 - 7. RELAY DRIVERS
 - 8. RIBBON CABLES
 - 9. FRONT PANEL
 - 10. CLOCKS, BIST
 - 11. USB/ETHERNET
 - 12. AUX I/O
 - 13. POWER

RIBBON CABLES
TO RANGE BOARDS

1_BLOCK_DIAGRAM

CONFIDENTIAL PROPRIETARY INFORMATION
COPYRIGHT HIGHLAND TECHNOLOGY
ENGINEERING CHANGE ORDERS MAY APPLY

ENGINEER J LARKIN	DATE 3/6/2020	HIGHLAND TECHNOLOGY INC.	
DRAWN B GLASS	3/6/2020	SCHEMATIC, P900 CONTROLLER	
CHECKED			
APPROVED			
RELEASED		DRAWING NO: 23S901	REV: A
SHEET: 1 OF 13 FILE: 23S901A.sch			