



- SHEETS
- 1 BLOCK DIAGRAM
 - 2-5 DAC OUTPUTS
 - 6 ADC INPUTS
 - 7 JX1 MICROZED CONNECTOR
 - 8 JX2 MICROZED CONNECTOR
 - 9 BIST
 - 10 POWER SUPPLIES
 - 11 SYS, POWER SUPPLIES 2
 - 12 CLOCK
 - 13 BUFFERS, CONNECTORS, ETC.

BLOCK DIAGRAM

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ENGINEER C DEVEZE	DATE 2/21/18	HIGHLAND TECHNOLOGY INC.	
DRAWN C DEVEZE	2/21/18	SCHEMATIC, P351	
CHECKED		WAVEFORM PROCESSOR	
APPROVED			
RELEASED		DRAWING NO: 23S351	REV: B
		SHEET: 1 OF 13	FILENAME:23S351B.SCH

UNMARKED PARTS ARE 0603