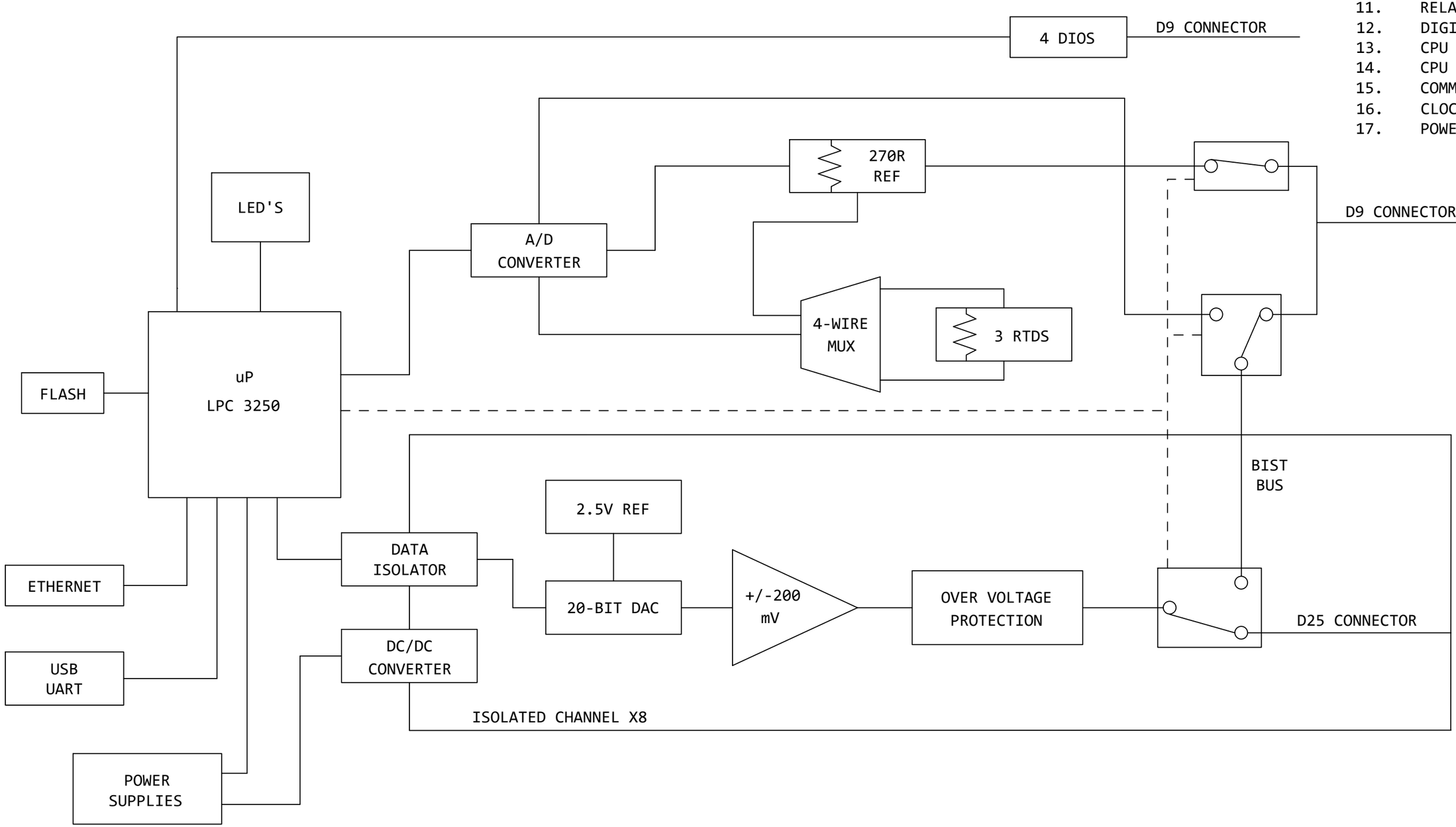
1. BLOCK DIAGRAM
2-9. CHANNELS
10. BIST
11. RELAYS & SSR DRIVERS
12. DIGITAL INPUT/OUTPUT
13. CPU AND CPU POWER
14. CPU DATA FLASH
15. COMMUNICATIONS
16. CLOCKS
17. POWER SUPPLIES



1_BLOCK_DIAGRAM

ENGINEER C DEVEZE	DATE 1/30/18	HIGHLAND TECHNOLOGY INC.
DRAWN C DEVEZE	4/11/2022	SCHEMATIC, P470 THERMOCOUPLE SIMULATOR
CHECKED		
APPROVED		
RELEASED		

DRAWING NO: 23S471 REV: C
SHEET: 1 OF 17 FILE: 23S471C.sch