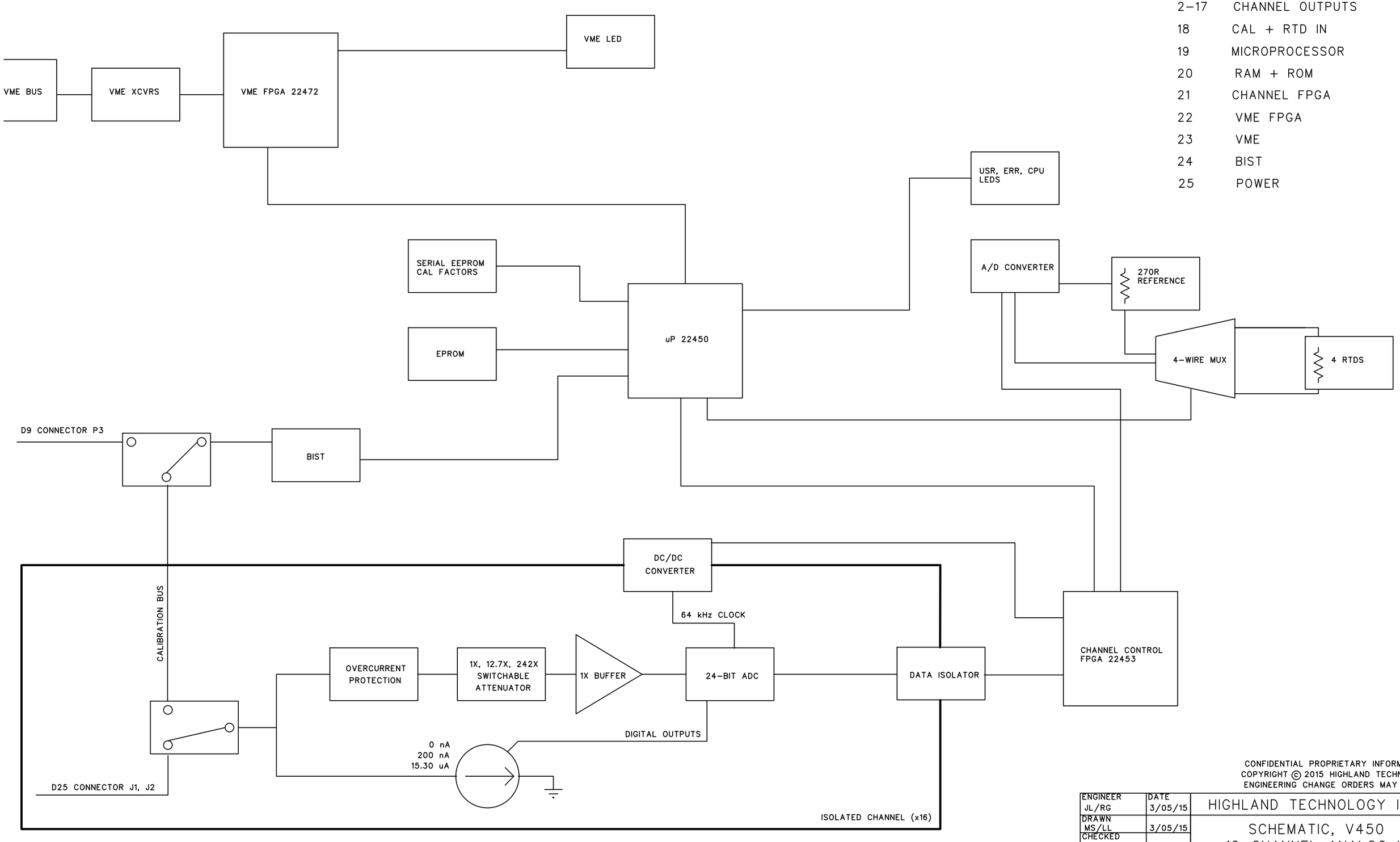
1	BLOCK DIAGRAM
2–17	CHANNEL OUTPUTS
18	CAL + RTD IN
19	MICROPROCESSOR
20	RAM + ROM
21	CHANNEL FPGA
22	VME FPGA
23	VME
24	BIST
25	POWER



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ENGINEER JL/RG	DATE 3/05/15	HIGHLAND TECHNOLOGY INC.
DRAWN MS/LL	3/05/15	SCHEMATIC, V450 16–CHANNEL ANALOG IN
CHECKED		
APPROVED		DRAWING NO: 22S450 REV: G
RELEASED		SHEET: 1 OF 25 FILENAME:22S450G.SCH