

## <u>SHEETS</u>

- 1 BLOCK DIAGRAM
- 2 FRONT PANEL
- 3 PCIe CABLE
- 4 PCIe BUFFER
- 5 PCIe FPGA IF
- 6 CLOCKS

- 7 SENSORS

- 9 VME XCVR 1

- 12 VME64 CONNECTOR
- 8 VOLTAGE MON
- 10 VME XCVR 2
- 11 VME CONNECTOR

- - 14 MICROPROCESSOR
- 13 COMMUNICATIONS
  - 19 BIG CHIP POWER 20 FPGA GROUND

21 POWER SW

22 POWER LIN

23 DIP SWITCHES

- 15 DRAM & FLASH MEMORY
- 16 FPGA 1
- 17 FPGA 2
- 18 FPGA CONFIGURATION

## BLOCK DIAGRAM

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ENGINEER RG / CD	DATE 6/8/18	HIGHLAND TECHNOLOG	Y INC
ING / CD	0,0,10	111011271110 12011110200	
DRAWN 6/8/18 SCHEN		SCHEMATIC, V120	
CHECKED			
		PCIE VME CRATE CONT	ROLLER
APPROVED			1
		drawing no: 22S120	REV: C
RELEASED		DRAWING NO: ZZSIZO	REV: O
		SHEET: 1 OF 23 FILENAME:	22S120C.SCH