

1 BLOCK DIAGRAM

2-25 CHANNELS

26 MICROPROCESSOR

27 VME FPGA

28 VME I/O

29 BIST & LEDs

30 POWER & RELAYS

## 1\_BLOCK\_DIAGRAM

CONFIDENTIAL PROPRIETARY INFORMATION COPYRIGHT HIGHLAND TECHNOLOGY ENGINEERING CHANGE ORDERS MAY APPLY

J LARKIN	DATE 11/8/2018	HIGHLAND	TECHNOLO	GY INC.
CHECKED	11/9/2018	SCHEMATIC, V220 12-CHANNEL 4-20mA		
APPROVED		DRAWING NO:	225220	rev: E
RELEASED				
		SHEET: 1 OF	30 FILE: 22S22	20E.sch