

T560 BLOCK DIAGRAM

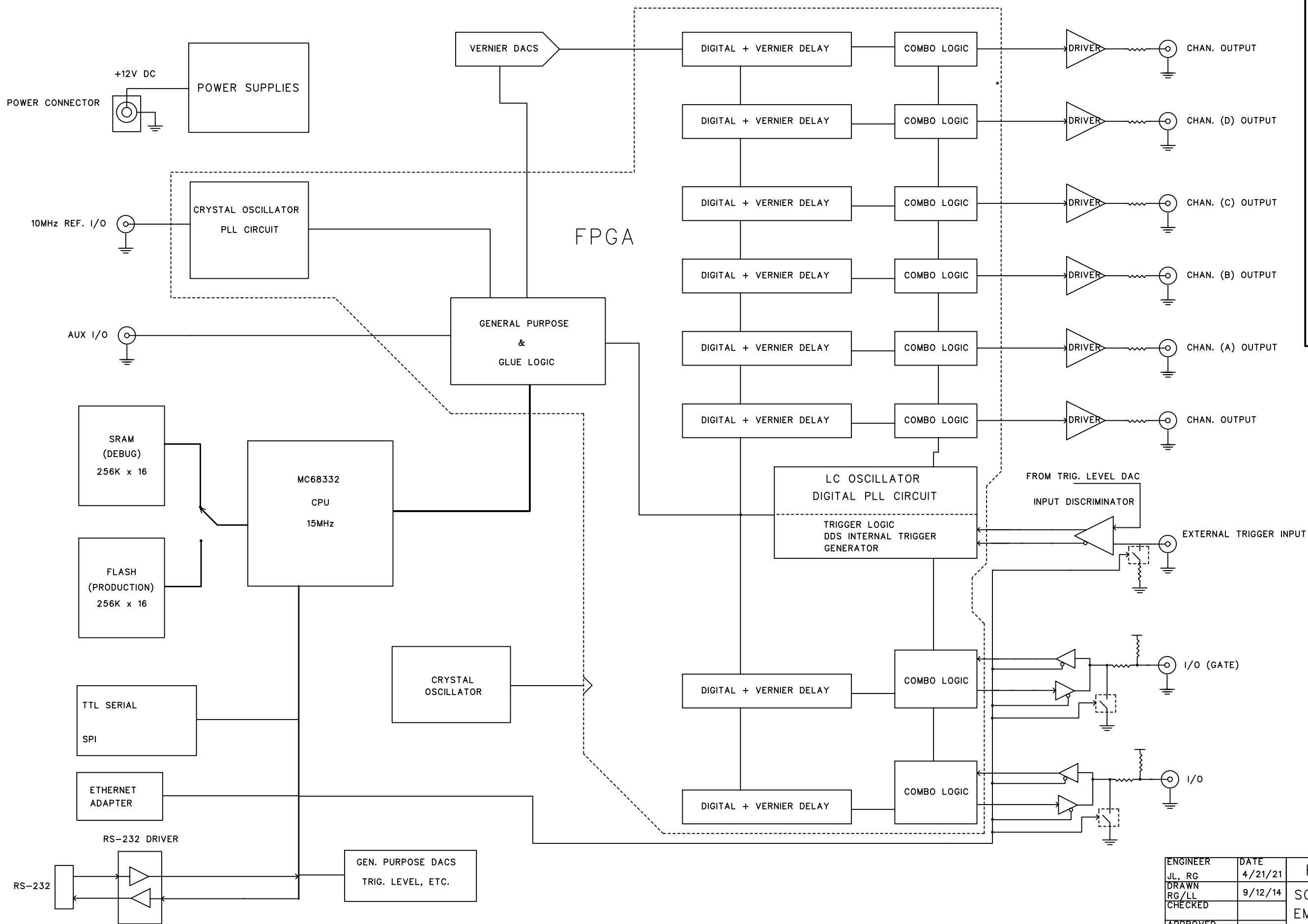


TABLE OF CONTENTS	
1.	BLOCK DIAGRAM & TOC
2.	I/Os, EXT. TRIGGER INPUT
3.	OUTPUTS
4.	LC OSCILLATOR, DPLL
5.	DACS
6.	FPGA
7.	VERNIERS
8.	VERNIERS
9.	CPU
10.	SRAM, FLASH MEMORY
11.	CRYSTAL OSCILLATOR
12.	DDS
13.	COMMUNICATIONS
14.	POWER SUPPLIES

Paved over flecap fix  
28A562Lp June 1 2021

CONFIDENTIAL PROPRIETARY INFORMATION  
COPYRIGHT © 2021 HIGHLAND TECHNOLOGY  
ENGINEERING CHANGE ORDERS MAY APPLY

ENGINEER	DATE	HIGHLAND TECHNOLOGY INC.	
JL, RG	4/21/21		
DRAWN	9/12/14	SCHEMATIC, T560	
RG/LL		EMBEDDED DDG MODULE	
CHECKED			
APPROVED			
RELEASED			
DRAWING NO: 28S562		REV: L	
SHEET: 1 OF 14		FILENAME:28S562L.SCH	