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- The diagram illustrates the hardware architecture for the digital temperature measurement system. The central component is the **uP LPC 3250**, which is connected to several peripheral components:
- LED'S**: Connected to the uP.
 - A/D CONVERTER**: Receives data from the uP and is connected to the **4-WIRE MUX** and the **270R REF**.
 - 4-WIRE MUX**: Connected to the A/D CONVERTER and the **3 RTDS** (Resistance Temperature Detectors).
 - 270R REF**: A reference resistor connected to the A/D CONVERTER and the **4-WIRE MUX**.
 - 20-BIT DAC**: Receives data from the uP via a **DATA ISOLATOR** and is connected to the **DC/DC CONVERTER** and the **OVER VOLTAGE PROTECTION** block.
 - DC/DC CONVERTER**: Connected to the 20-BIT DAC and the **OVER VOLTAGE PROTECTION** block.
 - OVER VOLTAGE PROTECTION**: Connected to the 20-BIT DAC and the **D25 CONNECTOR**.
 - D25 CONNECTOR**: Connected to the OVER VOLTAGE PROTECTION block and the **BIST BUS**.
 - BIST BUS**: Connected to the D25 CONNECTOR and the **4-WIRE MUX**.
 - D9 CONNECTOR**: Connected to the 4-WIRE MUX and the **4 DIOS** (Digital Input/Output System).
 - 4 DIOS**: Connected to the D9 CONNECTOR and the **D9 CONNECTOR**.
 - ISOLATED CHANNEL X8**: A bus connecting the uP to the DC/DC CONVERTER and the DATA ISOLATOR.
- The diagram also includes a legend for the connectors:
- 12. DIGITAL
 - 13. CPU AND
 - 14. CPU DAT.
 - 15. COMMUNI
 - 16. CLOCKS
 - 17. POWER S

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ENGINEER C DEVEZE	DATE 1/30/18	HIGHLAND TECHNOLOGY INC.	
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