



SHEETS

- 1 BLOCK DIAGRAM
- 2 BUS CONNECTOR
- 3 FPGA POWER CONFIG
- 4 FPGA IO NE
- 5 FPGA IO SW
- 6 BIST
- 7 POWER
- 8 CH A CH B DIGITAL CONTROL
- 9 ISOLATED POWER SUPPLY CH A
- 10 ISOLATED POWER SUPPLY CH B
- 11 BUCK CONVERTER CH A
- 12 BUCK CONVERTER CH B
- 13 SIGNAL PROCESSING CH A
- 14 SIGNAL PROCESSING CH B

THIS SCHEMATIC IS BASED ON TEMPLATE 99S522B

1\_BLOCK\_DIAGRAM

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ENGINEER M.G.	DATE 4/3/2023	HIGHLAND TECHNOLOGY INC.	
DRAWN S.Y./L.L.	6/23/2023	SCHEMATIC, P941 DUAL DC POWER SUPPLY	
CHECKED			
APPROVED			
RELEASED		DRAWING NO: 23S941	REV: B
		SHEET: 1 OF 14	FILE: 23S941B.sch