



OFF	DIP SWITCHES	ON
HI-Z IN	1	50R IN
LO-Z OUT	2	50R OUT
0, 2 NORMAL	3	0̄, 2̄ INVERT
1, 3 NORMAL	4	1̄, 3̄ INVERT
TTL	5	10 MHz BPF
3.3V	6	5V

SHEETS

- 1 BLOCK DIAGRAM
- 2 SIGNAL INPUT
- 3 OUTPUTS 1 & 2
- 4 OUTPUTS 2 & 3
- 5 MONITOR
- 6 POWER SUPPLY

1_BLOCK_DIAGRAM

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ENGINEER RG/CD	DATE 3/21/2019	HIGHLAND TECHNOLOGY INC.	
DRAWN RG/CD/BG	3/21/2019	SCHEMATIC, T124 1 TO 4 LOGIC BUFFER	
CHECKED			
APPROVED			
RELEASED		DRAWING NO: 28S123	REV: A
		SHEET: 1 OF 6	FILE: 28S123A.sch