



SHEETS

- 2 CHANNELS 0-7
- 3 CHANNELS 8-15
- 4 CHANNELS 16-23
- 5 CHANNELS 24-31
- 6 CHANNELS 32-39
- 7 CHANNELS 40-47
- 8 CHANNELS 48-55
- 9 CHANNELS 56-63
- 10 CH CONNECTOR J1
- 11 CH CONNECTOR J2
- 12 RELAY DRIVERS
- 13 ADCS
- 14 POWER
- 15 BIST
- 16 SUPPLY BIST
- 17 SYNC TRIGGER
- 18 MICROPROCESSOR DATA
- 19 BIG CHIP POWER
- 20 MEMORY BUS
- 21 FPGA
- 22 VME BUS 1
- 23 VME BUS 2

|  |                  |  |
|--|------------------|--|
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| ENGINEER<br>R GADDI  | DATE<br>10/24/17 | HIGHLAND TECHNOLOGY INC.<br><br>SCHEMATIC, V230<br>ANALOG INPUT MODULE<br><br>DRAWING NO: 22S230<br>SHEET: 1 OF 23 |
| DRAWN<br>B GLASS   | 10/24/17         |  |
| CHECKED  |                  |  |
| APPROVED   |                  |  |
| RELEASED   |                  | REV: D<br>FILENAME:22S230D.SCH   |