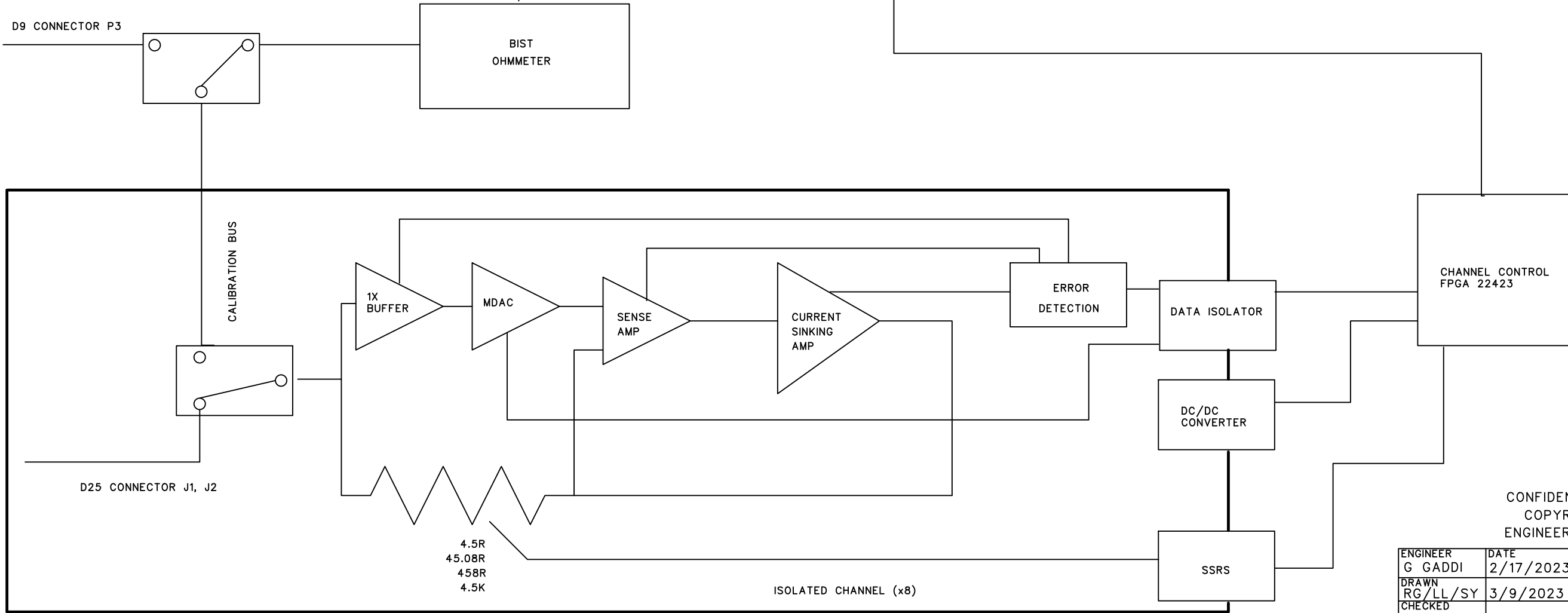


- 1 BLOCK DIAGRAM
- 2-25 CHANNELS
- 26 CAL + RTD IN
- 27 MICROPROCESSOR
- 28 RAM + ROM
- 29 CHANNEL FPGA
- 30 VME FPGA
- 31 VME
- 32 POWER



BLOCK DIAGRAM

CONFIDENTIAL PROPRIETARY INFORMATION
COPYRIGHT HIGHLAND TECHNOLOGY
ENGINEERING CHANGE ORDERS MAY APPLY

ENGINEER	DATE	HIGHLAND TECHNOLOGY INC.	
G GADDI	2/17/2023		
DRAWN	3/9/2023	SCHEMATIC, V420 RESISTANCE SIMULATOR	
RG/LL/SY			
CHECKED			
APPROVED			
RELEASED		DRAWING No:22S420	REV:E
SHEET: 1 OF 32 FILE:22S420E.sch			