



SHEETS

- 1 BLOCK DIAGRAM
- 2-9 CHANNELS 0-63
- 10 RELAY READBACKS
- 11 FPGA CONFIG ETC.
- 12 VME INTERFACE
- 13 POWER SUPPLIES
- 14 FPGA

1_BLOCK_DIAGRAM
CONFIDENTIAL PROPRIETARY INFORMATION
COPYRIGHT HIGHLAND TECHNOLOGY
ENGINEERING CHANGE ORDERS MAY APPLY

ENGINEER	DATE	HIGHLAND TECHNOLOGY INC.	
R GADDI	7/22/2019		
DRAWN	7/22/2019	SCHEMATIC, V210 VME RELAY MODULE	
RG/BG			
CHECKED			
APPROVED			
RELEASED		DRAWING NO: 22S210	REV: D
SHEET: 1 OF 14 FILE: 22S210D.sch			