# HW4: LeNet5 with CPU and CUDA

## 0 Introduction

This is HW4 for 'Multi-core and GPU Programming 2020 Spring' at Yonsei University.

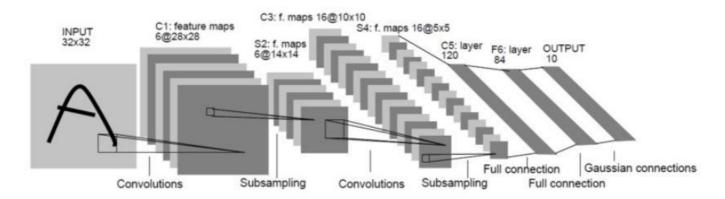
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# 1 Related Things

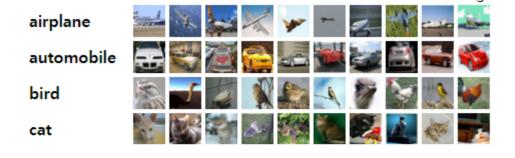
### 1-1 LeNet5 Architectures

There are two convolution layers and max-pooling layers. Then three fully connected layers are applied. You will implement these layers in CUDA version.



#### 1-2 CIFAR 10

The CIFAR-10 dataset consists of 60000 32x32 colour images in 10 classes, with 6000 images per class. You will use 10000 test images to inference the class of image.



# 2 Explanation about the structure of skeleton code

```
user@acsys:/HW4$ tree .
 - build
                                 # objective file will be there
  — hello cuda.cmd
                                 # Example for running cuda on
                                 # condor server
                                 # Run predict on condor server
├── predict.cmd
                                 # Images used by README.md
 — image
    — cifar10 samples.PNG
                                     # CIFAR-10 samples
    LeNet5-architecture.jpg
                                     # LeNet5-architecture
  Makefile
                                 # Makefile
  - model
                                 # Provided pre-trained model
                                     # activation for index 0 at test
    ─ conv1_index_0.txt
    pool1_index_0.txt
     — conv2_index_0.txt
    pool2_index_0.txt
    fc1_index_0.txt
    ├── fc2 index 0.txt
    ─ fc3 index 0.txt
    ─ lenet5-cifar10-epoch_100.pth # Pre-trained model(pickle)
                                     # Used code to train
     - main.py
    └─ values.txt
                                     # model parameters you should use
                                     # test accuracy 72%
  README.md
                                 # What you are reading :3
                                 # Results from condor server
  - result
                                 # Source Code
  - src
     — common.h
                                     # Common
    - hello.cpp
                                     # Example Hello World for CPU
     - hello.cu
                                     # Example Hello World for CUDA
                                     # Base LeNet5
      LeNet5.h
      LeNet5.cpp
```

./predict help will print followings description about arguments.(You should compile first!)

```
user@acsys:/HW4$ ./predict help
[ERROR] Invalid arguments
Usage: ./predict INPUT_PATH DATA_OFFSET BATCH IMG_PATH_TEMPLATE
    INPUT_PATH: path to input data, e.g. /nfs/data/cifar10/test_batch.bin
    DATA_OFFSET: data_offset for input data, e.g. 0
    BATCH: batch size to inference, e.g. 1
    IMG_PATH_TEMPLATE: path template to img, %d will data_offset and %s will be label, e.g. tmp/cifar10_test_%d_%s.bmp
    PARAMETER_PATH: path to parameter, e.g. model/values.txt
```

## 3 What you have todo

### Step 1: Check the data

!!!!!Don't write or update the data!!!!!

```
user@acsys:/HW4$ ls /nfs/data/cifar10/
batches.meta.txt data_batch_2.bin data_batch_4.bin download_cifar10.sh
test_batch.bin
data_batch_1.bin data_batch_3.bin data_batch_5.bin readme.html
```

### Step 2: Implement LeNet\_cuda

Implement normalize, conv, relu, pool, fc with CUDA at src/LeNet5\_cuda.cu.

You should use 32 bit float or 64 bit double on CUDA. In other words, you can't use 8 bit, 16 bit quantization. Also, tensorcore is not allowed on this homework.

You should NOT modify any of main.cpp. We will give ycppou significant penalty if it is modified from the original code. If you think some modification is necessary, please contact us.

In general, we have designed our skeleton code so that you only have to fill LeNet5\_cuda.cu:predict() with your own kernels. However, you can change the class structure at your will if you are not happy with what

we've provided, as long as you don't touch main.cpp.

Check model/main.py to see original python code. Check src/LeNet5\_cpu.cpp to see converted c++
referenced code.

Step 3: Compile and Submit to run on condor server(NOT SUBMIT to PROFESSOR)

```
user@acsys:/HW4$ make run_on_server
/usr/local/cuda-10.2/bin/nvcc -I/usr/local/include/opencv4/opencv -
I/usr/local/include/opencv4 -L/usr/local/lib -lopencv_dnn -lopencv_gapi -
lopencv_highgui -lopencv_ml -lopencv_objdetect -lopencv_photo -lopencv_stitching -
lopencv_video -lopencv_calib3d -lopencv_features2d -lopencv_flann -lopencv_videoio
-lopencv_imgcodecs -lopencv_imgproc -lopencv_core -o build/util.o -c src/util.cpp
/usr/local/cuda-10.2/bin/nvcc -I/usr/local/include/opencv4/opencv -
I/usr/local/include/opencv4 -L/usr/local/lib -lopencv_dnn -lopencv_gapi -
lopencv_highgui -lopencv_ml -lopencv_objdetect -lopencv_photo -lopencv_stitching -
lopencv_video -lopencv_calib3d -lopencv_features2d -lopencv_flann -lopencv_videoio
-lopencv_imgcodecs -lopencv_imgproc -lopencv_core -o build/LeNet5.o -c
src/LeNet5.cpp
/usr/local/cuda-10.2/bin/nvcc -I/usr/local/include/opencv4/opencv -
I/usr/local/include/opencv4 -L/usr/local/lib -lopencv_dnn -lopencv_gapi -
lopencv_highgui -lopencv_ml -lopencv_objdetect -lopencv_photo -lopencv_stitching -
lopencv_video -lopencv_calib3d -lopencv_features2d -lopencv_flann -lopencv_videoio
-lopencv_imgcodecs -lopencv_imgproc -lopencv_core -o build/LeNet5_cpu.o -c
src/LeNet5_cpu.cpp
/usr/local/cuda-10.2/bin/nvcc -I/usr/local/include/opencv4/opencv -
I/usr/local/include/opencv4 -L/usr/local/lib -lopencv_dnn -lopencv_gapi -
lopencv_highgui -lopencv_ml -lopencv_objdetect -lopencv_photo -lopencv_stitching -
lopencv_video -lopencv_calib3d -lopencv_features2d -lopencv_flann -lopencv_videoio
-lopencv_imgcodecs -lopencv_imgproc -lopencv_core -o build/LeNet5_cuda.o -c
src/LeNet5 cuda.cu
/usr/local/cuda-10.2/bin/nvcc -I/usr/local/include/opencv4/opencv -
I/usr/local/include/opencv4 -L/usr/local/lib -lopencv_dnn -lopencv_gapi -
lopencv_highgui -lopencv_ml -lopencv_objdetect -lopencv_photo -lopencv_stitching -
lopencv_video -lopencv_calib3d -lopencv_features2d -lopencv_flann -lopencv_videoio
-lopencv_imgcodecs -lopencv_imgproc -lopencv_core -o build/main.o -c src/main.cpp
/usr/local/cuda-10.2/bin/nvcc -I/usr/local/include/opencv4/opencv -
I/usr/local/include/opencv4 -L/usr/local/lib -lopencv_dnn -lopencv_gapi -
lopencv_highgui -lopencv_ml -lopencv_objdetect -lopencv_photo -lopencv_stitching -
lopencv_video -lopencv_calib3d -lopencv_features2d -lopencv_flann -lopencv_videoio
-lopencv_imgcodecs -lopencv_imgproc -lopencv_core -o predict build/main.o
build/util.o build/LeNet5 cpu.o build/LeNet5 cuda.o build/LeNet5.o
condor submit predict.cmd
Submitting job(s).
1 job(s) submitted to cluster 29608.
```

### Step 4: Check the result

```
user@acsys:/HW4$ ls tmp/
cifar10_test_0_cat.bmp cifar10_test_3_airplane.bmp cifar10_test_6_airplane.bmp
```

```
cifar10_test_7_frog.bmp
cifar10 test 1 ship.bmp cifar10 test 4 frog.bmp
cifar10_test_6_automobile.bmp cifar10_test_8_cat.bmp
cifar10_test_2_ship.bmp cifar10_test_5_frog.bmp
                                                     cifar10_test_7_airplane.bmp
cifar10_test_9_automobile.bmp
user@acsys:/HW4$ cat result/lenet5.out
[INFO] Arguments will be as following:
    INPUT_PATH: test_batch.bin
   DATA_OFFSET: 0
    BATCH: 10
    IMG_PATH_TEMPLATE: tmp/cifar10_test_%d_%s.bmp
    PARAMETER_PATH: values.txt
[INFO] Arguments will be as following:
    INPUT PATH: /nfs/data/cifar10/test batch.bin
    DATA OFFSET: 0
    BATCH: 10
    IMG PATH TEMPLATE: tmp/cifar10 test %d %s.bmp
    PARAMETER PATH: model/values.txt
[INFO] Initialize variables
[INFO] Allocate memories
[INFO] Read image from data offset 0 at /nfs/data/cifar10/test batch.bin
[INFO] Save image to tmp/cifar10_test_0_cat.bmp
[INFO] Save image to tmp/cifar10_test_1_ship.bmp
[INFO] Save image to tmp/cifar10_test_2_ship.bmp
[INFO] Save image to tmp/cifar10_test_3_airplane.bmp
[INFO] Save image to tmp/cifar10_test_4_frog.bmp
[INFO] Save image to tmp/cifar10 test 5 frog.bmp
[INFO] Save image to tmp/cifar10_test_6_automobile.bmp
[INFO] Save image to tmp/cifar10_test_7_frog.bmp
[INFO] Save image to tmp/cifar10 test 8 cat.bmp
[INFO] Save image to tmp/cifar10 test 9 automobile.bmp
[INFO] CPU elapsed time is 21.5806 msec
[INFO] CUDA elapsed time is 492.896 msec
[INFO] CUDA predict is as following:
CPU:CLASS(NUMBER,T/F),CUDA:CLASS(NUMBER,T/F),Label:CLASS(NUMBER)
CPU:
           cat(3,1), CUDA:
                                 cat(3,1), Label:
                                                         cat(3)
CPU: automobile(1,0), CUDA: automobile(1,0), Label:
                                                          ship(8)
CPU:
           ship(8,1), CUDA:
                                 ship(8,1), Label:
                                                          ship(8)
CPU: airplane(0,1), CUDA: airplane(0,1), Label: airplane(0)
CPU:
          frog(6,1), CUDA:
                                 frog(6,1), Label:
                                                         frog(6)
CPU:
           frog(6,1), CUDA:
                                 frog(6,1), Label:
                                                          frog(6)
CPU:
          frog(6,0), CUDA:
                                 frog(6,0), Label: automobile(1)
CPU:
          frog(6,1), CUDA:
                                 frog(6,1), Label:
                                                         frog(6)
CPU:
           cat(3,1), CUDA:
                                 cat(3,1), Label:
                                                          cat(3)
CPU: automobile(1,1), CUDA: automobile(1,1), Label: automobile(1)
Correct
CPU error:20% GPU error:20%
```

Step 5: Have fun speeding up!

- Matrix Multiplication Techniques
- Im2Col
- · Relu, FC, etc
- (We will check the performance with batch=1 and batch=128)

### 4 Criteria

- You should implement the entire LeNet-5 with cuda
- Your kernels should be functionally equivalent to the cpu version
- The trained model is supposed to give around 60% accuracy. We have set a huge margin of +-5% for the difference of the cuda version and the reference C++ version.
- If you fail to implement some part of the kernel in cuda, you can use the CPU version. However, it is your job to make sure to cudaMemcpy() so that the function still works correctly.
- No external libraries. Especially cuDNN. If you think you need something and it's not about cuda programming, contact us before doing so.
- As in HW3, we will measure the performance of batchsize=1 and batchsize=128, and use the product of the two.

## 5 Report

Your report should include

- What techniques you have implemented
- How to run your code
- How each technique affected your performance (+ comparison)
- Why you think your technique, or your combination of techniques produced the best result
- This does not mean that it's a good idea to have the four items above as your report's section names.
- This does not mean that those four items above are all you need in the report.
- You're supposed to show us how much you know, how much you've studied, and how much effort you've put to finish this assignment,
- max 4 pages (firm, including the cover and references if you have those)
- PDF only
- If 4 pages is to short to contain everything you want to say, use a double-column format (e.g., https://ieeecs-media.computer.org/assets/zip/Trans\_final\_submission.zip , https://ieeecs-media.computer.org/assets/zip/ieeetran-final\_sub.zip )

## 6 Grading

- Correct parallel implementation (40) finish within 1 minute, produce correct result with full CUDA implementation
- Report (25) Refer to 5. Report
- Ranking (35) 35 \* (91 rank)/90. The ranking is decided by comparing the product of two inputs (batch=1 and batch=128). We will announce the current top score at least once a week.

## 7 Plus Alpha

#### 7-1 Pretrained Model

There is a pre-trained model at model/values.txt. Loading pre-trained model from the txt file is already implemented in src/LeNet5.cpp.

You can refer to pretrained-model(model/lenet5-cifar10-epoch\_100.pth), activations(\*\_index\_0.txt) and code(model/.main.py).

Activations are result of each layer with index 0 image(cat) at test data.

### 7-2 Hello, World!

I made a hello world example for you.

There are cpu version and cuda version.

#### **CPU**

```
user@acsys:/HW4$ make hello_cpu
/usr/local/cuda-10.2/bin/nvcc -I/usr/local/include/opencv4/opencv -
I/usr/local/include/opencv4 -L/usr/local/lib -lopencv_dnn -lopencv_gapi -
lopencv_highgui -lopencv_ml -lopencv_objdetect -lopencv_photo -lopencv_stitching -
lopencv_video -lopencv_calib3d -lopencv_features2d -lopencv_flann -lopencv_videoio
-lopencv_imgcodecs -lopencv_imgproc -lopencv_core -o hello_cpu src/hello.cpp
./hello_cpu
Hello, World!
```

#### **CUDA**

You can reference this to implment, compile and submit(run on condor) your LeNet5\_cuda.

```
user@acsys:/HW4$ make hello_cuda
/usr/local/cuda-10.2/bin/nvcc -I/usr/local/include/opencv4/opencv -
I/usr/local/include/opencv4 -L/usr/local/lib -lopencv_dnn -lopencv_gapi -
lopencv_highgui -lopencv_ml -lopencv_objdetect -lopencv_photo -lopencv_stitching -
lopencv_video -lopencv_calib3d -lopencv_features2d -lopencv_flann -lopencv_videoio
-lopencv_imgcodecs -lopencv_imgproc -lopencv_core -o hello_cuda src/hello.cu
```

```
user@acsys:/HW4$ make hello_run_on_server
/usr/local/cuda-10.2/bin/nvcc -I/usr/local/include/opencv4/opencv -
I/usr/local/include/opencv4 -L/usr/local/lib -lopencv_dnn -lopencv_gapi -
lopencv_highgui -lopencv_ml -lopencv_objdetect -lopencv_photo -lopencv_stitching -
lopencv_video -lopencv_calib3d -lopencv_features2d -lopencv_flann -lopencv_videoio
-lopencv_imgcodecs -lopencv_imgproc -lopencv_core -o hello_cuda src/hello.cu
condor_submit hello_cuda.cmd
Submitting job(s).
1 job(s) submitted to cluster 29601.
```

```
user@acsys:/HW4$ cat result/hello.out
Hello World from host!
Hello World from device!
    threadIdx.x: 0
    blockIdx.x: 0
    blockDim.x: 3
Hello World from device!
    threadIdx.x: 1
    blockIdx.x: 0
    blockDim.x: 3
Hello World from device!
    threadIdx.x: 2
    blockIdx.x: 0
    blockDim.x: 3
Hello World from device!
    threadIdx.x: 0
    blockIdx.x: 1
    blockDim.x: 3
Hello World from device!
    threadIdx.x: 1
    blockIdx.x: 1
    blockDim.x: 3
Hello World from device!
    threadIdx.x: 2
    blockIdx.x: 1
    blockDim.x: 3
```

## 8 References

- Lecture Slides
- LeCun et al, 1998, Gradient-Based Learning Applied to Document Recognition
- CIFAR-10 dataset
- Neural Network Tutorials Pytorch