



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Sixth Semester July 2015
Electrical & Electronics Engineering

J 872756
15

Name of the Student : POOJA B MALI

Father's / Mother's Name : BHARATESH

Name of the College : KLE DR.M.S. SHESHGIRI COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Seat No. : 2KL12EE022

Sl. No.	Code	Subject Title	Examination Marks			Internal Assessment Marks		Total Marks			Subject Result
			Max.	Min.	Obtained	Max.	Min.	Max.	Min.	Obtained	
1	10EE61	Power System Analysis & Stability	100	35	69	25	-	125	50	90	Pass
2	10EE62	Switchgear & Protection	100	35	45	25	-	125	50	69	Pass
3	10EE63	Electrical Machine Design	100	35	39	25	-	125	50	59	Pass
4	10EE64	Digital Signal Processing	100	35	66	25	-	125	50	87	Pass
5	10EE65	Computer Aided Electrical Drawing	100	35	68	25	-	125	50	83	Pass
6	10EE685	Embedded Systems	100	35	66	25	-	125	50	85	Pass
7	10EEL67	D.C. Machines & Synchronous Machines Lab.	50	20	42	25	12	75	32	63	Pass
8	10EEL68	Control Systems Laboratory	50	20	46	25	12	75	32	68	Pass
GRAND TOTAL			900	364	604						

Total Marks Obtained (in words) : SIX HUNDRED AND FOUR ONLY
Result of the Semester : FIRST CLASS

DATE : OCT 29, 2015

A - Absent NE - Not Eligible

Registrar (Evaluation)

