



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Seventh Semester January 2016
 Electrical & Electronics Engineering

J 256469
 16

Name of the Student : **POOJA B MALI**

Father's / Mother's Name : **BHARATESH**

Seat No. : **2KL12EE022**

Name of the College : **KLE DR.M.S. SHESHGIRI COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM**

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EE71	Computer Techniques in Power System Analysis	100	35	70	25	-	20	125	50	90	Pass
2	10EE72	Electrical Power Utilization	100	35	49	25	-	18	125	50	67	Pass
3	10EE73	High Voltage Engineering	100	35	68	25	-	23	125	50	89	Pass
4	10EE74	Industrial Drives & Applications	100	35	57	25	-	15	125	50	72	Pass
5	10EE752	Programmable Logic Controllers	100	35	72	25	-	16	125	50	88	Pass
6	10EE764	VLSI Circuits & Design	100	35	39	25	-	23	125	50	62	Pass
7	10EEL77	Relay & High Voltage Lab.	50	20	41	25	12	23	75	32	64	Pass
8	10EEL78	Power System Simulation Lab.	50	20	40	25	12	22	75	32	62	Pass
GRAND TOTAL									900	364	594	

Total Marks Obtained (in words) : **FIVE HUNDRED AND NINETY FOUR ONLY**

Result of the Semester : **FIRST CLASS**

DATE : **JUL 25, 2016**

A - Absent NE - Not Eligible



Registrar (Evaluation)