



ವಿಶ್ವವಿದ್ಯಾನಿಲಯ, ಬೆಂಗಳೂರು
VISHVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Third Semester January 2014
Electrical & Electronics Engineering

14 208448

Name of the Student : POOJA B MALI

Father's / Mother's Name : BHARATESH

Seat No. : ZKL12EE022

Name of the College : KLE DR.M.S. SHESHGIRI COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT31	Engineering Mathematics-III	100	35	77	25	-	23	125	50	100	Pass
2	10ES32	Analog Electronic Circuits	100	35	68	25	-	24	125	50	92	Pass
3	10ES33	Logic Design	100	35	49	25	-	22	125	50	71	Pass
4	10ES34	Network Analysis	100	35	67	25	-	21	125	50	88	Pass
5	10EE35	E & E Measurements & Instrumentation	100	35	64	25	-	22	125	50	86	Pass
6	10EE36	Electric Power Generation	100	35	61	25	-	18	125	50	79	Pass
7	10ESL37	Analog Electronics Lab	50	20	40	25	12	25	75	32	65	Pass
8	10ESL38	Logic Design Lab	50	20	44	25	12	23	75	32	67	Pass
GRAND TOTAL									900	364	648	

Total Marks Obtained (in words) : SIX HUNDRED AND FORTY EIGHT ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

DATE : APR 10, 2014

A - Absent NE - Not Eligible

Registrar (Evaluation)