



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS
 BE Fifth Semester January 2015
 Electrical & Electronics Engineering

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15 116418

Name of the Student : POOJA B MALI

Father's / Mother's Name : BHARATESH

Seat No. : 2K112EE022

Name of the College : KLE DR.M.S. SHESHGIRI COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Sl. No.	Code	Subject Title	Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
			Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10AL51	Management & Entrepreneurship	100	35	49	25	-	18	125	50	67	Pass
2	10EE52	Signals & Systems	100	35	69	25	-	23	125	50	92	Pass
3	10EE53	Transmission & Distribution	100	35	62	25	-	18	125	50	80	Pass
4	10EE54	D. C. Machines & Synchronous Machines	100	35	59	25	-	21	125	50	80	Pass
5	10EE55	Modern Control Theory	100	35	71	25	-	25	125	50	96	Pass
6	10EE56	Linear IC's & Applications	100	35	56	25	-	20	125	50	76	Pass
7	10EEL57	Measurements & Circuit Simulation Lab.	50	20	40	25	12	22	75	32	62	Pass
8	10EEL58	Transformers & Induction Machines Lab.	50	20	38	25	12	20	75	32	58	Pass
GRAND TOTAL									900	364	611	

Total Marks Obtained (in words) : SIX HUNDRED AND ELEVEN ONLY

Result of the Semester : FIRST CLASS

DATE : APR 13, 2015

A - Absent NE - Not Eligible



Registrar (Evaluation)

[Signature]