# **M2**

# Schematics (Rev 1.0) JULY. 2007

### **Schematics Description**

SHEET 10

SHEET 11

SHEET Number	SHEET Description	
SHEET 01	01_INDEX	

10 AC'97 Audio Codec

11\_UART

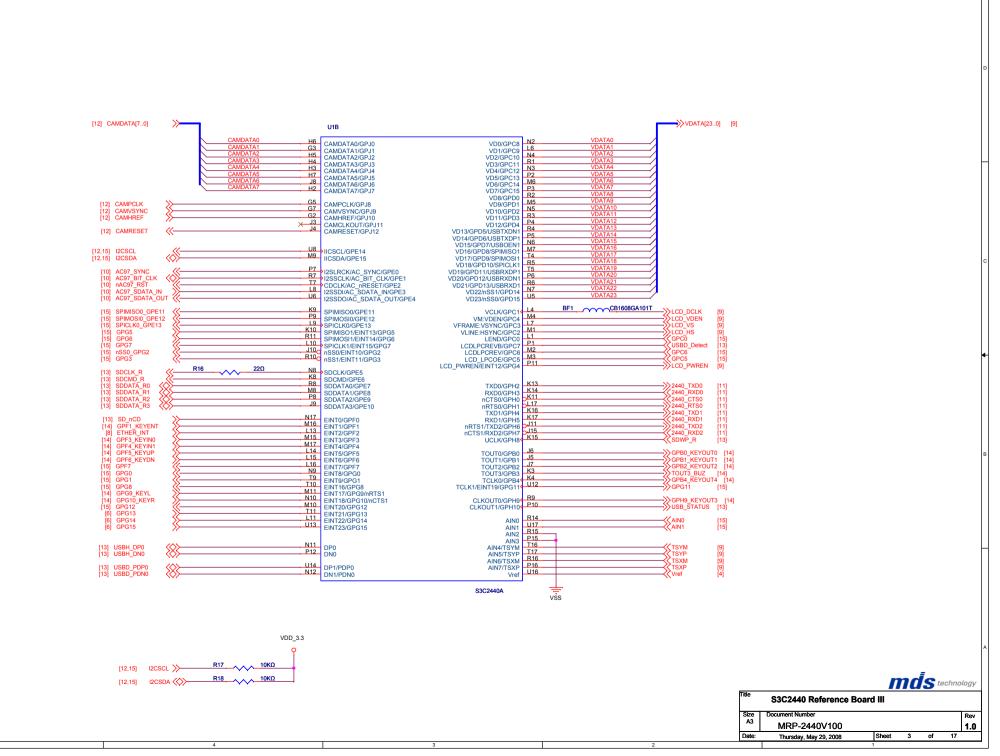
SHEET 01	01_INDEX		
S3C2440A I	Part		
SHEET 02	02_S3C2440A Local Bus	SHEET 12	12_Video Decoder / CMOS Anlaog Camera
SHEET 03	03_S3C2440A I/O	SHEET 13	13_SD, USB Host, USB Device
SHEET 04	04_S3C2440A Power , JTAG	SHEET 14	14_Button, Test LED, Buzzer
SHEET 05	05_Memory (SDRAM)	SHEET 15	15_Extension IO Connector
SHEET 06	06_NOR, NAND Flash Memory	SHEET 16	16_Power
SHEET 07	07_Local BUS Buffer	SHEET 17	17_Debuging History
SHEET 08	08_10/100M Ethernet Controller		
SHEET 09	09_4.3" TFT LCD, Touch, LED Driver		
	7	1	

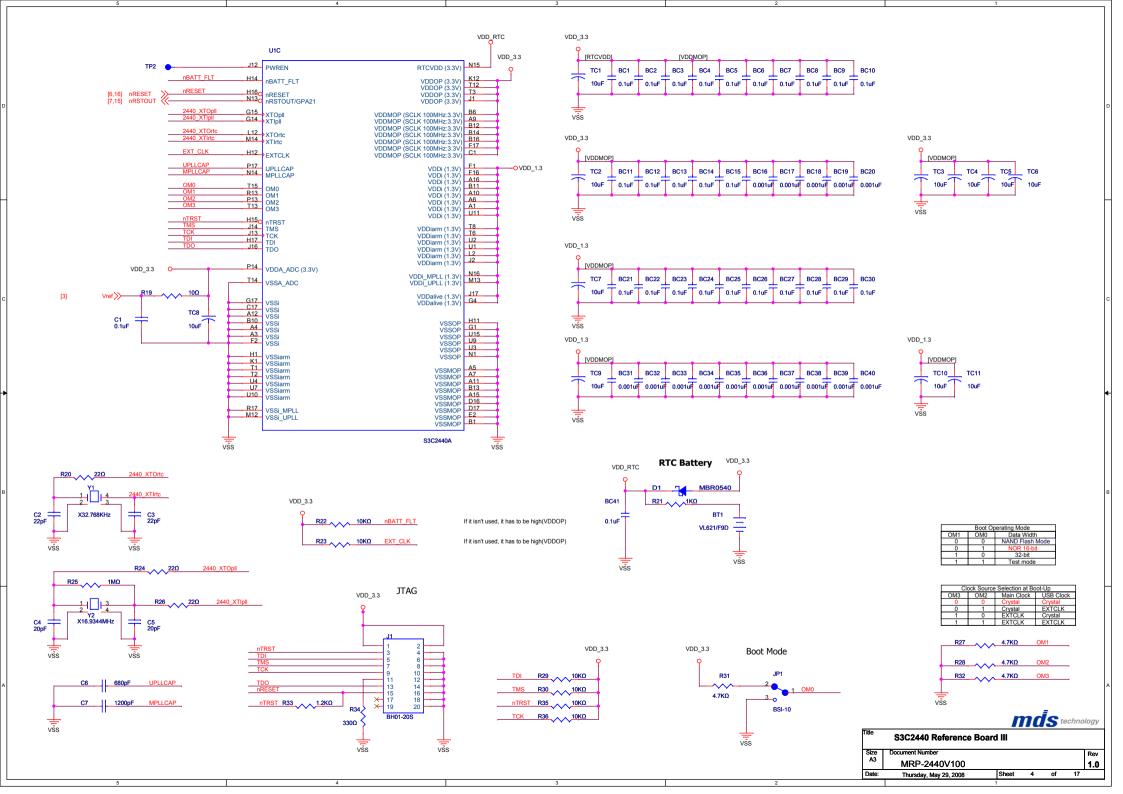
## M<sub>3</sub> S<sub>3</sub>C<sub>2440</sub> Reference Board

os	WinCE 5.0
СРИ	SAMSUNG S3C2440A
Memory	128MBytes SDRAM 128MBytes NAND Flash, 4MByte NOR Flash
POWER	Main DC12V 2A IN CPU CORE: 1.3V , IO : 3.3V

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mds technology S3C2440 Reference Board III Size A3 Document Number Rev MRP-2440V100 1.0 Date: Sheet 2 of Thursday, May 29, 2008 17





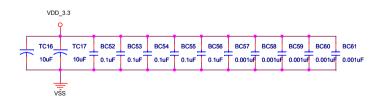


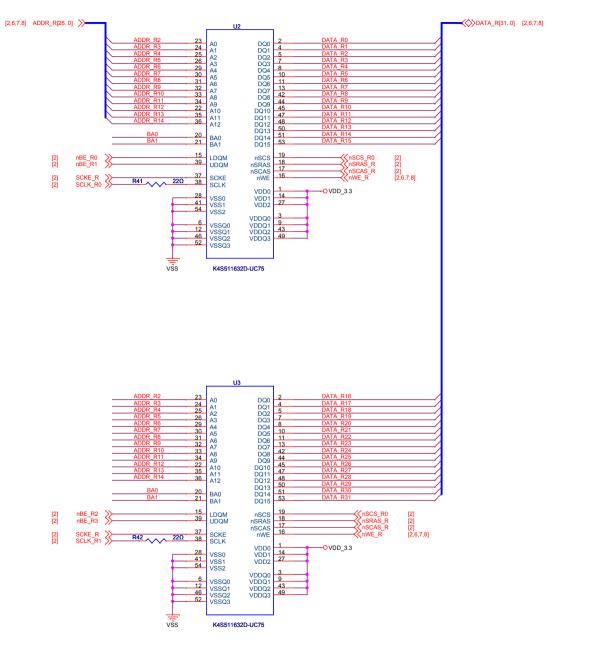


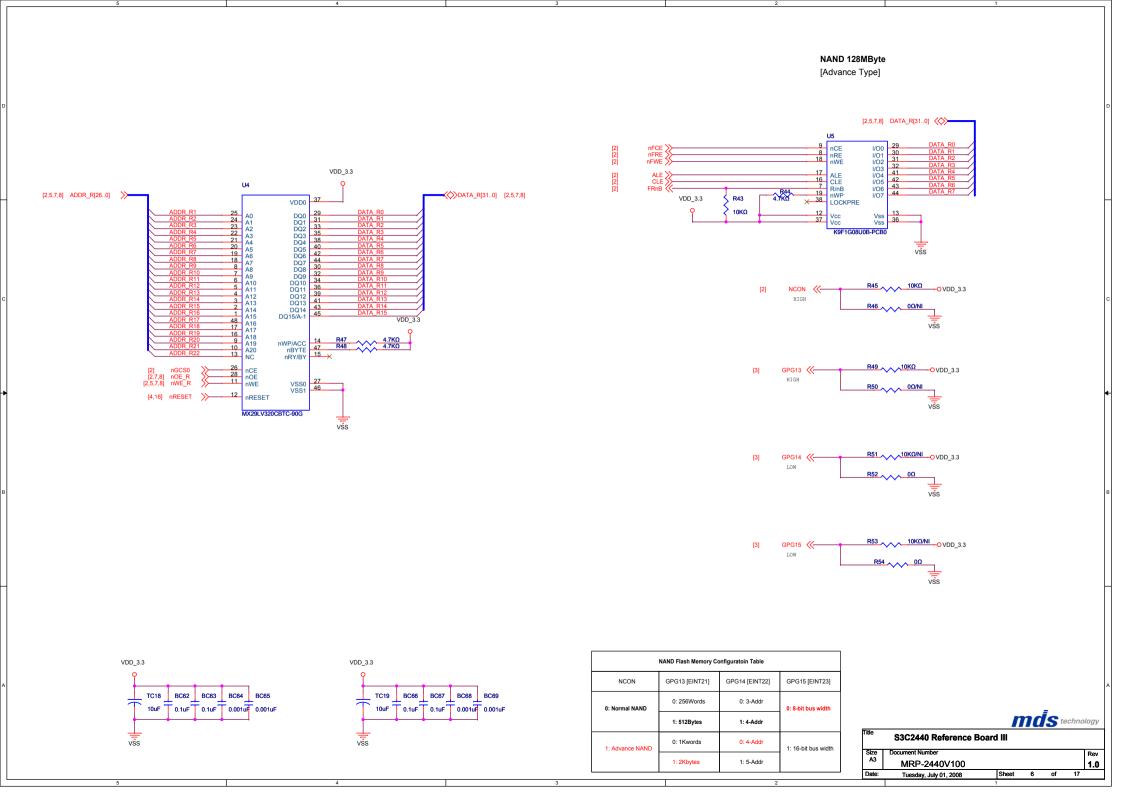
#### Memory Size Bank Select

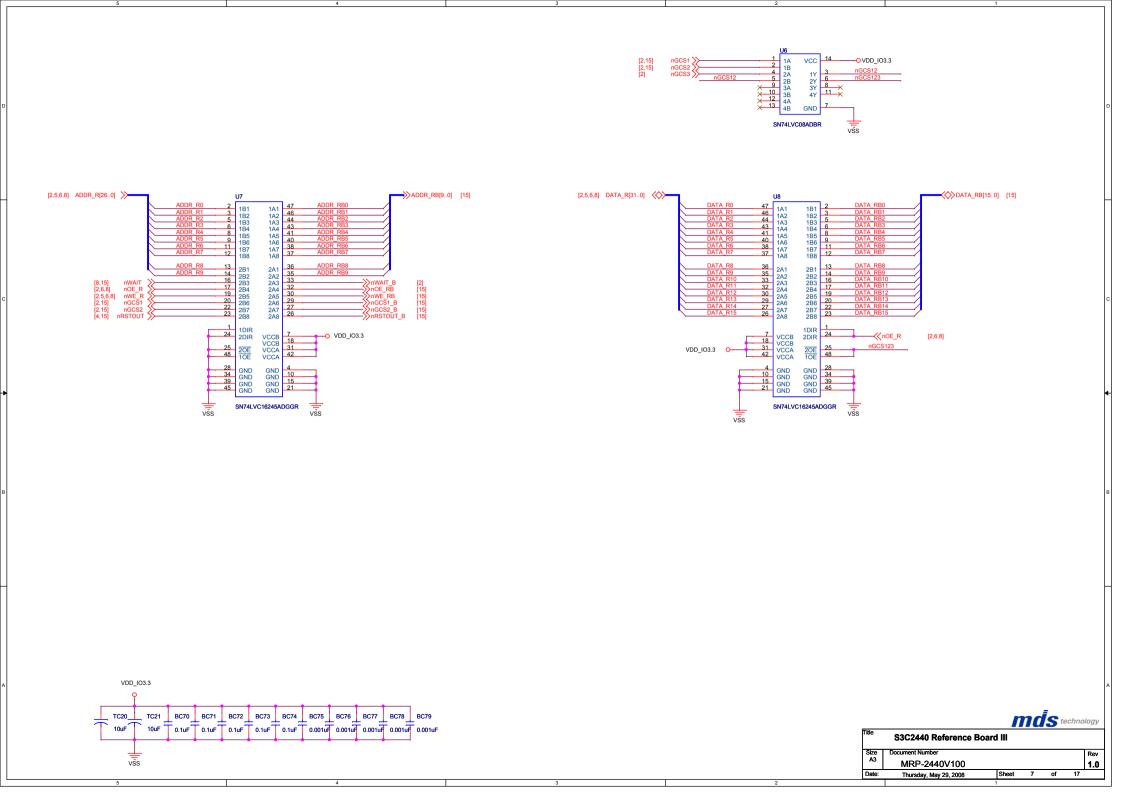
	64MByte 128MByte	
BA0	ADDR_R24	ADDR_R25
BA1	ADDR_R25	ADDR_R26

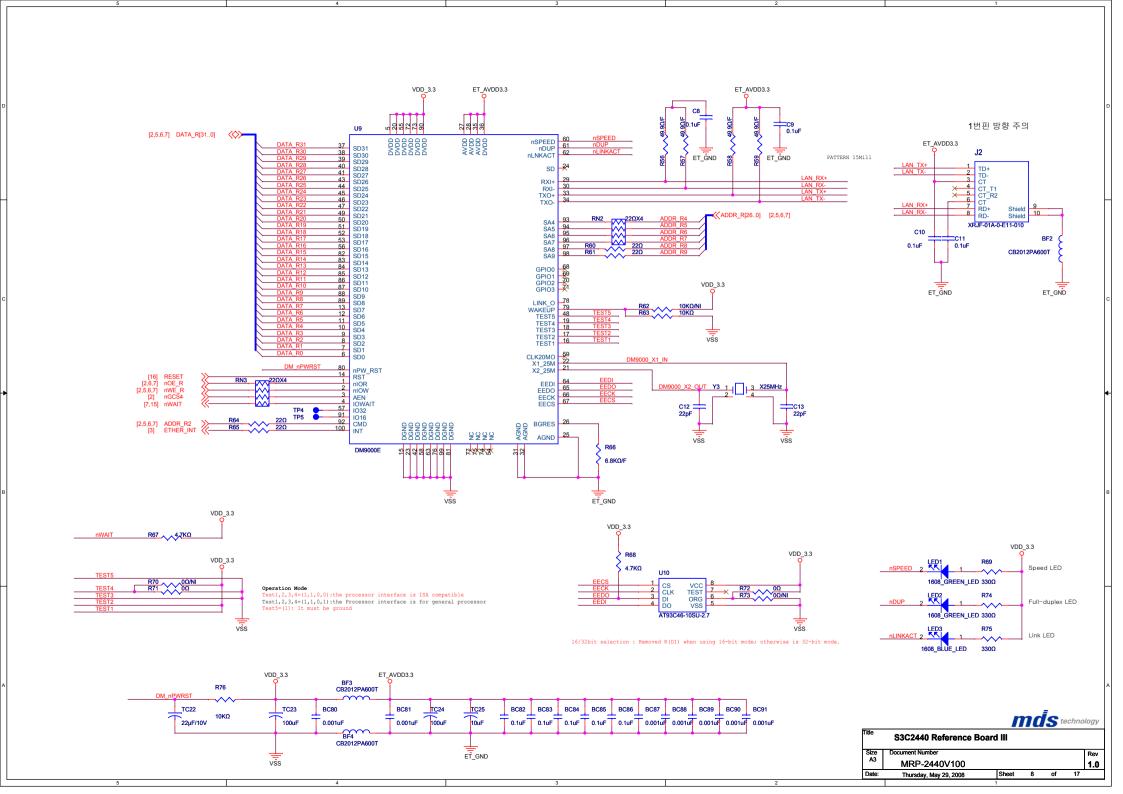


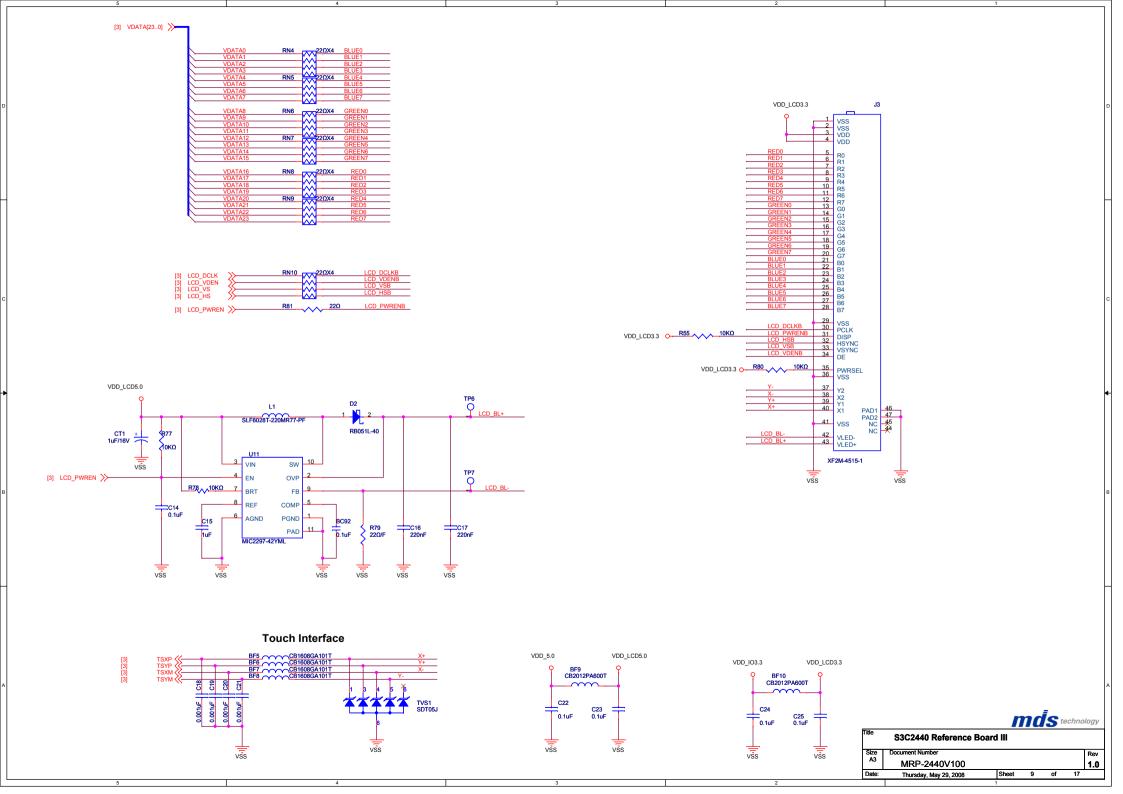


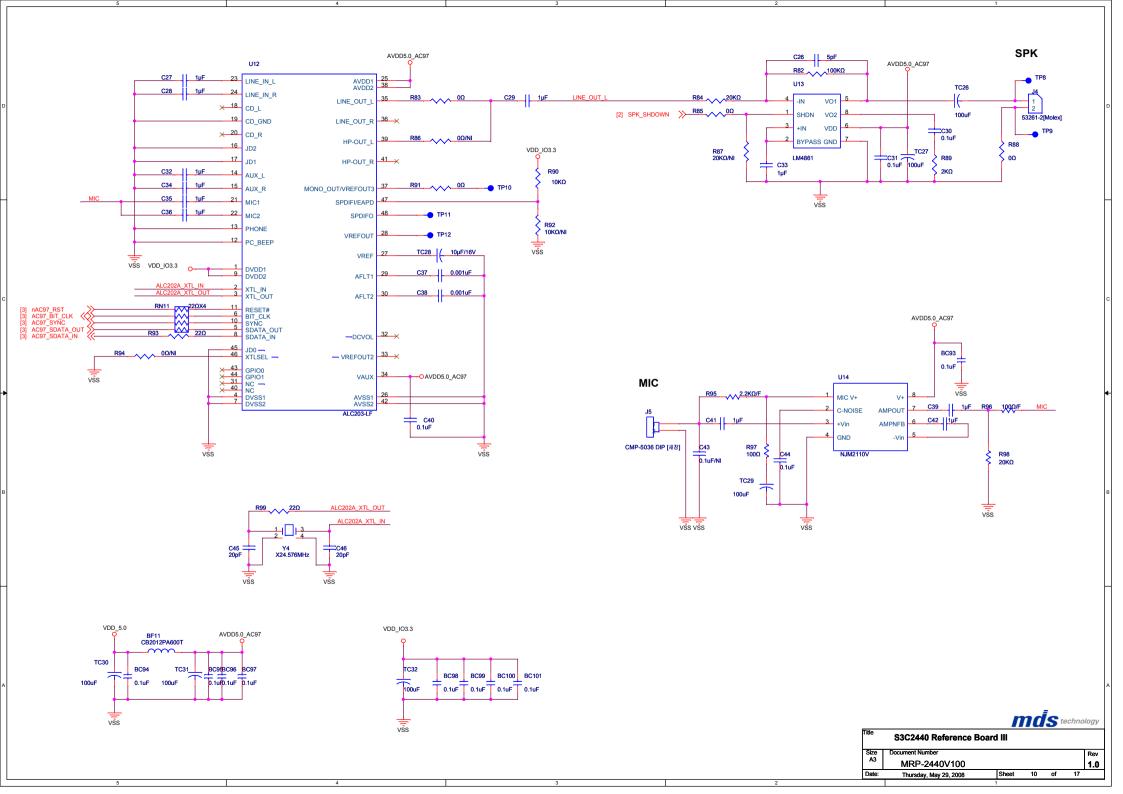


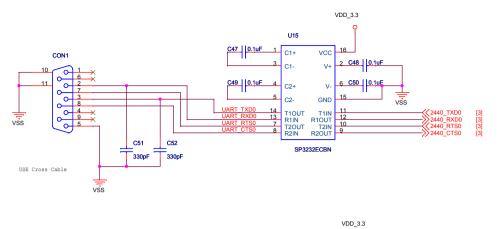


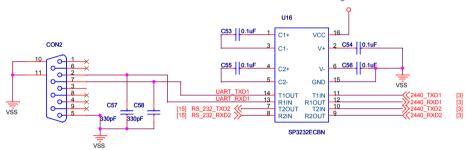






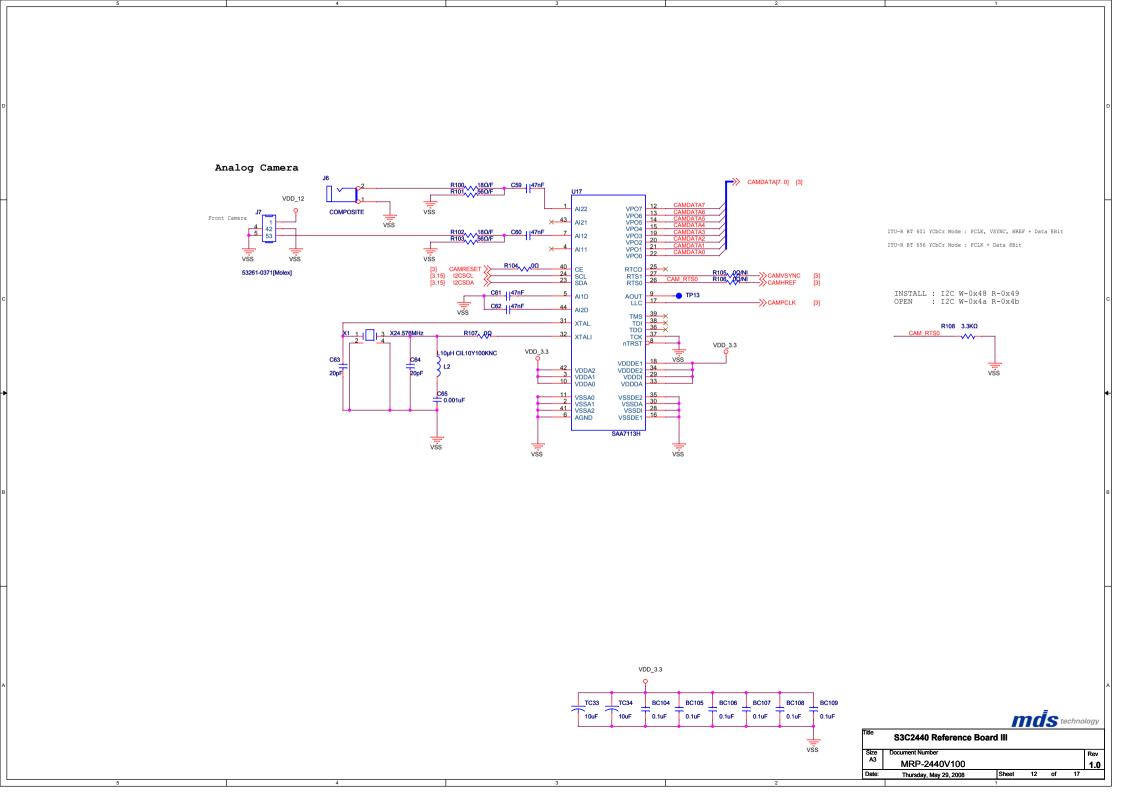


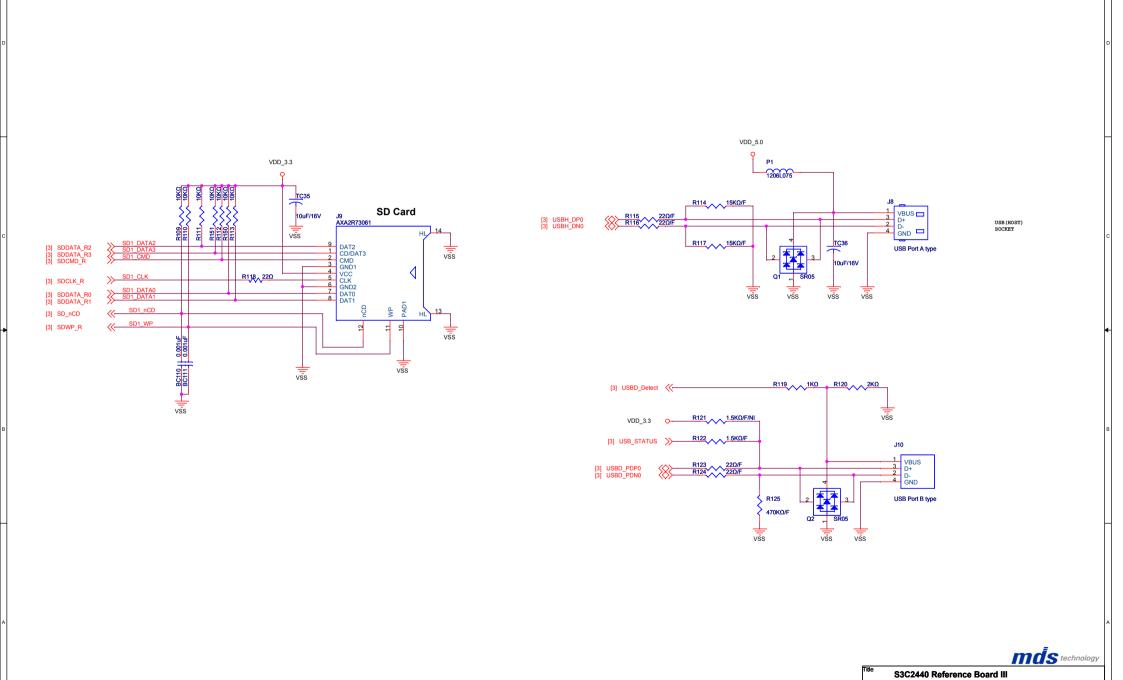






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Title	S3C2440 Reference Board III							
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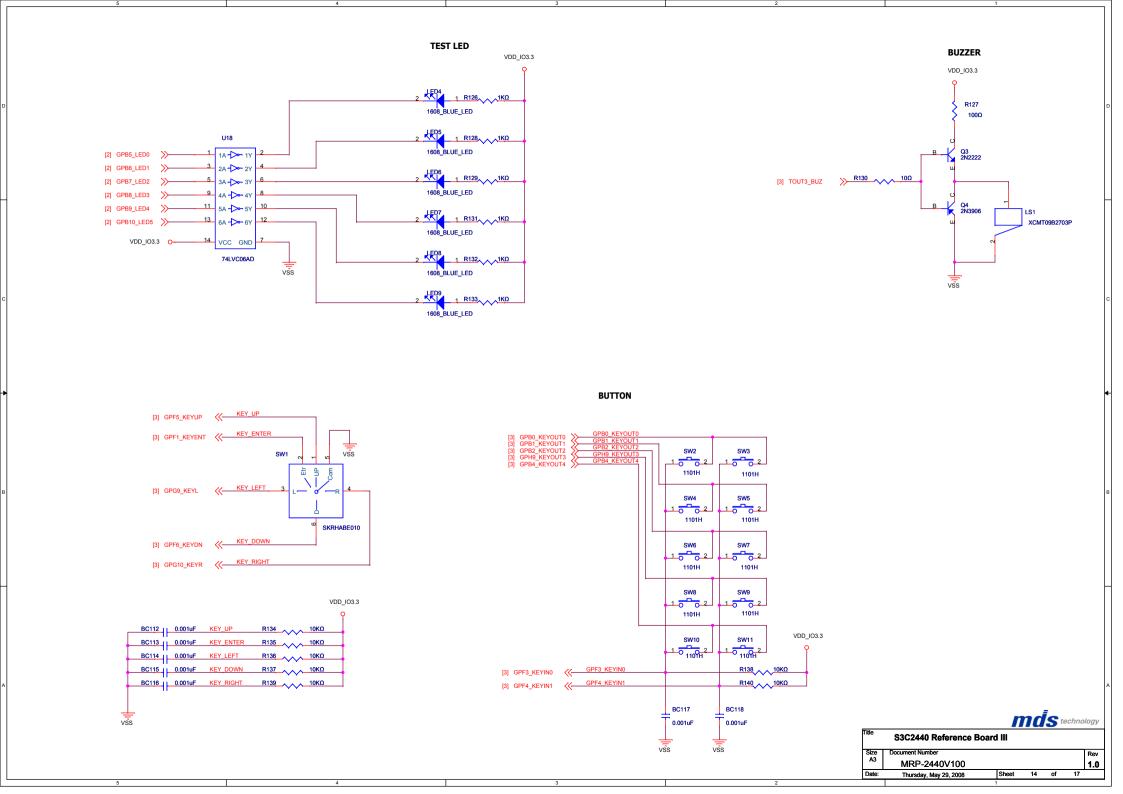


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### Extension I/O Connector

