Luka Kurešević

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Work Experience

Parallel Systems Architecture Lab, EPFL (Summer@EPFL)   May 2025 – Jul 2025
Research Intern

Research and development of heuristics for addressing NP-hard problems in electronic design automation for FPGAs (specifically FPGA routing; construction of congestion aware Steiner minimal trees in graphs).

Education

Faculty of Sciences, University of Novi Sad   Oct 2023 – Jun 2026*
BSc in Computer Science
- Current GPA: 10.00/10

Gymnasium "Jovan Jovanović Zmaj", Novi Sad   Sep 2019 – Jun 2023
Special Course for Students Gifted in Mathematics
- GPA: 4.96/5

Primary School at the "Jovan Jovanović Zmaj" Gymnasium, Novi Sad   Sep 2017 – Jun 2019
Honors Class
- GPA: 5.00/5

Skills

Programming Languages: C++, Java, Python

Tools and Frameworks: PyTorch (including Torchaudio and PyTorch Geometric), VTR, Pandas, Numpy, Scikit-learn, SQL Developer

Coursework

MLIR-Based Optimizations for Graph Neural Networks   4th semester (in progress)
Coursework for "Experiments with Neural Networks 2" (Faculty of Sciences, University of Novi Sad)

A Novel RSMT Construction Technique Based On GNN-Inspired Message Passing   3rd Semester
Coursework for "Algorithms in Hardware Design" (Faculty of Sciences, University of Novi Sad)

Performance comparison of CNN and LSTM Architectures for Environmental Sound Classification (link)   2nd Semester
Coursework for "Experiments with Neural Networks 1" (Faculty of Sciences, University of Novi Sad)

Other Projects

Graph coverage with random walks (link)   *(Probability theory)*

Competitive Programming

Qualified for Nationals   2020, 2023

Volunteer Work

Data Science Conference Europe 2024: Moderated online tech tutorials and assisted hosts during the live event.

Languages

English (C2, Cambridge Proficiency Exam), **German** (B1, Internationales Kulturinstitut Vienna), **Serbian** (Native)