

1 Assembly Examples

The following examples may or may not have accompanying C code. Most of it is just assembly.

Random Examples

This section just shows some random examples, they're not organized into functions or anything. Most lines are independent.

```
1 // Add and subtract
2 ADD R0, R0, R1    // R0 <- R0 + R1
3 SUB R0, R0, R1    // R0 <- R0 - R1
4 ADDS R0, R0, R1   // R0 <- R0 + R1 and update flags
5 SUBS R0, R0, R1   // R0 <- R0 - R1 and update flags
6 // The flags will be things like the zero flag (z), negative (n), etc. See the manual for more info
7
8
9 // BX LR returns from the assembly function
10 // It puts the address of the next instruction (from the caller)
11 // into the correct address.
12 BX LR
13
14
15 // Form CMP, the result is thrown away and flags are updated
16 // This is useful for a lot of things. For example, CMP to a number
17 // and the z flag will be set if they're equal.
18 CMP R0, R1
19
20
21 // This means R0 - 0, which seems like it does nothing,
22 // but all the flags are being updated.
23 // Example, the zero flag Z = 1 if R0 is zero
24 CMP R0, #0
25 // This would set Z = 1 if R0 is 5
26 CMP R0, #5
27
28 // This ADDs if the zero flag = 0 (meaning the number is nonzero)
29 // R0 <- R1 + R2 if ZF = 0
30 ADDNE R0, R1, R2
31 // ^^ NE is called a "condition code"
32
33
34
35 // Condition codes are in the manual
36 // Here's an example of using them
37 // CMP to 0, if R0 is 0 then set Z = 1
38 CMP R0, #0
39 // This just adds 5 to R0
40 // But only if the Z flag is set (which CMP just did)
41 // So these two lines add 5 only if R0 is 0
42 ADDEQ R0, R0, #5
```

Reading and writing to a memory location

```

1 #include <stdio.h>
2 #include <stdint.h>
3 #include <stdlib.h>
4
5 extern void writeU32(uint32_t* p, uint32_t x);
6 extern uint32_t readU32(uint32_t* p);
7
8 int main(void) {
9     // Make an integer on the stack, we just need the mem location
10    uint32_t a;
11    printf("&a = %p\n", &a);
12    // Write this hex number at the address of a
13    writeU32(&a, 0x12345678);
14    printf("a = 0x%08x\n", readU32(&a));
15    return EXIT_SUCCESS;
16 }

```

```

1 .global writeU32
2 .global readU32
3
4 .text
5
6
7 // extern void writeU32(uint32_t* p, uint32_t x);
8 // pointer is in R0, value is in R1
9 writeU32:
10     STR R1, [R0]
11     BX LR
12
13 // extern uint32_t readU32(uint32_t* p);
14 // Pointer is in R0
15 readU32:
16     LDR R0, [R0]
17     BX LR

```

Is Even

```

1 is_even:
2     // This will bitshift with 1, leaving the result in R0
3     // This will leave a 1 in R0 if the number is odd
4     AND R0, R0, #1
5     // We use this to switch from 0-1 or vice versa
6     RSB R0, R0, #1
7     // Note: You can also use exclusive or (EOR) to flip the bit
8     //     EOR R0, R0, #1
9     BX LR

```

is Positive

```

1 // Return true (1) if positive, false (0) otherwise
2 // Becuse this accepts an unsigned number, it really just checks if
3 // the number is zero or nonzero
4 isPositiveU32:
5     // This sets the zero flag = 1 if R0 is a 0
6     // if (R0 = 0) set Z = 1
7     CMP R0, #0
8     // So now, if Z = 1, the argument R0 is 0
9     // If Z = 0, the number is not 0, therefore positive
10    // becuase it's an unsigned.
11
12    // This moves a 1 (true) into R0 only if Z clear (Z = 0)
13    MOVNE R0, #1
14    // this moves a 0 (false) into R0 only if Z set (Z = 1)
15    MOVEQ R0, #0
16    BX LR
17
18 isPositiveS32:

```

```

19  CMP R0, #0
20  // Default case is positive (return true)
21  MOV R0, #1
22  // Return 0 (false) if the number == 0
23  MOVEQ R0, #0
24  // return 0 (false) if MI is set
25  MOVMI R0, #0
26  BX LR

```

Add 64 Bit

```

1  // uint64_t addU64(uint64_t x, uint64_t y);
2  // x in R1:R0 (bits 63-32 are in R1, bits 31-0 are in R0)
3  // Same case for y, in R3, R2
4  // Results are returned in R1:R0, just how x is passed in.
5  addU64:
6      ADDS R0, R0, R2      // Add with flags
7      ADC R1, R1, R3      // Adds with carry
8      BX LR

```