# **Report Generated by Test Manager**

Title: Test HVB

**Author:** 

Date: 08-Sep-2022 12:01:12

## **Test Environment**

Platform: PCWIN64 MATLAB: (R2021b)

## Summary

Name	Outcome	Duration (Seconds)
Results: 2022-Sep-08 12:00:13	<b>Ø</b>	0.356
□ <u>HVB</u>	<b>Ø</b>	0.357
New Test Suite 1	<b>Ø</b>	0.356
New Test Case 1	<b>⊘</b>	0.356

## Results: 2022-Sep-08 12:00:13

Result Type: Result Set

Parent: None

Start Time: 08-Sep-2022 12:00:13 End Time: 08-Sep-2022 12:00:13 Outcome: Total: 1, Passed: 1

**Back to Report Summary** 

#### **HVB**

#### **Test Result Information**

Result Type: Test File Result

Parent: <u>Results: 2022-Sep-08 12:00:13</u>

Start Time: 08-Sep-2022 12:00:13 End Time: 08-Sep-2022 12:00:13 Outcome: Total: 1, Passed: 1

#### **Test Suite Information**

Name: HVB

**Back to Report Summary** 

### **New Test Suite 1**

#### **Test Result Information**

Result Type: Test Suite Result

Parent: <u>HVB</u>

Start Time: 08-Sep-2022 12:00:13 End Time: 08-Sep-2022 12:00:13 Outcome: Total: 1, Passed: 1

#### **Test Suite Information**

Name: New Test Suite 1

**Back to Report Summary** 

## **New Test Case 1**

## **Test Result Information**

Result Type: Test Case Result
Parent: New Test Suite 1
Start Time: 08-Sep-2022 12:00:13
End Time: 08-Sep-2022 12:00:13

Outcome: Passed

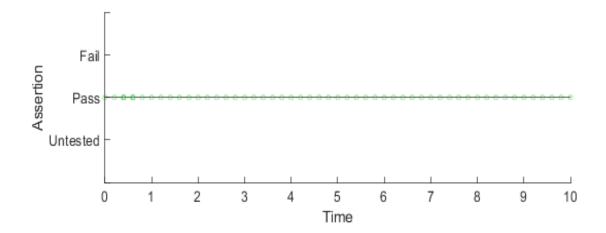
## **Test Case Information**

Name: New Test Case 1 Type: Baseline Test

## **Verify Result**

Name	Link to Plo t
Sertion Assertion	<u>Link</u>

Name			
	á		
Assertion			



## Back to Report SummaryBack to Signal Summary

## **Simulation**

## **System Under Test Information**

Model: HVB

Harness: HVB\_Harness1

Harness Owner: HVB/HVB Release: Current Simulation Mode: normal Override SIL or PIL 0

Mode:

Configuration Set: Configuration1

Start Time: 0 Stop Time: 10

Checksum: 2185157304 58078173 1171713442 1276881733

Simulink Version: 10.4 Model Version: 1.8

Model Author: whamada

Date: Tue Sep 06 17:12:00 2022

User ID: whamada

Model Path: C:\MATLAB\Reha\_Prj04\Submodules\HVB\HVB\_H

arness1.slx

Machine Name: WHAMADA-P15 Solver Name: VariableStepDiscrete

Solver Type: Variable-Step

Max Step Size: 0.20000000000000001 Simulation Start Time: 2022-09-08 12:00:13 Simulation Stop Time: 2022-09-08 12:00:13

Platform: PCWIN64

#### Test Logs:

No baseline criteria evaluation performed as no baseline data is available for this test.

**Back to Report Summary**