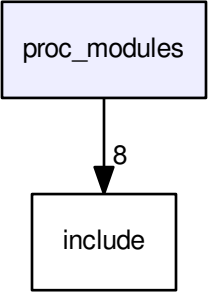


proc_modules



```
graph TD; A[proc_modules] -- 8 --> B[include];
```

8

include