

SWLED 7	Pin
1	8 Pin
2	7 Pin
3	6 Pin
4	5 Pin
5	4 Pin
6	3 Pin
7	2 Pin

The circuit diagram shows a 4-bit parallel adder. It consists of two 74181 ICs (4-bit ALUs) and one 74180 IC (4-bit majority gate). The 74181 ICs are configured to perform addition. The 74180 IC is used to generate the carry-in for the second 74181 IC. The inputs are labeled KEY1, KEY2, KEY3, and KEY4, which are connected to the ALU inputs. The output is labeled P1, P2, P3, and P4, which are connected to the ALU outputs. The carry-in is connected to the carry-in input of the second 74181 IC. The 74180 IC is configured to generate the carry-in for the second 74181 IC. The circuit is powered by a 5V supply and ground.

	P0	P1	P2	P3	P4	P5	P6	P7	P8	P9	P10	P11	P12	P13	P14	P15	P16	P17	P18	P19	P20	P21	P22	P23	P24	P25	P26	P27	P28	P29	P30	P31	P32	P33	P34	P35	P36	P37	P38	P39	P40	P41	P42	P43	P44	P45	P46	P47	P48	P49	P50	P51	P52	P53	P54	P55	P56	P57	P58	P59	P60	P61	P62	P63	P64	P65	P66	P67	P68	P69	P70	P71	P72	P73	P74	P75	P76	P77	P78	P79	P80	P81	P82	P83	P84	P85	P86	P87	P88	P89	P90	P91	P92	P93	P94	P95	P96	P97	P98	P99	P100	P101	P102	P103	P104	P105	P106	P107	P108	P109	P110	P111	P112	P113	P114	P115	P116	P117	P118	P119	P120	P121	P122	P123	P124	P125	P126	P127	P128	P129	P130	P131	P132	P133	P134	P135	P136	P137	P138	P139	P140	P141	P142	P143	P144	P145	P146	P147	P148	P149	P150	P151	P152	P153	P154	P155	P156	P157	P158	P159	P160	P161	P162	P163	P164	P165	P166	P167	P168	P169	P170	P171	P172	P173	P174	P175	P176	P177	P178	P179	P180	P181	P182	P183	P184	P185	P186	P187	P188	P189	P190	P191	P192	P193	P194	P195	P196	P197	P198	P199	P200	P201	P202	P203	P204	P205	P206	P207	P208	P209	P210	P211	P212	P213	P214	P215	P216	P217	P218	P219	P220	P221	P222	P223	P224	P225	P226	P227	P228	P229	P230	P231	P232	P233	P234	P235	P236	P237	P238	P239	P240	P241	P242	P243	P244	P245	P246	P247	P248	P249	P250	P251	P252	P253	P254	P255	P256	P257	P258	P259	P260	P261	P262	P263	P264	P265	P266	P267	P268	P269	P270	P271	P272	P273	P274	P275	P276	P277	P278	P279	P280	P281	P282	P283	P284	P285	P286	P287	P288	P289	P290	P291	P292	P293	P294	P295	P296	P297	P298	P299	P300	P301	P302	P303	P304	P305	P306	P307	P308	P309	P310	P311	P312	P313	P314	P315	P316	P317	P318	P319	P320	P321	P322	P323	P324	P325	P326	P327	P328	P329	P330	P331	P332	P333	P334	P335	P336	P337	P338	P339	P340	P341	P342	P343	P344	P345	P346	P347	P348	P349	P350	P351	P352	P353	P354	P355	P356	P357	P358	P359	P360	P361	P362	P363	P364	P365	P366	P367	P368	P369	P370	P371	P372	P373	P374	P375	P376	P377	P378	P379	P380	P381	P382	P383	P384	P385	P386	P387	P388	P389	P390	P391	P392	P393	P394	P395	P396	P397	P398	P399	P400	P401	P402	P403	P404	P405	P406	P407	P408	P409	P410	P411	P412	P413	P414	P415	P416	P417	P418	P419	P420	P421	P422	P423	P424	P425	P426	P427	P428	P429	P430	P431	P432	P433	P434	P435	P436	P437	P438	P439	P440	P441	P442	P443	P444	P445	P446	P447	P448	P449	P450	P451	P452	P453	P454	P455	P456	P457	P458	P459	P460	P461	P462	P463	P464	P465	P466	P467	P468	P469	P470	P471	P472	P473	P474	P475	P476	P477	P478	P479	P480	P481	P482	P483	P484	P485	P486	P487	P488	P489	P490	P491	P492	P493	P494	P495	P496	P497	P498	P499	P500	P501	P502	P503	P504	P505	P506	P507	P508	P509	P510	P511	P512	P513	P514	P515	P516	P517	P518	P519	P520	P521	P522	P523</
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Pin connection diagram for the JP-ISP module. The module has 10 pins. Pin 1 is MOSI, Pin 2 is VCC, Pin 3 is XTAL1, Pin 4 is GND, Pin 5 is RESET, Pin 6 is GND, Pin 7 is RESET, Pin 8 is GND, Pin 9 is SCK, and Pin 10 is MISO. The diagram shows connections to a breadboard: Pin 1 to P1.5, Pin 3 to P1.7, Pin 5 to P1.7, Pin 7 to P1.7, Pin 9 to P1.6, and Pin 10 to P1.6. A red 'X' is over the connection between Pin 3 and P1.7. A ground symbol is connected to Pin 4, Pin 6, Pin 8, and Pin 10.

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