

SRIO VIP Functional Coverage

V1.3

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Revision History

Revision	Date	By	Change
1.0	04/26/2013	MV	Initial Version
1.1	06/03/2013	MV	GEN3 related coverage points are included
1.2	07/26/2013	MV	More Coverage Points
1.3	09/20/2013	MV	More Coverage Points

Glossary

BFM	Bus Functional Model
CRF	Critical Request Flow
CS	Control Symbol
DS	Data Streaming
DUT	Design Under Test
ENV	Environment
FC	Functional Coverage
GSM	Globally Shared Memory
I/O	Input/Output
LFC	Logical Flow Control
LL	Logical Layer
PL	Physical Layer
REG	Register
SRIO	Serial RapidIO
SV	System Verilog
TL	Transport Layer
TLM	Transaction Level Model
UVM	Universal Verification Methodology
VC	Virtual Channel
VIP	Verification IP
VMIN	Minimum Valid Characters

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1 INTRODUCTION

1.1 Purpose

This document provides the description of sRIO Functional Coverage points.

1.2 Scope

The document covers the implementation details of sRIO Functional Coverage points.

1.3 Audience

This document is intended for verification engineers and testcase writers who are going to use the sRIO VIP.

1.4 References

- *“Serial RapidIO specification 1.3”*
- *“Serial RapidIO specification 2.x”*
- *“Serial RapidIO specification 3.x”*

2 Functional Coverage

SRIO VIP Functional Coverage description is provided in [Table 1](#), “List Of Functional Coverage Points,” on page 9.

Table 1: List Of Functional Coverage Points

S.No	Spec.Ref	Cover Group	Cover Points	Description
Logical Layer				
1	Part1 : Part1: 3.1	CG_LL_TX_PATH	CP_LL_TX_TXN_ID	BFM transmitting transactions with all possible transactions IDs
2	Part1: 3.1	CG_LL_RX_PATH	CP_LL_RX_TXN_ID	BFM receiving transactions with all possible transaction IDs
3	Part1 - 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_TYPES	BFM transmitting all possible types of ATOMIC transactions.
4	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_TYPES	BFM receiving all possible types of ATOMIC transactions.
5	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_FTYPE	BFM requesting transactions with all possible ftype values.
6	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_FTYPE	BFM receiving transactions with all possible ftype values.
7	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_TTYPE	BFM requesting transactions with all possible ttype values.
8	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_TTYPE	BFM receiving transactions with all possible ttype values.
9	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_TYPE2_TTYPE	BFM transmitting all possible types of type2 packets
10	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_TYPE5_TTYPE	BFM transmitting all possible types of type5 packets
11	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_NREAD_RESP_TYPE	BFM transmitting all possible response types for NREAD request
12	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_NREAD_RESP_STATUS	BFM transmitting all possible response status for NREAD request
13	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_NWRITE_RESP_TYPE	BFM transmitting all possible response types for NWRITE_R request
14	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_NWRITE_RESP_STATUS	BFM transmitting all possible response status for NWRITE_R request
15	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_TYPE8_TTYPE	BFM transmitting all possible types of type8 packets
16	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_TYPE10_TTYPE	BFM transmitting all possible types of type10 packets
17	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_TYPE11_TTYPE	BFM transmitting all possible types of type11 packets
18	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_TYPE13_TTYPE	BFM transmitting all possible types of type13 packets
19	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_TYPE2_TTYPE	BFM receiving all possible types of type2 packets

S.No	Spec.Ref	Cover Group	Cover Points	Description
20	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_TYPE5_T TYPE	BFM receiving all possible types of type5 packets
21	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_TYPE8_T TYPE	BFM receiving all possible types of type8 packets
22	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_TYPE10_ TTYPE	BFM receiving all possible types of type10 packets
23	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_TYPE11_ TTYPE	BFM receiving all possible types of type11 packets
24	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_TYPE13_ TTYPE	BFM receiving all possible types of type13 packets
25	Part1: 2.3.1	CG_LL_TX_PATH	CP_LL_TX_MAINT_P RRIORITY	BFM transmitting maintenance transactions with all possible priorities.
26	Part1: 2.3.1	CG_LL_TX_PATH	CP_LL_TX_MAINT_P RRIORITY_ORDER	Maintenance transactions may not pass other maintenance transactions of the same or higher priority. Two back to back maintenance transactions transmitted with the following flow id combination 1. High, High 2. High, Low 3. Low, High 4. Low, Low
27	Part1: 2.3.1	CG_LL_TX_PATH	CP_LL_TX_WRITE_T XN_PRIORITY	Write request transactions in a transaction request flow shall be completed at the logical layer of the destination in the same order that the transactions were delivered to the logical layer of the destination. Send back to back high and low priority write request transactions.
28	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_WDPTR	BFM requesting transactions with all possible wdptr values.
29	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_WDPTR	BFM receiving transactions with all possible wdptr values.
30	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_RDSIZE	BFM requesting transactions with all possible rdsiz values.
31	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_RDSIZE	BFM receiving transactions with all possible rdsiz values.
32	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_WRSIZE	BFM requesting transactions with all possible wrsiz values.
33	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_WRSIZE	BFM receiving transactions with all possible wrsiz values.
34	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_ADDR	BFM requesting transactions with all possible address field values.
35	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_ADDR	BFM receiving transactions with all possible address field values.
36	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_EXT_ADD R	BFM requesting transactions with all possible extended address field values.
37	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_EXT_ADD R	BFM receiving transactions with all possible extended address field values.

S.No	Spec.Ref	Cover Group	Cover Points	Description
38	Part1: 4.1.2	CG_LL_TX_PATH	CP_LL_TX_XAMSBS	BFM requesting transactions with all possible xamsbs field values.
39	Part1: 4.1.2	CG_LL_RX_PATH	CP_LL_RX_XAMSBS	BFM receiving transactions with all possible xamsbs field values.
40	Part1: 4.1.2	CG_LL_TX_PATH	CR_LL_TX_TYPE2_W DPTR_RDSIZE	cross CP_LL_TX_TYPE2_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_RDSIZE
41	Part1: 4.1.2	CG_LL_RX_PATH	CR_LL_RX_TYPE2_W DPTR_RDSIZE	cross CP_LL_RX_TYPE2_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_RDSIZE
42	Part1: 4.1.2	CG_LL_TX_PATH	CR_LL_TX_TYPE5_W DPTR_WRSIZE	cross CP_LL_TX_TYPE5_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_WRSIZE
43	Part1: 4.1.2	CG_LL_RX_PATH	CR_LL_RX_TYPE5_W DPTR_WRSIZE	cross CP_LL_RX_TYPE5_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_WRSIZE
44	Part1: 3.3.4	CG_LL_TX_PATH	CR_LL_TX_TYPE5_A TOMIC_VALID_SIZE	BFM transmitting ATOMIC transactions with data sizes 4, 2 and 1 byte cross CP_LL_TX_TYPE5_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_WRSIZE
45	Part1: 3.3.4	CG_LL_RX_PATH	CR_LL_RX_TYPE5_A TOMIC_VALID_SIZE	BFM receiving ATOMIC transactions with data sizes 4, 2 and 1 byte cross CP_LL_RX_TYPE5_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_WRSIZE
46	Part1: 3.3.4	CG_LL_TX_PATH	CR_LL_TX_TYPE2_A TOMIC_VALID_SIZE	BFM transmitting ATOMIC transactions with data sizes 4, 2 and 1 byte cross CP_LL_TX_TYPE2_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_WRSIZE
47	Part1: 3.3.4	CG_LL_RX_PATH	CR_LL_RX_TYPE2_A TOMIC_VALID_SIZE	BFM receiving ATOMIC transactions with data sizes 4, 2 and 1 byte cross CP_LL_RX_TYPE2_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_WRSIZE
48	Part1: 3.3.4	CG_LL_TX_PATH	CR_LL_TX_TYPE5_A TOMIC_INVALID_SIZ E	BFM transmitting ATOMIC transactions with data sizes 3, 5, 6, 7 and 8 cross CP_LL_TX_TYPE2_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_WRSIZE
49	Part1: 3.3.4	CG_LL_RX_PATH	CR_LL_RX_TYPE5_A TOMIC_INVALID_SIZ E	BFM receiving ATOMIC transactions with data sizes 3, 5, 6, 7 and 8 CP_LL_RX_TYPE2_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_WRSIZE
50	Part1: 3.3.4	CG_LL_TX_PATH	CR_LL_TX_TYPE2_A TOMIC_INVALID_SIZ E	BFM transmitting ATOMIC transactions with data sizes 3, 5, 6, 7 and 8 cross CP_LL_TX_TYPE2_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_WRSIZE
51	Part1: 3.3.4	CG_LL_RX_PATH	CR_LL_RX_TYPE2_A TOMIC_INVALID_SIZ E	BFM receiving ATOMIC transactions with data sizes 3, 5, 6, 7 and 8 CP_LL_RX_TYPE2_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_WRSIZE

S.No	Spec.Ref	Cover Group	Cover Points	Description
52	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_INC_RESP_TYPE	BFM transmitting all possible types of response packets for ATOMIC INC request
53	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_INC_RESP_STATUS	BFM transmitting all possible response status for ATOMIC INC request
54	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_DEC_RESP_TYPE	BFM transmitting all possible types of response packets for ATOMIC DEC request
55	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_DEC_RESP_STATUS	BFM transmitting all possible response status for ATOMIC DEC request
56	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_SET_RESP_TYPE	BFM transmitting all possible types of response packets for ATOMIC SET request
57	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_SET_RESP_STATUS	BFM transmitting all possible response status for ATOMIC SET request
58	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_CLR_RESP_TYPE	BFM transmitting all possible types of response packets for ATOMIC CLR request
59	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_CLR_RESP_STATUS	BFM transmitting all possible response status for ATOMIC CLR request
60	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_SWAP_RESP_TYPE	BFM transmitting all possible types of response packets for ATOMIC SWAP request
61	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_SWAP_RESP_STATUS	BFM transmitting all possible response status for ATOMIC SWAP request
62	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_COMP_RESP_TYPE	BFM transmitting all possible types of response packets for ATOMIC COMP request
63	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_COMP_RESP_STATUS	BFM transmitting all possible response status for ATOMIC COMP request
64	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_TEST_RESP_TYPE	BFM transmitting all possible types of response packets for ATOMIC TEST request
65	Part1: 3.3.4	CG_LL_TX_PATH	CP_LL_TX_ATOMIC_TEST_RESP_STATUS	BFM transmitting all possible response status for ATOMIC TEST request
66	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_INC_RESP_TYPE	BFM receiving all possible types of response packets for ATOMIC INC request
67	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_INC_RESP_STATUS	BFM receiving all possible response status for ATOMIC INC request
68	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_DEC_RESP_TYPE	BFM receiving all possible types of response packets for ATOMIC DEC request
69	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_DEC_RESP_STATUS	BFM receiving all possible response status for ATOMIC DEC request
70	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_SET_RESP_TYPE	BFM receiving all possible types of response packets for ATOMIC SET request
71	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_SET_RESP_STATUS	BFM receiving all possible response status for ATOMIC SET request
72	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_CLR_RESP_TYPE	BFM receiving all possible types of response packets for ATOMIC CLR request
73	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_CLR_RESP_STATUS	BFM receiving all possible response status for ATOMIC CLR request
74	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_SWAP_RESP_TYPE	BFM receiving all possible types of response packets for ATOMIC SWAP request

S.No	Spec.Ref	Cover Group	Cover Points	Description
75	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_SWAP_RESP_STATU S	BFM receiving all possible response status for ATOMIC SWAP request
76	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_COMP_RESP_TYPE	BFM receiving all possible types of response packets for ATOMIC COMP request
77	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_COMP_RESP_STATU S	BFM receiving all possible response status for ATOMIC COMP request
78	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_TEST_RESP_TYPE	BFM receiving all possible types of response packets for ATOMIC TEST request
79	Part1: 3.3.4	CG_LL_RX_PATH	CP_LL_RX_ATOMIC_TEST_RESP_STATUS	BFM receiving all possible response status for ATOMIC TEST request
80	Part1: 4.1.2	CG_LL_TX_PATH	CR_LL_TX_FTYPE_XAMSBS_SRCTID	cross CP_LL_TX_FTYPE, CP_LL_TX_XAMSBS, CP_LL_TX_SRCTID
81	Part1: 4.1.2	CG_LL_RX_PATH	CR_LL_RX_FTYPE_XAMSBS_SRCTID	cross CP_LL_RX_FTYPE, CP_LL_RX_XAMSBS, CP_LL_RX_SRCTID
82	Part1: 4.1.7	CG_LL_TX_PATH	CP_LL_TX_NWRITE_INVALID_PAYLOAD_L EN	BFM transmitting NWRITE and NWRITE_R transactions with payload size greater or lesser than wdptr,wrsiz specified in the packet header
83	Part1: 4.1.10	CG_LL_TX_PATH	CP_LL_TX_MAINT_WRITE_INVALID_PAYL OAD_LEN	BFM transmitting Maintenance write transactions with payload size greater or lesser than wdptr,wrsiz specified in the packet header
84	Part1: 4.1.2	CG_LL_TX_PATH	CR_LL_TX_MAINT_RD_WDPTR_RDSIZE	cross CP_LL_TX_TYPE8_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_RDSIZE
85	Part1: 4.1.2	CG_LL_RX_PATH	CR_LL_RX_MAINT_RD_WDPTR_RDSIZE	cross CP_LL_RX_TYPE8_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_RDSIZE
86	Part1: 4.1.10	CG_LL_TX_PATH	CR_LL_TX_MAINT_WR_WDPTR_WRSIZE	cross CP_LL_TX_TYPE8_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_WRSIZE
87	Part1: 4.1.10	CG_LL_RX_PATH	CR_LL_RX_MAINT_WR_WDPTR_WRSIZE	cross CP_LL_RX_TYPE8_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_WRSIZE
88	Part1: 4.1.10	CG_LL_TX_PATH	CP_LL_TX_MAINT_CONFIG_OFFSET	BFM transmitting maintenance transactions with all possible values for config offset sub-field
89	Part1: 4.1.10	CG_LL_TX_PATH	CP_LL_TX_MAINT_SRCTID	BFM transmitting maintenance transactions with all possible values for srcTID sub-field
90	Part1: 4.1.10	CG_LL_TX_PATH	CP_LL_TX_MAINT_TARGET_TID	BFM transmitting maintenance transactions with all possible values for targetTID sub-field
91	Part1: 4.1.10	CG_LL_RX_PATH	CP_LL_TX_MAINT_STATUS	BFM transmitting maintenance transactions with all possible values for status sub-field 'b0000 - DONE response 'b0111 - ERROR response
92	Part1: 4.1.10	CG_LL_TX_PATH	CP_LL_RX_MAINT_STATUS	BFM receiving maintenance transactions with all possible values for status sub-field 'b0000 - DONE response 'b0111 - ERROR response
93	Part1: 4.1.10	CG_LL_RX_PATH	CP_LL_RX_MAINT_CONFIG_OFFSET	BFM receiving maintenance transactions with all possible values for config offset sub-field
94	Part1: 4.1.10	CG_LL_RX_PATH	CP_LL_RX_MAINT_SRCTID	BFM receiving maintenance transactions with all possible values for srcTID sub-field

S.No	Spec.Ref	Cover Group	Cover Points	Description
95	Part1: 4.1.10	CG_LL_RX_PATH	CP_LL_RX_MAINT_TARGET_TID	BFM receiving maintenance transactions with all possible values for targetTID sub-field
96	Part1: 4.2.1	CG_LL_TX_PATH	CP_LL_TX_RESP_TXN	BFM transmitting all possible values for 4-bit transaction sub-field in response packet 'b0000 - Response with no data payload 'b1000 - Response with data payload
97	Part1: 4.2.1	CG_LL_TX_PATH	CP_LL_TX_RESP_TARGET_TID	BFM transmitting all possible values for target tid sub-field in response packet.
98	Part1: 4.2.1	CG_LL_TX_PATH	CP_LL_TX_IO_RESP_STATUS	BFM transmitting all possible values for 4-bit status sub-field in response packet. 'b0000 - DONE response 'b0111 - ERROR response
99	Part1: 4.2.1	CG_LL_RX_PATH	CP_LL_RX_RESP_TXN	BFM receiving all possible values for 4-bit transaction sub-field in response packet 'b0000 - Response with no data payload 'b1000 - Response with data payload
100	Part1: 4.2.1	CG_LL_RX_PATH	CP_LL_RX_RESP_TARGET_TID	BFM receiving all possible values for target tid sub-field in response packet.
101	Part1: 4.2.1	CG_LL_RX_PATH	CP_LL_RX_IO_RESP_STATUS	BFM receiving all possible values for 4-bit status sub-field in response packet. 'b0000 - DONE response 'b0111 - ERROR response
102	Part2: 2.3.1	CG_LL_TX_PATH	CP_LL_TX_MSG_MSGLEN	BFM transmitting messages with length ranging from 0 to 15.
103	Part2: 2.3.1	CG_LL_TX_PATH	CP_LL_TX_MSG_MSGSEG	BFM transmitting messages with all possible values for message segment field which ranges from 0 to 15.
104	Part2: 2.3.1	CG_LL_TX_PATH	CP_LL_TX_MSG_XMBOX	BFM transmitting messages with all possible values for mbox sub-field.
105	Part2: 2.3.1	CG_LL_TX_PATH	CP_LL_TX_MSG_SSIZE	BFM transmitting messages with all possible values for segment size sub-field.
106	Part2: 2.3.1	CG_LL_TX_PATH	CP_LL_TX_MSG_MBOX	BFM transmitting messages with all possible values for mbox sub-field.
107	Part2: 2.3.1	CG_LL_TX_PATH	CP_LL_TX_MSG_LETTER	BFM transmitting messages with all possible values for letter sub-field.
108	Part2: 2.3.1	CG_LL_TX_PATH	CR_LL_TX_MSG_SINGLE_XMBOX_MBOX_LETTER	cross CP_LL_TX_MSG_XMBOX, CP_LL_TX_MSG_MBOX, CP_LL_TX_MSG_LETTER
109	Part2: 2.3.1	CG_LL_TX_PATH	CR_LL_TX_MSG_MULTI_SEG_SSIZE_MBOX_LETTER	cross CP_LL_TX_MSG_MSGLEN, CP_LL_TX_MSG_SSIZE, CP_LL_TX_MSG_MBOX, CP_LL_TX_MSG_LETTER
110	Part2: 2.3.1	CG_LL_RX_PATH	CP_LL_RX_MSG_MSGLEN	BFM receiving messages with length ranging from 0 to 15.
111	Part2: 2.3.1	CG_LL_RX_PATH	CP_LL_RX_MSG_MSGSEG	BFM receiving messages with all possible values for message segment field which ranges from 0 to 15.
112	Part2: 2.3.1	CG_LL_RX_PATH	CP_LL_RX_MSG_MSGSEG	BFM receiving messages with all possible values for message segment field which ranges from 0 to 15.

S.No	Spec.Ref	Cover Group	Cover Points	Description
113	Part2: 2.3.1	CG_LL_RX_PATH	CP_LL_RX_MSG_XMBOX	BFM receiving messages with all possible values for xmbox sub-field.
114	Part2: 2.3.1	CG_LL_RX_PATH	CP_LL_RX_MSG_SSIZE	BFM receiving messages with all possible values for segment size sub-field.
115	Part2: 2.3.1	CG_LL_RX_PATH	CP_LL_RX_MSG_MBOX	BFM receiving messages with all possible values for mbox sub-field.
116	Part2: 2.3.1	CG_LL_RX_PATH	CP_LL_RX_MSG_LETTER	BFM receiving messages with all possible values for letter sub-field.
117	Part2: 2.3.1	CG_LL_RX_PATH	CR_LL_RX_MSG_SINGL_XMBOX_MBOX_LETTER	cross CP_LL_RX_MSG_XMBOX, CP_LL_RX_MSG_MBOX, CP_LL_RX_MSG_LETTER
118	Part2: 2.3.1	CG_LL_RX_PATH	CR_LL_RX_MSG_MULTI_SEG_SSIZE_MBOX_LETTER	cross CP_LL_RX_MSG_MSGLEN, CP_LL_RX_MSG_SSIZE, CP_LL_RX_MSG_MBOX, CP_LL_RX_MSG_LETTER
119	Part2 - 2.4.2	CG_LL_TX_PATH	CP_LL_TX_MSG_OUT_ORDER	BFM transmitting out of order messages.
120	Part2 - 2.4.2	CG_LL_RX_PATH	CP_LL_RX_MSG_OUT_ORDER	BFM receiving out of order messages.
121	Part2 - 2.4.2	CG_LL_TX_PATH	CP_LL_TX_MSG_INTERLEAVE	BFM transmitting interleaved messages.
122	Part2 - 2.4.2	CG_LL_RX_PATH	CP_LL_RX_MSG_INTERLEAVE	BFM receiving interleaved messages.
123	Part2 - 2.4.2	CG_LL_TX_PATH	CP_LL_TX_DOORBELL_RESP_TYPE	BFM transmitting responses with all possible values for type sub-field in doorbell response packet
124	Part2 - 2.4.2	CG_LL_TX_PATH	CP_LL_TX_DOORBELL_RESP_STATUS	BFM transmitting responses with all possible values for status sub-field in doorbell response packet
125	Part2 - 2.4.2	CG_LL_RX_PATH	CP_LL_RX_DOORBELL_RESP_TYPE	BFM receiving responses with all possible values for type sub-field in doorbell response packet
126	Part2 - 2.4.2	CG_LL_RX_PATH	CP_LL_RX_DOORBELL_RESP_STATUS	BFM receiving responses with all possible values for status sub-field in doorbell response packet
127	Part2 - 2.4.2	CG_LL_TX_PATH	CP_LL_TX_MSG_RESPONSE_TYPE	BFM transmitting responses with all possible values for type sub-field in message response packet
128	Part2 - 2.4.2	CG_LL_TX_PATH	CP_LL_TX_MSG_RESPONSE_STATUS	BFM transmitting responses with all possible values for status sub-field in message response packet
129	Part2 - 2.4.2	CG_LL_RX_PATH	CP_LL_RX_MSG_RESPONSE_TYPE	BFM receiving responses with all possible values for type sub-field in message response packet
130	Part2 - 2.4.2	CG_LL_RX_PATH	CP_LL_RX_MSG_RESPONSE_STATUS	BFM receiving responses with all possible values for status sub-field in message response packet
131	Part2 - 2.4.2	CG_LL_TX_PATH	CP_LL_TX_MSG_RESPONSE_TARGET_TID	BFM transmitting responses with all possible values for target tid sub-field in message response packet
132	Part2 - 2.4.2	CG_LL_RX_PATH	CP_LL_RX_MSG_RESPONSE_TARGET_TID	BFM receiving responses with all possible values for target tid sub-field in message response packet

S.No	Spec.Ref	Cover Group	Cover Points	Description
133	Part2 - 2.4.2	CG_LL_TX_PATH	CR_LL_TX_MSG_RESP_STATUS_TARGET_TID	cross CP_LL_TX_MSG_RESP_STATUS, CP_LL_TX_MSG_RESP_TARGET_TID
134	Part2 - 2.4.2	CG_LL_RX_PATH	CR_LL_RX_MSG_RESP_STATUS_TARGET_TID	cross CP_LL_RX_MSG_RESP_STATUS, CP_LL_RX_MSG_RESP_TARGET_TID
135	Part2 - A2	CG_LL_TX_PATH	CP_LL_TX_MSG_PRIORITY	BFM transmitting message transactions with different priorities
136	Part2 - A2	CG_LL_RX_PATH	CP_LL_RX_MSG_PRIORITY	BFM receiving message transactions with different priorities
137	Part3 - 2.4	CG_LL_TX_PATH	CP_TL_TX_TT_VALID	BFM transmitting packets with all possible valid values for tt field.
138	Part3 - 2.4	CG_LL_RX_PATH	CP_TL_RX_TT_VALID	BFM receiving packets with all possible valid values for tt field.
139	Part3 - 2.4	CG_LL_TX_PATH	CR_TX_TT_FTYPE	cross CP_LL_TX_FTYPE, CP_TL_TX_TT_VALID
140	Part3 - 2.4	CG_LL_TX_PATH	CR_TX_TT_TTYPE	cross CP_LL_TX_TTYPE, CP_TL_TX_TT_VALID
141	Part3 - 2.4	CG_LL_TX_PATH	CR_TX_TT_TYPE2_TTYPE	cross CP_TL_TX_TT_VALID, CP_LL_TX_TYPE2_TTYPE
142	Part3 - 2.4	CG_LL_TX_PATH	CR_TX_TT_TYPE5_TTYPE	cross CP_TL_TX_TT_VALID, CP_LL_TX_TYPE5_TTYPE
143	Part3 - 2.4	CG_LL_TX_PATH	CR_TX_TT_TYPE8_TTYPE	cross CP_TL_TX_TT_VALID, CP_LL_TX_TYPE8_TTYPE
144	Part3 - 2.4	CG_LL_TX_PATH	CR_TX_TT_TYPE10_TTYPE	cross CP_TL_TX_TT_VALID, CP_LL_TX_TYPE10_TTYPE
145	Part3 - 2.4	CG_LL_TX_PATH	CR_TX_TT_TYPE11_TTYPE	cross CP_TL_TX_TT_VALID, CP_LL_TX_TYPE11_TTYPE
146	Part3 - 2.4	CG_LL_TX_PATH	CR_TX_TT_TYPE13_TTYPE	cross CP_TL_TX_TT_VALID, CP_LL_TX_TYPE13_TTYPE
147	Part3 - 2.4	CG_LL_RX_PATH	CR_RX_TT_FTYPE	cross CP_LL_RX_FTYPE, CP_TL_RX_TT_VALID
148	Part3 - 2.4	CG_LL_RX_PATH	CR_RX_TT_TTYPE	cross CP_LL_RX_TTYPE, CP_TL_RX_TT_VALID
149	Part3 - 2.4	CG_LL_RX_PATH	CR_RX_TT_TYPE2_TTYPE	cross CP_TL_RX_TT_VALID, CP_LL_RX_TYPE2_TTYPE
150	Part3 - 2.4	CG_LL_RX_PATH	CR_RX_TT_TYPE5_TTYPE	cross CP_TL_RX_TT_VALID, CP_LL_RX_TYPE5_TTYPE
151	Part3 - 2.4	CG_LL_RX_PATH	CR_RX_TT_TYPE8_TTYPE	cross CP_TL_RX_TT_VALID, CP_LL_RX_TYPE8_TTYPE
152	Part3 - 2.4	CG_LL_RX_PATH	CR_RX_TT_TYPE10_TTYPE	cross CP_TL_RX_TT_VALID, CP_LL_RX_TYPE10_TTYPE
153	Part3 - 2.4	CG_LL_RX_PATH	CR_RX_TT_TYPE11_TTYPE	cross CP_TL_RX_TT_VALID, CP_LL_RX_TYPE11_TTYPE
154	Part3 - 2.4	CG_LL_RX_PATH	CR_RX_TT_TYPE13_TTYPE	cross CP_TL_RX_TT_VALID, CP_LL_RX_TYPE13_TTYPE
155	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_REQ_TTYPE	BFM transmitting GSM packets with all possible values for ttype sub-field.
156	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_REQ_TTYPE	BFM receiving GSM packets with all possible values for ttype sub-field.
157	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_REQ_SRCTID	BFM transmitting GSM packets with all possible values for srcTID sub-field.

S.No	Spec.Ref	Cover Group	Cover Points	Description
158	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_REQ_SRCTID	BFM receiving GSM packets with all possible values for srcTID sub-field.
159	Part5 - 3.1	CG_LL_TX_PATH	CR_LL_TX_GSM_REQ_SRCTID	cross CP_LL_TX_GSM_REQ_TTYPE, CP_LL_TX_GSM_REQ_SRCTID
160	Part5 - 3.1	CG_LL_RX_PATH	CR_LL_RX_GSM_REQ_SRCTID	cross CP_LL_RX_GSM_REQ_TTYPE, CP_LL_RX_GSM_REQ_SRCTID
161	Part5 - 3.1	CG_LL_TX_PATH	CR_LL_TX_GSM_REQ_WDPTR_RDSIZE	cross CP_LL_TX_GSM_REQ_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_RDSIZE
162	Part5 - 3.1	CG_LL_RX_PATH	CR_LL_RX_GSM_REQ_WDPTR_RDSIZE	cross CP_LL_RX_GSM_REQ_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_RDSIZE
163	Part5 - 3.1	CG_LL_TX_PATH	CR_LL_TX_GSM_REQ_TTYPE_WDPTR_WRSIZE	cross CP_LL_TX_GSM_REQ_TTYPE, CP_LL_TX_WDPTR, CP_LL_TX_WRSIZE
164	Part5 - 3.1	CG_LL_RX_PATH	CR_LL_RX_GSM_REQ_TTYPE_WDPTR_WRSIZE	cross CP_LL_RX_GSM_REQ_TTYPE, CP_LL_RX_WDPTR, CP_LL_RX_WRSIZE
165	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_RD_O_RESP_TYPE	BFM transmitting all possible response types for GSM Read Owner request
166	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_RD_O_RESP_STATUS	BFM transmitting all possible response status for GSM Read Owner request
167	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_RD_O_O_RESP_TYPE	BFM transmitting all possible response types for GSM Read To Own Owner request
168	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_RD_O_O_RESP_STATUS	BFM transmitting all possible response status for GSM Read To Own Owner request
169	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_IO_RD_O_RESP_TYPE	BFM transmitting all possible response types for GSM IO Read Owner request
170	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_IO_RD_O_RESP_STATUS	BFM transmitting all possible response status for GSM IO Read Owner request
171	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_RD_H_RESP_TYPE	BFM transmitting all possible response types for GSM Read Home request
172	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_RD_H_RESP_STATUS	BFM transmitting all possible response status for GSM Read Home request
173	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_RD_O_H_RESP_TYPE	BFM transmitting all possible response types for GSM Read To Own Home request
174	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_RD_O_H_RESP_STATUS	BFM transmitting all possible response status for GSM Read To Own Home request
175	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_IO_RD_H_RESP_TYPE	BFM transmitting all possible response types for GSM IO Read Home request
176	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_IO_RD_H_RESP_STATUS	BFM transmitting all possible response status for GSM IO Read Home request
177	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_D_H_RESP_TYPE	BFM transmitting all possible response types for GSM DKILL Home request
178	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_D_H_RESP_STATUS	BFM transmitting all possible response status for GSM DKILL Home request
179	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_I_H_RESP_TYPE	BFM transmitting all possible response types for GSM IKILL Home request

S.No	Spec.Ref	Cover Group	Cover Points	Description
180	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_IH_RESP_STATUS	BFM transmitting all possible response status for GSM IKILL Home request
181	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_TLBIE_RESP_TYPE	BFM transmitting all possible response types for GSM TLBIE request
182	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_TLBIE_RESP_STATUS	BFM transmitting all possible response status for GSM TLBIE request
183	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_TLBSYNC_RESP_TYPE	BFM transmitting all possible response types for GSM TLBSYNC request
184	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_TLBSYNC_RESP_STATUS	BFM transmitting all possible response status for GSM TLBSYNC request
185	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_IRD_H_RESP_TYPE	BFM transmitting all possible response types for GSM IREAD Home request
186	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_IRD_H_RESP_STATUS	BFM transmitting all possible response status for GSM IREAD Home request
187	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_FLUSH_WO_DATA_RESP_TYPE	BFM transmitting all possible response types for GSM FLUSH Without Data request
188	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_FLUSH_WO_DATA_RESP_STATUS	BFM transmitting all possible response status for GSM FLUSH Without Data request
189	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_IKILL_SHARER_RESP_TYPE	BFM transmitting all possible response types for GSM IKILL_SHARER request
190	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_IKILL_SHARER_RESP_STATUS	BFM transmitting all possible response status for GSM IKILL_SHARER request
191	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_DKILL_SHARER_RESP_TYPE	BFM transmitting all possible response types for GSM DKILL_SHARER request
192	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_DKILL_SHARER_RESP_STATUS	BFM transmitting all possible response status for GSM DKILL_SHARER request
193	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_CASTOUT_RESP_TYPE	BFM transmitting all possible response types for GSM CASTOUT request
194	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_CASTOUT_RESP_STATUS	BFM transmitting all possible response status for GSM CASTOUT request
195	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_FLUSH_WD_RESP_TYPE	BFM transmitting all possible response types for GSM FLUSH WD request
196	Part5 - 3.1	CG_LL_TX_PATH	CP_LL_TX_GSM_FLUSH_WD_RESP_STATUS	BFM transmitting all possible response status for GSM FLUSH WD request
197	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_READ_OWNER_RESP_TYPE	BFM receiving all possible response types for GSM Read Owner request
198	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_READ_OWNER_RESP_STATUS	BFM receiving all possible response status for GSM Read Owner request
199	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_READ_TO_OWN_OWNER_RESP_TYPE	BFM receiving all possible response types for GSM Read To Own Owner request
200	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_READ_TO_OWN_OWNER_RESP_STATUS	BFM receiving all possible response status for GSM Read To Own Owner request
201	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_IO_READ_OWNER_RESP_TYPE	BFM receiving all possible response types for GSM IORead Owner request

S.No	Spec.Ref	Cover Group	Cover Points	Description
202	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_IO_RD_O_RESP_STATU S	BFM receiving all possible response status for GSM IORead Owner request
203	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_RD_H_RESP_TYPE	BFM receiving all possible response types for GSM Read Home request
204	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_RD_H_RESP_STATUS	BFM receiving all possible response status for GSM Read Home request
205	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_RD_O_H_RESP_TYPE	BFM receiving all possible response types for GSM Read To Own Home request
206	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_RD_O_H_RESP_STATUS	BFM receiving all possible response status for GSM Read To Own Home request
207	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_IO_RD_H_RESP_TYPE	BFM receiving all possible response types for GSM IORead Home request
208	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_IO_RD_H_RESP_STATU S	BFM receiving all possible response status for GSM IORead Home request
209	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_D_H_RESP_TYPE	BFM receiving all possible response types for GSM DKILL Home request
210	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_D_H_RESP_STATUS	BFM receiving all possible response status for GSM DKILL Home request
211	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_I_H_RESP_TYPE	BFM receiving all possible response types for GSM IKILL Home request
212	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_I_H_RESP_STATUS	BFM receiving all possible response status for GSM IKILL Home request
213	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_TLBIE_RESP_TYPE	BFM receiving all possible response types for GSM TLBIE request
214	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_TLBIE_RESP_STATUS	BFM receiving all possible response status for GSM TLBIE request
215	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_TLBSYNC_RESP_TYPE	BFM receiving all possible response types for GSM TLBSYNC request
216	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_TLBSYNC_RESP_STATU S	BFM receiving all possible response status for GSM TLBSYNC request
217	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_IRD_H_RESP_TYPE	BFM receiving all possible response types for GSM IREAD Home request
218	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_IRD_H_RESP_STATUS	BFM receiving all possible response status for GSM IREAD Home request
219	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_FLUSH_WO_D_RESP_TY PE	BFM receiving all possible response types for GSM FLUSH Without Data request
220	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_FLUSH_WO_D_RESP_ST ATUS	BFM receiving all possible response status for GSM FLUSH Without Data request
221	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_IKILL_SH_RESP_TYPE	BFM receiving all possible response types for GSM IKILL_SHARER request
222	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_IKILL_SH_RESP_STATUS	BFM receiving all possible response status for GSM IKILL_SHARER request
223	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_DKILL_SH_RESP_TYPE	BFM receiving all possible response types for GSM DKILL_SHARER request

S.No	Spec.Ref	Cover Group	Cover Points	Description
224	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_DK_SH_RESP_STATUS	BFM receiving all possible response status for GSM DKILL_SHARER request
225	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_CASTOUT_RESP_TYPE	BFM receiving all possible response types for GSM CASTOUT request
226	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_CASTOUT_RESP_STATUSES	BFM receiving all possible response status for GSM CASTOUT request
227	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_FLUSH_WD_RESP_TYPE	BFM receiving all possible response types for GSM FLUSH WD request
228	Part5 - 3.1	CG_LL_RX_PATH	CP_LL_RX_GSM_FLUSH_WD_RESP_STATUSES	BFM receiving all possible response status for GSM FLUSH WD request
229	Part5 - 4.2.1	CG_LL_TX_PATH	CP_LL_TX_GSM_REQ_DATA_PAYLOAD	BFM transmitting GSM requests with aligned and un-aligned data payload lengths.
230	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_IO_ERROR_RESP	BFM transmitting ERROR response for a IO logical layer request.
231	Part8 - 2.3.2.2	CG_LL_RX_PATH	CP_LL_RX_IO_ERROR_RESP	BFM receiving ERROR response for a IO logical layer request.
232	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_MSG_ERROR_RESP	BFM transmitting ERROR response for a MSG logical layer request.
233	Part8 - 2.3.2.2	CG_LL_RX_PATH	CP_LL_RX_MSG_ERROR_RESP	BFM receiving ERROR response for a MSG logical layer request.
234	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_GSM_ERROR_RESP	BFM transmitting ERROR response for a GSM logical layer request.
235	Part8 - 2.3.2.2	CG_LL_RX_PATH	CP_LL_RX_GSM_ERROR_RESP	BFM receiving ERROR response for a GSM logical layer request.
236	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_MSG_FORMAT_ERR_INVALID_SIZE	BFM transmitting message packet data payload with an invalid size
237	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_MSG_FORMAT_ERR_INVALID_SEG	BFM transmitting message packet data payload with an invalid segment
238	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_MSG_FORMAT_ERR_INVALID_SIZE_SEGMENT	BFM transmitting message packet data payload with an invalid size and invalid segment
239	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_IO_ILLEGAL_TRANS_DEC	BFM transmitting IO request packet with undefined field values
240	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_IO_RESP_ILLEGAL_TRANS_DEC	BFM transmitting IO response packet with undefined field values
241	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_MSG_ILLEGAL_TRANS_DEC	BFM transmitting MSG request packet with undefined field values
242	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_MSG_RESP_ILLEGAL_TRANS_DEC	BFM transmitting MSG response packet with undefined field values

S.No	Spec.Ref	Cover Group	Cover Points	Description
243	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_GSM_ILLEGAL_TRANS_DEC	BFM transmitting GSM request packet with undefined field values
244	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_GSM_RESP_ILLEGAL_TRANS_DEC	BFM transmitting GSM response packet with undefined field values
245	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_ILLEGAL_TARGET	BFM transmitting a packet with illegal destination ID
246	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_IO_ILLEGAL_TRANS_TARGET	BFM transmitting a IO packet with illegal destination ID
247	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_GSM_ILLEGAL_TRANS_TARGET	BFM transmitting a GSM packet with illegal destination ID
248	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_MSG_REQUEST_TIMEOUT	BFM forcing the endpoint to create message request timeout condition.
249	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_RESPONSE_TIMEOUT	BFM blocking the IO/GSM/MSG response packet to create packet response timeout condition in endpoint.
250	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_UNEXPECTED_IO_RESP	BFM transmitting unexpected/unwanted IO response packet.
251	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_UNEXPECTED_MAINT_RESP	BFM transmitting unexpected/unwanted maintenance response packet.
252	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_UNEXPECTED_MSG_RESP	BFM transmitting unexpected/unwanted MSG response packet.
253	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_UNSUPPORTED_IO_TXN	BFM transmitting unsupported IO transaction
254	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_UNSUPPORTED_MSG_TXN	BFM transmitting unsupported MSG transaction
255	Part8 - 2.3.2.2	CG_LL_TX_PATH	CP_LL_TX_UNSUPPORTED_GSM_TXN	BFM transmitting unsupported GSM transaction
256	Part8 - 1.3	CG_LL_TX_PATH	CP_LL_TX_CONSECUTIVE_IO_ERROR_RESPONSE	BFM transmitting consecutive IO error responses.
257	Part8 - 1.3	CG_LL_TX_PATH	CP_LL_TX_CONSECUTIVE_MSG_ERROR_RESPONSE	BFM transmitting consecutive MSG error responses.
258	Part8 - 1.3	CG_LL_TX_PATH	CP_LL_TX_IO_GOOD_ERROR_RESPONSE	BFM transmitting consecutive DONE, ERROR, DONE, ERROR IO error responses.
259	Part8 - 1.3	CG_LL_TX_PATH	CP_LL_TX_MSG_GOOD_ERROR_RESPONSE	BFM transmitting consecutive DONE, ERROR, DONE, ERROR MSG error responses.
260	Part8 - 1.3	CG_LL_TX_PATH	CP_LL_TX_BACK2BACK_MSG_INVALID_SIZE	BFM transmitting back 2 back message requests with invalid size
261	Part8 - 1.3	CG_LL_TX_PATH	CP_LL_TX_BACK2BACK_MSG_INVALID_SEGMENT	BFM transmitting back 2 back message requests with invalid segment
262	Part8 - 1.3	CG_LL_TX_PATH	CR_LL_TX_BACK2BACK_MSG_INVALID_SIZE_SEGMENT	cross CP_LL_TX_BACK2BACK_MSG_INVALID_SIZE, CP_LL_TX_BACK2BACK_MSG_INVALID_SEGMENT

S.No	Spec.Ref	Cover Group	Cover Points	Description
263	Part8 - 1.3	CG_LL_TX_PATH	CP_LL_TX_CONSECUTIVE_MSG_VALID_INVALID_SIZE	BFM transmitting consecutive message requests with valid, invalid, valid followed by invalid data payload size
264	Part8 - 1.3	CG_LL_TX_PATH	CP_LL_TX_CONSECUTIVE_MSG_VALID_INVALID_SEGMENT	BFM transmitting consecutive message requests with valid, invalid, valid followed by invalid segment
265	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_PDU_LENGTH	BFM transmitting data streaming packets with all possible values for PDU length sub-field
266	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_PDU_LENGTH	BFM receiving data streaming packets with all possible values for PDU length sub-field
267	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_PDU_COS	BFM transmitting data streaming packets with all possible values for Class Of Service (COS) sub-field
268	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_PDU_COS	BFM receiving data streaming packets with all possible values for Class Of Service (COS) sub-field
269	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_PDU_MTU	BFM transmitting data streaming packets with all possible values for MTU size
270	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_PDU_MTU	BFM receiving data streaming packets with all possible values for MTU size
271	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_PDU_LENGTH_MTU	cross CP_LL_TX_PDU_LENGTH, CP_LL_TX_PDU_MTU
272	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_PDU_LENGTH_MTU	cross CP_LL_RX_PDU_LENGTH, CP_LL_RX_PDU_MTU
273	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_PDU_S	BFM transmitting data streaming packets with all possible values of S field.
274	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_PDU_S	BFM receiving data streaming packets with all possible values of S field.
275	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_PDU_E	BFM transmitting data streaming packets with all possible values of E field.
276	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_PDU_E	BFM receiving data streaming packets with all possible values of E field.
277	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_PDU_O	BFM transmitting data streaming packets with all possible value for O field
278	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_PDU_O	BFM receiving data streaming packets with all possible value for O field
279	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_PDU_P	BFM transmitting data streaming packets with all possible value for P field
280	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_PDU_P	BFM receiving data streaming packets with all possible value for P field
281	Part10 - 3.2.5	CG_LL_TX_PATH	CP_LL_TX_PDU_XH	BFM transmitting data stream packets with all possible values for extended header field.
282	Part10 - 3.2.5	CG_LL_RX_PATH	CP_LL_RX_PDU_XH	BFM receiving data stream packets with all possible values for extended header field.
283	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_PDU_STREAM_ID	BFM transmitting data streaming packets with all possible values for stream id field
284	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_PDU_STREAM_ID	BFM receiving data streaming packets with all possible values for stream id field

S.No	Spec.Ref	Cover Group	Cover Points	Description
285	Part10 - 1.31	CG_LL_TX_PATH	CR_LL_TX_PDU_SIN GLE_SEGMENT	cross CP_LL_TX_PDU_O, CP_LL_TX_PDU_P, CP_LL_TX_PDU_S, CP_LL_TX_PDU_E
286	Part10 - 1.31	CG_LL_RX_PATH	CR_LL_RX_PDU_SIN GLE_SEGMENT	cross CP_LL_RX_PDU_O, CP_LL_RX_PDU_P, CP_LL_RX_PDU_S, CP_LL_RX_PDU_E
287	Part10 - 1.31	CG_LL_TX_PATH	CR_LL_TX_PDU_STA RT_SEGMENT	cross CP_LL_TX_PDU_S, CP_LL_TX_PDU_E, CP_LL_TX_PDU_MTU
288	Part10 - 1.31	CG_LL_RX_PATH	CR_LL_RX_PDU_STA RT_SEGMENT	cross CP_LL_RX_PDU_S, CP_LL_RX_PDU_E, CP_LL_RX_PDU_MTU
289	Part10 - 1.31	CG_LL_TX_PATH	CR_LL_TX_PDU_MID DLE_SEGMENT	cross CP_LL_TX_PDU_S, CP_LL_TX_PDU_E, CP_LL_TX_PDU_MTU
290	Part10 - 1.31	CG_LL_RX_PATH	CR_LL_RX_PDU_MID DLE_SEGMENT	cross CP_LL_RX_PDU_S, CP_LL_RX_PDU_E, CP_LL_RX_PDU_MTU
291	Part10 - 1.31	CG_LL_TX_PATH	CR_LL_TX_PDU_LAS T_SEGMENT	cross CP_LL_TX_PDU_O, CP_LL_TX_PDU_P, CP_LL_TX_PDU_S, CP_LL_TX_PDU_E, CP_LL_TX_PDU_MTU
292	Part10 - 1.31	CG_LL_RX_PATH	CR_LL_RX_PDU_LAS T_SEGMENT	cross CP_LL_RX_PDU_O, CP_LL_RX_PDU_P, CP_LL_RX_PDU_S, CP_LL_RX_PDU_E, CP_LL_RX_PDU_MTU, CP_LL_RX_PDU_LENGTH
293	Part10 - 1.31	CG_LL_TX_PATH	CP_LL_TX_DATA_ST REAM_INTERLEAVE	BFM transmitting data streaming packets with interleaved PDUs.
294	Part10 - 1.31	CG_LL_RX_PATH	CP_LL_RX_DATA_ST REAM_INTERLEAVE	BFM receiving data streaming packets with interleaved PDUs.
295	Part10 - 3.2.5	CG_LL_TX_PATH	CP_LL_TX_DATA_ST REAM_FLOW_ID	BFM transmitting data streaming packets with all possible values for flow id.
296	Part10 - 3.2.5	CG_LL_RX_PATH	CP_LL_RX_DATA_ST REAM_FLOW_ID	BFM receiving data streaming packets with all possible values for flow id.
297	Part10 - 3.2.5	CG_LL_TX_PATH	CR_LL_TX_PDU_LEN GTH_MTU_FLOW_ID	cross CP_LL_TX_PDU_MTU, CP_LL_TX_PDU_LENGTH, CP_LL_TX_DATA_STREAM_FLOW_ID
298	Part10 - 3.2.5	CG_LL_RX_PATH	CR_LL_RX_PDU_LEN GTH_MTU_FLOW_ID	cross CP_LL_RX_PDU_MTU, CP_LL_RX_PDU_LENGTH, CP_LL_RX_DATA_STREAM_FLOW_ID
299	Part10 - 3.2.5	CG_LL_TX_PATH	CR_LL_TX_PDU_S_E _FLOW_ID	cross CP_LL_TX_PDU_S, CP_LL_TX_PDU_E, CP_LL_TX_DATA_STREAM_FLOW_ID
300	Part10 - 3.2.5	CG_LL_RX_PATH	CR_LL_RX_PDU_S_E _FLOW_ID	cross CP_LL_RX_PDU_S, CP_LL_RX_PDU_E, CP_LL_RX_DATA_STREAM_FLOW_ID
301	Part10 - 4.3	CG_LL_TX_PATH	CP_LL_TX_TM_TMOP	BFM transmitting Traffic Management message packets with all possible values for TM Operand field.
302	Part10 - 4.3	CG_LL_RX_PATH	CP_LL_RX_TM_TMO P	BFM receiving Traffic Management message packets with all possible values for TM Operand field.
303	Part10 - 4.3	CG_LL_TX_PATH	CP_LL_TX_TM_WC	BFM transmitting Traffic Management message packets with all possible values for wildcard Operand field.
304	Part10 - 4.3	CG_LL_RX_PATH	CP_LL_RX_TM_WC	BFM receiving Traffic Management message packets with all possible values for wildcard Operand field.

S.No	Spec.Ref	Cover Group	Cover Points	Description
305	Part10 - 4.3	CG_LL_TX_PATH	CP_LL_TX_TM_MASK	BFM transmitting Traffic Management message packets with all possible values for mask field.
306	Part10 - 4.3	CG_LL_RX_PATH	CP_LL_RX_TM_MASK	BFM receiving Traffic Management message packets with all possible values for mask field.
307	Part10 - 4.3	CG_LL_TX_PATH	CP_LL_TX_TM_PARAMETER1	BFM transmitting Traffic Management message packets with all possible values for Parameter1 field.
308	Part10 - 4.3	CG_LL_RX_PATH	CP_LL_RX_TM_PARAMETER2	BFM receiving Traffic Management message packets with all possible values for Parameter2 field.
309	Part10 - 4.3	CG_LL_TX_PATH	CP_LL_TX_TM_BASIC_TRAFFIC	BFM transmitting basic traffic management message packets with all possible values in parameter1 and parameter2 fields
310	Part10 - 4.3	CG_LL_TX_PATH	CP_LL_TX_TM_RATE_BASED_TRAFFIC	BFM transmitting rate based traffic management message packets with all possible values in parameter1 and parameter2 fields
311	Part10 - 4.3	CG_LL_TX_PATH	CP_LL_TX_TM_CREDIT_BASED_TRAFFIC	BFM transmitting credit based traffic management message packets with all possible values in parameter1 and parameter2 fields
312	Part10 - 4.3	CG_LL_RX_PATH	CP_LL_RX_TM_BASIC_TRAFFIC	BFM receiving basic traffic management message packets with all possible values in parameter1 and parameter2 fields
313	Part10 - 4.3	CG_LL_RX_PATH	CP_LL_RX_TM_RATE_BASED_TRAFFIC	BFM receiving rate based traffic management message packets with all possible values in parameter1 and parameter2 fields
314	Part10 - 4.3	CG_LL_RX_PATH	CP_LL_RX_TM_CREDIT_BASED_TRAFFIC	BFM receiving credit based traffic management message packets with all possible values in parameter1 and parameter2 fields
315	Part9 - 2.2.1	CG_LL_TX_PATH	CP_LL_TX_XON_XOFF	BFM transmitting flow control packets with all possible values for XON/XOFF field.
316	Part9 - 2.2.1	CG_LL_RX_PATH	CP_LL_RX_XON_XOFF	BFM receiving flow control packets with all possible values for XON/XOFF field.
317	Part9 - 2.2.1	CG_LL_TX_PATH	CP_LL_TX_FLOW_CONTROL_FLOW_ID	BFM transmitting flow control packets with all possible values for flowID field.
318	Part9 - 2.2.1	CG_LL_RX_PATH	CP_LL_RX_FLOW_CONTROL_FLOW_ID	BFM receiving flow control packets with all possible values for flowID field.
319	Part9 - 2.2.1	CG_LL_TX_PATH	CP_LL_TX_FLOW_CONTROL_DEST_ID	BFM transmitting flow control packets with all possible values for destID field.
320	Part9 - 2.2.1	CG_LL_RX_PATH	CP_LL_RX_FLOW_CONTROL_DEST_ID	BFM receiving flow control packets with all possible values for destID field.
321	Part9 - 2.2.1	CG_LL_TX_PATH	CP_LL_TX_FLOW_CONTROL_TARGET_DEST_ID	BFM transmitting flow control packets with all possible values for tgtDestID field.
322	Part9 - 2.2.1	CG_LL_RX_PATH	CP_LL_RX_FLOW_CONTROL_TARGET_DEST_ID	BFM receiving flow control packets with all possible values for tgtDestID field.
323	Part9 - 2.2.1	CG_LL_TX_PATH	CP_LL_TX_FLOW_CONTROL_FLOW_ID_FAMILY	BFM transmitting flow control packets with all possible values for FAM field.
324	Part9 - 2.2.1	CG_LL_RX_PATH	CP_LL_RX_FLOW_CONTROL_FLOW_ID_FAMILY	BFM receiving flow control packets with all possible values for FAM field.

S.No	Spec.Ref	Cover Group	Cover Points	Description
325	Part9 - 2.2.1	CG_LL_TX_PATH	CP_LL_TX_FLOW_CTRL_SOC	BFM transmitting flow control packets with all possible values for SOC field.
326	Part9 - 2.2.1	CG_LL_RX_PATH	CP_LL_RX_FLOW_CTRL_SOC	BFM receiving flow control packets with all possible values for SOC field.
327	Part9 - 2.2.1	CG_LL_TX_PATH	CR_LL_TX_FLOW_CTRL_XON_XOFF_FAM_FLOW_ID	cross CP_LL_TX_XON_XOFF, CP_LL_TX_FLOW_FAM, CP_LL_TX_FLOW_CTRL_FLOW_ID
328	Part9 - 2.2.1	CG_LL_RX_PATH	CR_LL_RX_FLOW_CTRL_XON_XOFF_FAM_FLOW_ID	cross CP_LL_RX_XON_XOFF, CP_LL_RX_FLOW_FAM, CP_LL_RX_FLOW_CTRL_FLOW_ID
329	Part9 - 2.2.2	CG_LL_TX_PATH	CP_LL_TX_FLOW_CTRL_PIPELINE_REQ_SINGLE_PDU	BFM transmitting single PDU requests such that one request is outstanding. Coverage to check the pipeline functionality.
330	Part9 - 2.2.2	CG_LL_RX_PATH	CP_LL_RX_FLOW_CTRL_PIPELINE_REQ_SINGLE_PDU	BFM receiving single PDU requests such that one request is outstanding. Coverage to check the pipeline functionality.
331	Part9 - 2.2.2	CG_LL_TX_PATH	CP_LL_TX_FLOW_CTRL_PIPELINE_REQ_MULTI_PDU	BFM transmitting multi PDU requests such that one request is outstanding. Coverage to check the pipeline functionality.
332	Part9 - 2.2.2	CG_LL_RX_PATH	CP_LL_RX_FLOW_CTRL_PIPELINE_REQ_MULTI_PDU	BFM receiving multi PDU requests such that one request is outstanding. Coverage to check the pipeline functionality.
Transport Layer				
333	Part3 - 2.4	CG_TL_TX_PATH	CP_TL_TX_TT_VALID	BFM transmitting packets with all possible valid values for tt field
334	Part3 - 2.4	CG_TL_RX_PATH	CP_TL_RX_TT_VALID	BFM transmitting receiving with all possible valid values for tt field
335	Part3 - 2.4	CG_TL_TX_PATH	CP_TL_TX_TT_INVALID	BFM transmitting packets with all invalid values for tt field.
336	Part3 - 2.4	CG_TL_TX_PATH	CP_TL_TX_SOURCEID	BFM transmitting packets with all possible valid values for sourceID field.
337	Part3 - 2.4	CG_TL_RX_PATH	CP_TL_RX_SOURCEID	BFM receiving packets with all possible valid values for sourceID field.
338	Part3 - 2.4	CG_TL_TX_PATH	CP_TL_TX_DESTINATIONID	BFM transmitting packets with all possible valid values for destinationID field.
339	Part3 - 2.4	CG_TL_RX_PATH	CP_TL_RX_DESTINATIONID	BFM receiving packets with all possible valid values for destinationID field.
Physical Layer				
340	Part6 - 1.1	CG_PL	CP_PL_LANE_WIDTH	Coverage for the BFM transmitting and receiving data with all possible lane widths.
341	Part6 - 1.1	CG_PL	CP_PL_DATA_RATE	Coverage for the BFM transmitting and receiving data at all possible data rates.
342	Part6 - 1.1	CG_PL	CR_PL_LANE_WIDTH_DATA_RATE	cross CP_PL_LANE_WIDTH, CP_PL_DATA_RATE
343	Part6 - 1.1	CG_PL_TX	CP_PL_TX_ACK_ID	BFM transmitting packets with all possible values for ackID field.
344	Part6 - 1.1	CG_PL_RX	CP_PL_RX_ACK_ID	BFM receiving packets with all possible values for ackID field.

S.No	Spec.Ref	Cover Group	Cover Points	Description
345	Part6 - 1.1	CG_PL_TX	CP_PL_TX_CS_TYPE	BFM transmitting Short Control and Long Control Symbol
346	Part6 - 1.1	CG_PL_RX	CP_PL_RX_CS_TYPE	BFM receiving Short Control and Long Control Symbol
347	Part6 - 1.1	CG_PL_TX	CP_PL_TX_VC	BFM transmitting all possible values for VC field
348	Part6 - 1.1	CG_PL_RX	CP_PL_RX_VC	BFM receiving all possible values for VC field
349	Part6 - 1.1	CG_PL_TX	CP_PL_TX_PRIO	BFM transmitting packets with all possible values for PRIO field
350	Part6 - 1.1	CG_PL_RX	CP_PL_RX_PRIO	BFM receiving packets with all possible values for PRIO field
351	Part6 - 1.1	CG_PL_TX	CP_PL_TX_CRF	BFM transmitting packets with all possible values for CRF field
352	Part6 - 1.1	CG_PL_RX	CP_PL_RX_CRF	BFM receiving packets with all possible values for CRF field
353	Part6 - 1.1	CG_PL_TX	CR_PL_TX_VC_PRIO_CRF	cross CP_PL_TX_VC, CP_PL_TX_PRIO, CP_PL_TX_CRF
354	Part6 - 1.1	CG_PL_RX	CR_PL_RX_VC_PRIO_CRF	cross CP_PL_RX_VC, CP_PL_RX_PRIO, CP_PL_RX_CRF
355	Part6 - 1.1	CG_PL_TX	CP_PL_TX_FTYPE_TTYPE	BFM transmitting all possible types of packets
356	Part6 - 1.1	CG_PL_RX	CP_PL_RX_FTYPE_TTYPE	BFM receiving all possible types of packets
357	Part6 - 1.1	CG_PL_TX	CR_PL_TX_VC_PRIO_CRF_FTYPE_TTYPE	cross CR_PL_TX_VC_PRIO_CRF, CP_PL_TX_FTYPE_TTYPE
358	Part6 - 1.1	CG_PL_RX	CR_PL_RX_VC_PRIO_CRF_FTYPE_TTYPE	cross CR_PL_RX_VC_PRIO_CRF, CP_PL_RX_FTYPE_TTYPE
359	Part6 - 2.3	CG_PL_TX	CP_PL_TX_PACKET_PAD_ZEROS	BFM transmitting packets with both padded and unpadded data.
360	Part6 - 2.3	CG_PL_RX	CP_PL_RX_PACKET_PAD_ZEROS	BFM receiving packets with both padded and unpadded data.
361	Part6 - 2.4.1	CG_PL_TX	CP_PL_TX_PACKET_EARLY_CRC	BFM transmitting packets containing early CRC.
362	Part6 - 2.4.1	CG_PL_RX	CP_PL_RX_PACKET_EARLY_CRC	BFM receiving packets containing early CRC.
363	Part6 - 2.4.1	CG_PL_TX	CP_PL_TX_PACKET_FINAL_CRC	BFM transmitting packets containing final CRC.
364	Part6 - 2.4.1	CG_PL_RX	CP_PL_RX_PACKET_FINAL_CRC	BFM receiving packets containing final CRC.
365	Part6 - 2.4.1	CG_PL_TX	CR_PL_TX_PACKET_PAD_ZEROS_EARLY_CRC_FINAL_CRC	cross CP_PL_TX_PACKET_PAD_ZEROS, CP_PL_TX_PACKET_EARLY_CRC, CP_PL_TX_PACKET_FINAL_CRC
366	Part6 - 2.4.1	CG_PL_RX	CR_PL_RX_PACKET_PAD_ZEROS_EARLY_CRC_FINAL_CRC	cross CP_PL_RX_PACKET_PAD_ZEROS, CP_PL_RX_PACKET_EARLY_CRC, CP_PL_RX_PACKET_FINAL_CRC
367	Part6 - 2.4.1	CG_PL_TX	CP_PL_TX_PACKET_EARLY_CRC_CORRUPT	BFM transmitting LP-Serial packets with corrupted early CRC.

S.No	Spec.Ref	Cover Group	Cover Points	Description
368	Part6 - 2.4.1	CG_PL_TX	CP_PL_TX_PACKET_FINAL_CRC_CORRUPT	BFM transmitting LP-Serial packets with corrupted final CRC.
369	Part6 - 2.4.1	CG_PL_TX	CP_PL_TX_PACKET_DOUBLE_EARLY_CRC_CORRUPT	BFM transmitting greater than 80 byte LP-Serial packets with early CRC corrupted.
370	Part6 - 2.4.1	CG_PL_TX	CP_PL_TX_PACKET_DOUBLE_LAST_CRC_CORRUPT	BFM transmitting greater than 80 byte LP-Serial packets with last CRC corrupted.
371	Part6 - 2.4.1	CG_PL_TX	CR_PL_TX_PACKET_DOUBLE_EARLY_CRC_LAST_CORRUPT	cross CP_PL_TX_PACKET_DOUBLE_EARLY_CRC_CORRUPT, CP_PL_TX_PACKET_DOUBLE_LAST_CRC_CORRUPT
372	Part6 - 2.5	CG_PL_TX	CP_PL_TX_PACKET_LENGTH	BFM transmitting zero, short, medium, maximum and more than maximum size packets. Note: Zero and more than max size packets (>256 bytes) are error scenarios
373	Part6 - 2.5	CG_PL_RX	CP_PL_RX_PACKET_LENGTH	BFM receiving zero, short, medium maximum size packets.
374	Part6 - 3.2	CG_PL_TX	CP_PL_TX_STYPE0	BFM transmitting control symbols with all possible values for stype0 sub-field
375	Part6 - 3.2	CG_PL_RX	CP_PL_RX_STYPE0	BFM receiving control symbols with all possible values for stype0 sub-field
376	Part6 - 3.2	CG_PL_TX	CR_PL_TX_CSTYPE_STYPE0	cross CP_PL_TX_CS_TYPE, CP_PL_TX_STYPE0
377	Part6 - 3.2	CG_PL_RX	CR_PL_RX_CSTYPE_STYPE0	cross CP_PL_RX_CS_TYPE, CP_PL_RX_STYPE0
378	Part6 - 3.2	CG_PL_TX	CP_PL_TX_PARAMETER0	BFM transmitting control symbols with all possible values for parameter0 sub-field
379	Part6 - 3.2	CG_PL_RX	CP_PL_RX_PARAMETER0	BFM receiving control symbols with all possible values for parameter0 sub-field
380	Part6 - 3.2	CG_PL_TX	CP_PL_TX_PARAMETER1	BFM transmitting control symbols with all possible values for parameter1 sub-field
381	Part6 - 3.2	CG_PL_RX	CP_PL_RX_PARAMETER1	BFM receiving control symbols with all possible values for parameter1 sub-field
382	Part6 - 3.2	CG_PL_TX	CP_PL_TX_PACKET_NA_PARAM1	BFM transmitting packet not accepted control symbol with all possible values for cause field
383	Part6 - 3.2	CG_PL_RX	CP_PL_RX_PACKET_NA_PARAM1	BFM receiving packet not accepted control symbol with all possible values for cause field
384	Part6 - 3.2	CG_PL_TX	CP_PL_TX_STYPE1	BFM transmitting control symbols with all possible values for stype1 sub-field
385	Part6 - 3.2	CG_PL_RX	CP_PL_RX_STYPE1	BFM receiving control symbols with all possible values for stype1 sub-field
386	Part6 - 3.2	CG_PL_TX	CP_PL_TX_CMD	BFM transmitting control symbols with all possible values for cmd sub-field
387	Part6 - 3.2	CG_PL_RX	CP_PL_RX_CMD	BFM receiving control symbols with all possible values for cmd sub-field

S.No	Spec.Ref	Cover Group	Cover Points	Description
388	Part6 - 3.2	CG_PL_TX	CR_PL_TX_STYPE1_CMD	cross CR_PL_TX_STYPE1, CP_PL_TX_CMD
389	Part6 - 3.2	CG_PL_RX	CR_PL_RX_STYPE1_CMD	cross CR_PL_RX_STYPE1, CP_PL_RX_CMD
390	Part6 - 3.2	CG_PL_TX	CR_PL_TX_CS_TYPE_STYPE1_CMD	cross CP_PL_TX_CS_TYPE, CR_PL_TX_STYPE1_CMD
391	Part6 - 3.2	CG_PL_TX	CR_PL_RX_CS_TYPE_STYPE1_CMD	cross CP_PL_RX_CS_TYPE, CR_PL_RX_STYPE1_CMD
392	Part6 - 3.2	CG_PL_TX	CP_PL_TX_SCS_CORRUPT_CRC	BFM transmitting short control symbols with corrupted CRC.
393	Part6 - 3.2	CG_PL_TX	CP_PL_TX_LCS_CORRUPT_CRC	BFM transmitting Long control symbols with corrupted CRC.
394	Part6 - 3.5.5.1	CG_PL_TX	CP_PL_TX_RESET_DEV_CMD_B2B	BFM transmitting back to back reset device command with the following combinations a) 4 b2b reset device cmds. b) 3 b2b reset device cmds followed by any control symbol other than status control symbol and followed by 1 reset device command. c) 2 b2b reset device cmds followed by any control symbol other than status control symbol and followed by 2 b2b reset device cmds. d) 1 reset device cmds followed by any control symbol other than status control symbol and followed by 3 b2b reset device cmds. e) 3 b2b reset device cmds followed by status control symbol and followed by 1 reset device command. f) 2 b2b reset device cmds followed by status control symbol and followed by 2 reset device command. g) 1 reset device cmds followed by status control symbol and followed by 3 reset device command. h) 4 reset device cmds separated by 4 status control symbols in between each of them.
395	Part6 - 4.7	CG_PL_TX	CP_PL_TX_IDLE1	BFM transmitting idle1 sequence when port is initialize and un-initialized
396	Part6 - 4.7	CG_PL_TX	CP_PL_TX_IDLE2	BFM transmitting idle2 sequence when port is initialize and un-initialized
397	Part6 - 4.7	CG_PL_RX	CP_PL_RX_IDLE1	BFM receiving idle1 sequence when port is initialize and un-initialized
398	Part6 - 4.7	CG_PL_RX	CP_PL_RX_IDLE2	BFM receiving idle2 sequence when port is initialize and un-initialized
399	Part6 - 4.7	CG_PL_TX_SEQ	CP_PL_TX_PACKET_IDLE_SYMBOL_ERROR	BFM transmitting idle sequences embedded in a packet
400	Part6 - 4.7.1	CG_PL_TX_SEQ	CP_PL_TX_CLOCK_COMP_SEQ	BFM transmitting clock compensation sequence on all lanes in parallel. (K, R, R, R)
401	Part6 - 4.7.1	CG_PL_TX_SEQ	CP_PL_TX_MULTI_LANE_CLK_COMP_ERR	Coverage for BFM transmitting a) clock compensation sequence in one lane alone (applicable for multi-lane);

S.No	Spec.Ref	Cover Group	Cover Points	Description
402	Part6 - 4.7.1	CG_PL_TX_SEQ	CP_PL_TX_NO_CLK_COMP_ERR	Coverage for BFM not transmitting clock compensation sequence even after the transmission of 5000 characters.
403	Part6 - 4.7.2	CG_PL_TX_SEQ	CP_PL_TX_A_CHARACTER_INTERVAL	BFM transmitting various number of non-A special characters between two A character in IDLE1 sequence a) less than 16 non-A character between two A characters. b) exactly 16 non-A character between two A characters. c) between 16 to 31 non-A character between two A characters. d) exactly 31 non-A character between two A characters. e) greater than 31 non-A character between two A characters.
404	Part6 - 4.7.4.1	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_RANDOM_DATA_LENGTH	BFM transmitting IDLE2 sequences with the following random data field character lengths a) <509 characters b) 509 characters c) 509 to 515 characters d) 515 characters e) >515 characters
405	Part6 - 4.7.4.1.2	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_ACTIVE_LINK_WIDTH	BFM transmitting all possible values for active link width in CS field marker
406	Part6 - 4.7.4.1.2	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_LANE_NUM	BFM transmitting all possible values for lane number in CS field marker
407	Part6 - 4.7.4.1.2	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_MARKER_CORRUPT	BFM transmitting corrupted CS field marker in the IDLE2 sequence
408	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_CORRUPT	BFM transmitting corrupted cs fields in IDLE2 sequence Note: if cs_field[32:63] is not the bit complement of cs_field[0:31]
409	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_CMD	BFM transmitting CS field of IDLE2 sequence with all possible values for cmd field
410	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_RCVR_TRAINED	BFM transmitting CS field of IDLE2 sequence with all possible values for receiver trained field
411	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_TAP_MINUS_1_STATUS	BFM transmitting CS field of IDLE2 sequence with all possible values for Tap(-1) status field
412	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_TAP_PLUS_1_STATUS	BFM transmitting CS field of IDLE2 sequence with all possible values for Tap(+1) status field
413	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_TAP_MINUS_1_CMD	BFM transmitting CS field of IDLE2 sequence with all possible values for Tap(-1) cmd field
414	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_TAP_PLUS_1_CMD	BFM transmitting CS field of IDLE2 sequence with all possible values for Tap(+1) cmd field

S.No	Spec.Ref	Cover Group	Cover Points	Description
415	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_RST_EMP	BFM transmitting CS field of IDLE2 sequence with all possible values for reset emphasis field
416	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_PRESET_EMP	BFM transmitting CS field of IDLE2 sequence with all possible values for preset emphasis field
417	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_ACK	BFM transmitting CS field of IDLE2 sequence with all possible values for ack field
418	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_CS_FIELD_NACK	BFM transmitting CS field of IDLE2 sequence with all possible values for nack field
419	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CR_PL_TX_IDLE2_CS_FIELD_CMD_MINUS_TAP_ACK	cross CP_PL_TX_IDLE2_CS_FIELD_CMD CP_PL_TX_IDLE2_CS_FIELD_TAP_MINUS_1_CMD, CP_PL_TX_IDLE2_CS_FIELD_ACK
420	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CR_PL_TX_IDLE2_CS_FIELD_CMD_PLUS_TAP_ACK	cross CP_PL_TX_IDLE2_CS_FIELD_CMD CP_PL_TX_IDLE2_CS_FIELD_TAP_PLUS_1_CMD, CP_PL_TX_IDLE2_CS_FIELD_ACK
421	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CR_PL_TX_IDLE2_CS_FIELD_CMD_MINUS_TAP_NACK	cross CP_PL_TX_IDLE2_CS_FIELD_CMD CP_PL_TX_IDLE2_CS_FIELD_TAP_MINUS_1_CMD, CP_PL_TX_IDLE2_CS_FIELD_NACK
422	Part6 - 4.7.4.1.3	CG_PL_TX_SEQ	CR_PL_TX_IDLE2_CS_FIELD_CMD_PLUS_TAP_NACK	cross CP_PL_TX_IDLE2_CS_FIELD_CMD CP_PL_TX_IDLE2_CS_FIELD_TAP_PLUS_1_CMD, CP_PL_TX_IDLE2_CS_FIELD_NACK
423	Part6 - 4.7.5	CG_PL_TX_SEQ	CP_PL_TX_IDLE2_IDLE1	BFM transmitting idle2 sequence followed by idle1 sequence
424	Part6 - 4.8	CG_PL_TX_SEQ	CP_PL_TX_SYNC_SEQ	BFM transmitting SYNC sequence (MDDDD)
425	Part6 - 4.8	CG_PL_TX_SEQ	CP_PL_RX_SYNC_SEQ	BFM receiving SYNC sequence (MDDDD)
426	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_1X_MODE_DELIMITER	Coverage for assertion and de-assertion of 1x_Mode_Delimiter variable in BFM.
427	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_1X_MODE_DETECTED	Coverage for assertion and de-assertion of 1x_Mode_Detected variable in BFM.
428	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_2X_MODE_DELIMITER	Coverage for assertion and de-assertion of 1x_Mode_Delimiter variable in BFM.
429	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_2X_A_COUNTER	Coverage for assertion and de-assertion of A variable in 2x mode in BFM.
430	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_NX_A_COUNTER	Coverage for assertion and de-assertion of A variable in nx mode in BFM.
431	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_2X_A_COUNTER	Coverage for BFM taking all possible values for A_Counter in 2x mode
432	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_NX_A_COUNTER	Coverage for BFM taking all possible values for A_Counter in nx mode
433	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_2X_M_COUNTER	Coverage for BFM taking all possible values for M_Counter in 2x mode
434	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_NX_M_COUNTER	Coverage for BFM taking all possible values for M_Counter in nx mode
435	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_2X_ALIGN_ERROR	Coverage for assertion and de-assertion of align_error signal in BFM in 2x mode
436	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_NX_ALIGN_ERROR	Coverage for assertion and de-assertion of align_error signal in BFM in nx mode

S.No	Spec.Ref	Cover Group	Cover Points	Description
437	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_D_COUNTER	Coverage for BFM taking all possible values for D_Counter
438	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_DISC_TMR_DONE	Coverage for assertion and de-assertion of disc_tmr_done variable in BFM
439	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_DISC_TMR_START	Coverage for assertion and de-assertion of disc_tmr_start variable in BFM
440	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_DISC_TMR_EN	Coverage for assertion and de-assertion of disc_tmr_en variable in BFM
441	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_FORCE_1X_MODE	Coverage for assertion and de-assertion of force_1x_mode variable in BFM
442	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_FORCE_LANE_R	Coverage for assertion and de-assertion of force_laneR variable in BFM
443	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_FORCE_REINIT	Coverage for assertion and de-assertion of force_reinit variable in BFM
444	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_I_COUNTER	Coverage for BFM taking all possible values for I_Counter
445	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_IDLE_SELECTED	Coverage for assertion and de-assertion of idle_selected variable in BFM
446	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_K_COUNTER	Coverage for BFM taking all possible values for K_Counter
447	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_LANE_READY_N	Coverage for assertion and de-assertion of lane_ready[n] in BFM
448	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_LANE_SYNC_N	Coverage for assertion and de-assertion of lane_sync[n] in BFM
449	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_LANES01_DRV_OE	Coverage for assertion and de-assertion of lanes01_drvr_oe variable in BFM
450	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_LANES02_DRV_OE	Coverage for assertion and de-assertion of lanes02_drvr_oe variable in BFM
451	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_LANES13_DRV_OE	Coverage for assertion and de-assertion of lanes13_drvr_oe variable in BFM
452	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_N_LANES_ALIGNED	Coverage for assertion and de-assertion of n_lanes_aligned variable in BFM
453	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_N_LANES_DRV_OE	Coverage for assertion and de-assertion of n_lanes_drvr_oe variable in BFM
454	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_N_LANES_READY	Coverage for assertion and de-assertion of n_lanes_ready variable in BFM
455	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_N_LANES_SYNC	Coverage for assertion and de-assertion of n_lanes_sync variable in BFM
456	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_PORT_INITIALIZED	Coverage for assertion and de-assertion of Port_Initialized variable in BFM
457	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_RECEIVE_LANE1	Coverage for assertion and de-assertion of Receive_Lane1 variable in BFM
458	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_RECEIVE_LANE2	Coverage for assertion and de-assertion of Receive_Lane2 variable in BFM
459	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_RCVR_TRAINED_N	Coverage for assertion and de-assertion of Rcvr_Trained[n] variable in BFM
460	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_SIGNAL_DETECT_N	Coverage for assertion and de-assertion of Signal_Detect[n] variable in BFM

S.No	Spec.Ref	Cover Group	Cover Points	Description
461	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_SILENCE_TIMER_DONE	Coverage for assertion and de-assertion of Silence_Timer_Done variable in BFM
462	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_SILENCE_TIMER_EN	Coverage for assertion and de-assertion of Silence_Timer_En variable in BFM
463	Part6 - 4.12.3	CG_PL_SM_VARIABLE	CP_PL_SM_V_COUNTER	Coverage for BFM taking all possible values for V_Counter
464	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_NEXT_STATE	Coverage for BFM transitioning to all possible states in lane synchronization state machine. No_Sync, No_Sync1, No_Sync2, No_Sync3 Sync, Sync1, Sync2, Sync3, Sync4
465	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_RESET	Coverage for the assertion and deassertion of reset
466	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SIGNAL_DETECT	Coverage for the assertion and deassertion of SIGNAL_DETECT
467	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CR_PL_SYNC_NEXT_STATE_RESET	cross CP_PL_LANEX_SYNC_NEXT_STATE, CP_PL_RESET
468	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CR_PL_SYNC_NEXT_STATE_SIGNAL_DETECT	cross CP_PL_LANEX_SYNC_NEXT_STATE, CP_PL_SIGNAL_DETECT
469	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_TO_NS	Coverage for BFM making following transitions - No_Sync => No_Sync - No_Sync2 => No_Sync - No_Sync3 => No_Sync - Sync1 => No_Sync
470	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_TO_NS1	Coverage for BFM making following transitions - No_Sync => No_Sync1 - No_Sync3 => No_Sync1
471	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_TO_NS2	Coverage for BFM making following transitions - No_Sync1 => No_Sync2 - No_Sync3 => No_Sync2
472	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_TO_NS3	Coverage for BFM making following transitions - No_Sync2 => No_Sync3
473	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_TO_S	Coverage for BFM making following transitions - No_Sync1 => Sync - Sync => Sync - Sync4 => Sync
474	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_TO_S1	Coverage for BFM making following transitions - Sync => Sync1 - Sync2 => Sync1
475	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_TO_S2	Coverage for BFM making following transitions - Sync1 => Sync2 - Sync3 => Sync2 - Sync4 => Sync2
476	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_TO_S3	Coverage for BFM making following transitions - Sync2 => Sync3
477	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANEX	CP_PL_SYNC_TO_S4	Coverage for BFM making following transitions - Sync3 => Sync4

S.No	Spec.Ref	Cover Group	Cover Points	Description
478	Part6 - 4.12.4.2	CG_PL_SYNC_SM_LANE_X	CP_PL_SYNC_PATH_TRANSITIONS	Coverage for BFM making following transitions a) No_Sync -> No_Sync1 -> Sync b) No_Sync -> No_Sync1 -> No_Sync2 -> No_Sync3 -> No_Sync2 -> No_Sync1 -> Sync c) No_Sync -> No_Sync1 -> No_Sync2 -> No_Sync1 -> Sync d) No_Sync -> No_Sync1 -> No_Sync2 -> No_Sync -> No_Sync1 -> Sync e) No_Sync -> No_Sync1 -> Sync -> Sync1 -> No_Sync f) Sync -> Sync1 -> Sync2 -> Sync3 -> Sync4 -> Sync g) Sync -> Sync1 -> Sync2 -> Sync3 -> Sync4 -> Sync2 -> Sync3 -> Sync4 -> Sync h) Sync -> Sync1 -> Sync2 -> Sync3 -> Sync2 -> Sync3 -> Sync4 -> Sync i) Sync -> Sync1 -> Sync2 -> Sync3 -> Sync2 -> Sync3 -> Sync4 -> Sync2 -> Sync3 -> Sync4 -> Sync
479	Part6 - 4.12.4.3	CG_PL_LANE_ALIGN_2X	CP_PL_LANE_ALIGN_2X_NEXT_STATE	Coverage for BFM transitioning to the following states a) Not_Aligned b) Not_Aligned1 c) Not_Aligned2 d) Aligned e) Aligned1 f) Aligned2 g) Aligned3
480	Part6 - 4.12.4.3	CG_PL_LANE_ALIGN_2X	CP_PL_RESET	Coverage for the assertion and deassertion of reset
481	Part6 - 4.12.4.3	CG_PL_LANE_ALIGN_2X	CR_PL_LANE_ALIGN_2X_NEXT_STATE_RESET	cross CP_PL_LANE_ALIGN_2X_NEXT_STATE, CP_PL_RESET
482	Part6 - 4.12.4.3	CG_PL_LANE_ALIGN_2X	CP_PL_LANE_ALIGN_2X_TO_NA	Coverage for BFM making the following transitions a) Not_Aligned -> Not_Aligned b) Not_Aligned2 -> Not_Aligned c) Aligned1 -> Not_Aligned
483	Part6 - 4.12.4.3	CG_PL_LANE_ALIGN_2X	CP_PL_LANE_ALIGN_2X_TO_NA1	Coverage for BFM making the following transitions a) Not_Aligned -> Not_Aligned1 b) Not_Aligned2 -> Not_Aligned
484	Part6 - 4.12.4.3	CG_PL_LANE_ALIGN_2X	CP_PL_LANE_ALIGN_2X_TO_NA2	Coverage for BFM making the following transitions a) Not_Aligned1 -> Not_Aligned2 b) Not_Aligned2 -> Not_Aligned2
485	Part6 - 4.12.4.3	CG_PL_LANE_ALIGN_2X	CP_PL_LANE_ALIGN_2X_TO_A	Coverage for BFM making the following transitions a) Not_Aligned1 -> Aligned b) Aligned -> Aligned c) Aligned3 -> Aligned
486	Part6 - 4.12.4.3	CG_PL_LANE_ALIGN_2X	CP_PL_LANE_ALIGN_2X_TO_A1	Coverage for BFM making the following transitions a) Aligned -> Aligned1 b) Aligned2 -> Aligned1

S.No	Spec.Ref	Cover Group	Cover Points	Description
487	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_2X	CP_PL_LANE_ALIGN _2X_TO_A2	Coverage for BFM making the following transitions a) Aligned1 -> Aligned2 b) Aligned2 -> Aligned2 c) Aligned3 -> Aligned2
488	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_2X	CP_PL_LANE_ALIGN _2X_TO_A3	Coverage for BFM making the following transitions a) Aligned2 -> Aligned3
489	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_2X	CP_PL_LANE_ALIGN _2X_TO_PATH_TRAN SITIONS	Coverage for BFM making the following path transitions a) Not_Aligned -> Not_Aligned1 -> Aligned b) Not_Aligned -> Not_Aligned1 -> Not_Aligned2 -> Not_Aligned1 -> Aligned c) Not_Aligned -> Not_Aligned1 -> Not_Aligned2 -> Not_Aligned2 -> Not_Aligned1 -> Aligned d) Not_Aligned -> Not_Aligned1 -> Not_Aligned2 -> Not_Aligned -> Aligned e) Aligned -> Aligned1 -> Aligned2 -> Aligned3 -> Aligned f) Aligned -> Aligned1 -> Aligned2 -> Aligned3 -> Aligned2 -> Aligned3 -> Aligned g) Aligned -> Aligned1 -> Not_Aligned -> Not_Aligned1 -> Aligned
490	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_LANE_ALIGN _NX_NEXT_STATE	Coverage for BFM transitioning to the following states a) Not_Aligned b) Not_Aligned1 c) Not_Aligned2 d) Aligned e) Aligned1 f) Aligned2 g) Aligned3
491	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_RESET	Coverage for the assertion and deassertion of reset
492	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CR_PL_LANE_ALIGN _NX_NEXT_STATE_R ESET	cross CP_PL_LANE_ALIGN_NX_NEXT_STATE, CP_PL_RESET
493	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_LANE_ALIGN _NX_TO_NA	Coverage for BFM making the following transitions a) Not_Aligned -> Not_Aligned b) Not_Aligned2 -> Not_Aligned c) Aligned1 -> Not_Aligned
494	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_LANE_ALIGN _NX_TO_NA1	Coverage for BFM making the following transitions a) Not_Aligned -> Not_Aligned1 b) Not_Aligned2 -> Not_Aligned
495	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_LANE_ALIGN _NX_TO_NA2	Coverage for BFM making the following transitions a) Not_Aligned1 -> Not_Aligned2 b) Not_Aligned2 -> Not_Aligned2
496	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_LANE_ALIGN _NX_TO_A	Coverage for BFM making the following transitions a) Not_Aligned1 -> Aligned b) Aligned -> Aligned c) Aligned3 -> Aligned

S.No	Spec.Ref	Cover Group	Cover Points	Description
497	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_LANE_ALIGN _NX_TO_A1	Coverage for BFM making the following transitions a) Aligned -> Aligned1 b) Aligned2 -> Aligned1
498	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_LANE_ALIGN _NX_TO_A2	Coverage for BFM making the following transitions a) Aligned1 -> Aligned2 b) Aligned2 -> Aligned2 c) Aligned3 -> Aligned2
499	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_LANE_ALIGN _NX_TO_A3	Coverage for BFM making the following transitions a) Aligned2 -> Aligned3
500	Part6 - 4.12.4.3	CG_PL_LANE_ALI GN_NX	CP_PL_LANE_ALIGN _NX_TO_PATH_TRAN SITIONS	Coverage for BFM making the following path transitions a) Not_Aligned -> Not_Aligned1 -> Aligned b) Not_Aligned -> Not_Aligned1 -> Not_Aligned2 -> Not_Aligned1 -> Aligned c) Not_Aligned -> Not_Aligned1 -> Not_Aligned2 -> Not_Aligned2 -> Not_Aligned1 -> Aligned d) Not_Aligned -> Not_Aligned1 -> Not_Aligned2 -> Not_Aligned -> Aligned e) Aligned -> Aligned1 -> Aligned2 -> Aligned3 -> Aligned f) Aligned -> Aligned1 -> Aligned2 -> Aligned3 -> Aligned2 -> Aligned3 -> Aligned g) Aligned -> Aligned1 -> Not_Aligned -> Not_Aligned1 -> Aligned
501	Part6 - 4.12.4.4	CG_PL_MODE_DE TECT_SM	CP_PL_MODE_DETE CT_NEXT_STATE	Coverage for bfm transitioning to the following states a) Initialize b) Get_Column c) 1x_Delimiter d) 2x_Delimiter e) Set_1x_Mode f) Set_2x_Mode
502	Part6 - 4.12.4.4	CG_PL_MODE_DE TECT_SM	CP_PL_RESET	Coverage for assertion and de-assertion of reset signal
503	Part6 - 4.12.4.4	CG_PL_MODE_DE TECT_SM	CP_PL_2_LANES_ALI GNED	Coverage for assertion and de-assertion of 2_lanes_aligned
504	Part6 - 4.12.4.4	CG_PL_MODE_DE TECT_SM	CP_PL_MODE_DETE CT_TO_INITIALIZE	Coverage for bfm making the following transitions a) Initialize -> Initialize
505	Part6 - 4.12.4.4	CG_PL_MODE_DE TECT_SM	CP_PL_MODE_DETE CT_TO_GET_COLUM N	Coverage for bfm making the following transitions a) Initialize -> Get_Column b) Get_Column -> Get_Column c) Set_1x_Mode -> Get_Column d) Set_2x_Mode -> Get_Column e) 1x_Delimiter -> Get_Column f) 2x_Delimiter -> Get_Column
506	Part6 - 4.12.4.4	CG_PL_MODE_DE TECT_SM	CP_PL_MODE_DETE CT_TO_X1_DELIMITE R	Coverage for bfm making the following transitions a) Get_Column -> 1x_Delimiter
507	Part6 - 4.12.4.4	CG_PL_MODE_DE TECT_SM	CP_PL_MODE_DETE CT_TO_X2_DELIMITE R	Coverage for bfm making the following transitions a) Get_Column -> 2x_Delimiter

S.No	Spec.Ref	Cover Group	Cover Points	Description
508	Part6 - 4.12.4.4	CG_PL_MODE_DETECT_SM	CP_PL_MODE_DETECT_TO_SET_1X_MODE	Coverage for bfm making the following transitions a) 1x_Delimiter -> Set_1x_Mode
509	Part6 - 4.12.4.4	CG_PL_MODE_DETECT_SM	CP_PL_MODE_DETECT_TO_SET_2X_MODE	Coverage for bfm making the following transitions a) 2x_Delimiter -> Set_2x_Mode
510	Part6 - 4.12.4.4	CG_PL_MODE_DETECT_SM	CP_PL_MODE_DETECT_PATH_TRANSITIONS	Coverage for bfm making the following path transitions 1) GET_COLUMN => X1_DELIMITER => SET_1X_MODE 2) GET_COLUMN => X2_DELIMITER => SET_2X_MODE 3) GET_COLUMN => X1_DELIMITER => GET_COLUMN 4) GET_COLUMN => X2_DELIMITER => GET_COLUMN
511	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_INIT_NEXT_STATE	Coverage for making transitioning to the following states a) Silent b) Seek c) Discovery d) Nx_Mode e) 2x_Mode f) 1x_Mode_Lane0 g) 1x_Mode_Lane1 h) 1x_Mode_Lane2 i) 1x_Recovery j) 2x_Recovery
512	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_RESET	Coverage for assertion and de-assertion of reset signal
513	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_FORCE_REINIT	Coverage for assertion and de-assertion of force_reinit
514	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CR_PL_1X_2X_NX_INIT_NEXT_STATE_RESET	cross CP_PL_1X_2X_NX_INIT_NEXT_STATE, CP_PL_RESET
515	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CR_PL_1X_2X_NX_INIT_NEXT_STATE_FORCE_REINIT	cross CP_PL_1X_2X_NX_INIT_NEXT_STATE,, CP_PL_FORCE_REINIT
516	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_INIT_TO_SILENT	Coverage for bfm making the following transitions a) Silent -> Silent b) Discovery -> Silent c) 2x_Recovery -> Silent d) Nx_Mode -> Silent e) 2x_Mode -> Silent f) 1x_Mode_Lane0 -> Silent g) 1x_Mode_Lane1 -> Silent h) 1x_Mode_Lane2 -> Silent
517	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_INIT_TO_SEEK	Coverage for bfm making the following transitions a) Silent -> Seek

S.No	Spec.Ref	Cover Group	Cover Points	Description
518	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_DISCOVERY	Coverage for bfm making the following transitions a) Seek -> Discovery b) Nx_Mode -> Discovery
519	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_1X_RECOVERY	Coverage for bfm making the following transitions a) 1x_Mode_Lane0 -> 1x_Recovery b) 1x_Mode_Lane1 -> 1x_Recovery c) 1x_Mode_Lane2 -> 1x_Recovery
520	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_2X_RECOVERY	Coverage for bfm making the following transitions a) 2x_Mode -> 2x_Recovery
521	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_NX_MODE	Coverage for bfm making the following transitions a) Discovery -> Nx_Mode
522	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_2X_MODE	Coverage for bfm making the following transitions a) Discovery -> 2x_Mode b) 2x_Recovery -> 2x_Mode
523	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_1X_MODE_LANE0	Coverage for bfm making the following transitions a) Discovery -> 1x_Mode_Lane0 b) 2x_Recovery -> 1x_Mode_Lane0 c) 1x_Recovery -> 1x_Mode_Lane0
524	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_1X_MODE_LANE1	Coverage for bfm making the following transitions a) Discovery -> 1x_Mode_Lane1 b) 2x_Recovery -> 1x_Mode_Lane1 c) 1x_Recovery -> 1x_Mode_Lane1
525	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_1X_MODE_LANE2	Coverage for bfm making the following transitions a) Discovery -> 1x_Mode_Lane2 b) 1x_Recovery -> 1x_Mode_Lane2

S.No	Spec.Ref	Cover Group	Cover Points	Description
526	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_1X_2X_NX_INIT_PATH_TRANSITIONS	<p>Coverage for bfm making the following path transitions</p> <p>a) Silent -> Seek -> Discovery -> Silent</p> <p>b) Silent -> Seek -> Discovery -> Nx_Mode -> Silent</p> <p>c) Silent -> Seek -> Discovery -> 2x_Mode -> Silent</p> <p>d) Silent -> Seek -> Discovery -> 1x_Mode_Lane0 -> Silent</p> <p>e) Silent -> Seek -> Discovery -> 1x_Mode_Lane1 -> Silent</p> <p>f) Silent -> Seek -> Discovery -> 1x_Mode_Lane2 -> Silent</p> <p>g) Silent -> Seek -> Discovery -> 1x_Mode_Lane0 -> 1x_Recovery -> 1x_Mode_Lane0 -> Silent</p> <p>h) Silent -> Seek -> Discovery -> 1x_Mode_Lane1 -> 1x_Recovery -> 1x_Mode_Lane1 -> Silent</p> <p>i) Silent -> Seek -> Discovery -> 1x_Mode_Lane2 -> 1x_Recovery -> 1x_Mode_Lane2 -> Silent</p> <p>j) Silent -> Seek -> Discovery -> Nx_Mode -> Discovery -> Nx_Mode -> Silent</p> <p>k) Silent -> Seek -> Discovery -> Nx_Mode -> Discovery -> 2x_Mode -> Silent</p> <p>l) Silent -> Seek -> Discovery -> Nx_Mode -> Discovery -> 1x_Mode_Lane0 -> Silent</p> <p>m) Silent -> Seek -> Discovery -> Nx_Mode -> Discovery -> 1x_Mode_Lane1 -> Silent</p> <p>n) Silent -> Seek -> Discovery -> Nx_Mode -> Discovery -> 1x_Mode_Lane2 -> Silent</p> <p>o) Silent -> Seek -> Discovery -> 2x_Mode -> 2x_Recovery -> Silent</p> <p>p) Silent -> Seek -> Discovery -> 2x_Mode -> 2x_Recovery -> 2x_Mode -> Silent</p> <p>q) Silent -> Seek -> Discovery -> 2x_Mode -> 2x_Recovery -> 1x_Mode_Lane0 -> Silent</p> <p>r) Silent -> Seek -> Discovery -> 2x_Mode -> 2x_Recovery -> 1x_Mode_Lane1 -> Silent</p> <p>s) Silent -> Seek -> Discovery -> 2x_Mode -> 2x_Recovery -> 1x_Mode_Lane0 -> 1x_Recovery -> 1x_Mode_Lane0 -> Silent</p> <p>t) Silent -> Seek -> Discovery -> 2x_Mode -> 2x_Recovery -> 1x_Mode_Lane1 -> 1x_Recovery -> 1x_Mode_Lane1 -> Silent</p>
527	Part6 - 4.12.4.8	CG_PL_1X_2X_NX_INIT_SM	CP_PL_NUM_OF_LANES	Coverage for bfm working in all possible lane widths
528	Part6 - 5.2	CG_PL_TX	CP_PL_SERIAL_TRAFFIC_MODE	BFM transmitting and receiving traffic in both RT and CT mode.
529	Part6 - 5.2	CG_PL_TX	CP_PL_SERIAL_TRAFFIC_VC	BFM transmitting and receiving traffic in all the VCs
530	Part6 - 5.5.2	CG_PL_TX	CP_PL_TX_CS_DELIMITER	<p>BFM transmitting the following control symbol delimiters</p> <p>a) /SC/</p> <p>b) /PD/</p>

S.No	Spec.Ref	Cover Group	Cover Points	Description
531	Part6 - 5.5.2	CG_PL_RX	CP_PL_RX_CS_DELIMITER	BFM receiving the following control symbol delimiters a) /SC/ b) /PD/
532	Part6 - 5.5.2	CG_PL_TX	CP_PL_PORT_INITIALIZED	Coverage for the assertion and de-assertion of port_initialized signal
533	Part6 - 5.5.2	CG_PL_TX	CR_PL_TX_IDLE1_PORT_INITIALIZED	cross CP_PL_TX_IDLE1, CP_PL_PORT_INITIALIZED
534	Part6 - 5.5.2	CG_PL_TX	CR_PL_TX_IDLE2_PORT_INITIALIZED	cross CP_PL_TX_IDLE2_SEQ, CP_PL_PORT_INITIALIZED
535	Part6 - 5.5.3.1	CG_PL_TX_SEQ	CP_PL_TX_CS_STATUS_BLOCKED	BFM not transmitting status control symbol for every 1024 code-groups after port initialization.
536	Part6 - 5.5.3.1	CG_PL_TX	CP_PL_LINK_INITIALIZED	Coverage for assertion and de-assertion of link initialized variable
537	Part6 - 5.5.3.1	CG_PL_TX	CR_PL_LINK_INITIALIZED_PORT_INITIALIZED_CS_DELIMITER	cross CP_PL_TX_LINK_INITIALIZED, CP_PL_TX_PORT_INITIALIZED, CP_PL_TX_CS_DELIMITER
538	Part6 - 5.5.3.1	CG_PL_TX	CP_PL_INPUT_ERROR_STOPPED_STATE_LINK_INIT	Coverage for BFM asserting/de-asserting link initialized variable when the port is in input error stopped state
539	Part6 - 5.5.3.1	CG_PL_TX	CP_PL_OUTPUT_ERROR_STOPPED_STATE_LINK_INIT	Coverage for BFM asserting/de-asserting link initialized variable when the port is in output error stopped state
540	Part6 - 5.5.3.1	CG_PL_TX	CP_PL_TX_EMBEDDED_CS_STYPE0	Coverage for BFM transmitting embedded control symbols with all possible values for stype0
541	Part6 - 5.5.3.1	CG_PL_TX	CP_PL_TX_EMBEDDED_CS_STYPE1	Coverage for BFM transmitting embedded control symbols with all possible values for stype1
542	Part6 - 5.6.1	CG_PL_TX	CP_PL_PACKET_DELIMIT_SEQ	BFM transmitting sequence to terminate a packet a) start_of_packet -> end_of_packet b) start_of_packet -> start_of_packet c) start_of_packet -> stomp d) start_of_packet -> restart_from_retry e) start_of_packet -> link_request
543	Part6 - 5.6.2	CG_PL_TX	CP_PL_ACK_ID_SEQ	BFM transmitting the following ACK_ID values in sequence a) 0 -> 1 b) 1 -> 0
544	Part6 - 5.9.1	CG_PL_TX	CP_PL_BFM_RCVR_FLW_CTRL	BFM transmitting the control symbols with buf status field as 1 to indicate receiver based flow control
545	Part6 - 5.9.1	CG_PL_RX	CP_PL_DUT_RCVR_FLW_CTRL	BFM receiving the control symbols with buf status field as 1 to indicate that DUT is operating in receiver based flow control mode
546	Part6 - 5.9.1.4	CG_PL_INPUT_PORT_RETRY_STATE	CP_PL_INPUT_PORT_RETRY_STATE	Coverage for BFM transitioning to all possible states in input port retry state machine
547	Part6 - 5.9.1.4	CG_PL_INPUT_PORT_RETRY_STATE	CP_PL_INPUT_PORT_RETRY_TRANSITION	Coverage for BFM making 0 to 1 and 1 to 0 transition in input port retry state machine

S.No	Spec.Ref	Cover Group	Cover Points	Description
548	Part6 - 5.9.1.4	CG_PL_INPUT_PORT_RETRY_STATE	CP_PL_RESET	Coverage for assertion and de-assertion of reset signal
549	Part6 - 5.9.1.4	CG_PL_INPUT_PORT_RETRY_STATE	CR_PL_INPUT_PORT_RETRY_STATE_RESET	cross CP_PL_INPUT_PORT_RETRY_STATE, CP_PL_RESET
550	Part6 - 5.9.1.5	CG_PL_OUTPUT_PORT_RETRY_STATE	CP_PL_OUTPUT_PORT_RETRY_STATE	Coverage for BFM transitioning to all possible states in output port retry state machine
551	Part6 - 5.9.1.5	CG_PL_OUTPUT_PORT_RETRY_STATE	CP_PL_OUTPUT_PORT_RETRY_TRANSITION	Coverage for BFM making 0 to 1 and 1 to 0 transition in output port retry state machine
552	Part6 - 5.9.1.5	CG_PL_OUTPUT_PORT_RETRY_STATE	CP_PL_RESET	Coverage for assertion and de-assertion of reset signal
553	Part6 - 5.9.1.5	CG_PL_OUTPUT_PORT_RETRY_STATE	CR_PL_OUTPUT_PORT_RETRY_STATE_RESET	cross CP_PL_OUTPUT_PORT_RETRY_STATE, CP_PL_RESET
554	Part6 - 5.13.2.3.1	CG_PL_TX_SEQ	CP_PL_UNEXPECTED_PACKET_ACCEPTED	BFM transmitting unexpected packet accepted control symbol
555	Part6 - 5.13.2.3.1	CG_PL_TX_SEQ	CP_PL_UNEXPECTED_PACKET_NA	BFM transmitting unexpected packet not accepted control symbol
556	Part6 - 5.13.2.3.1	CG_PL_TX_SEQ	CP_PL_ACK_CORRUPT_PACKET_ACKID	BFM transmitting packet acknowledgement with corrupted packet_ackID
557	Part6 - 5.13.2.4	CG_PL_TX_SEQ	CP_PL_BFM_TX_PACKET_ERR_INVALID_ACKID	BFM transmitting packets with unexpected ackID.
558	Part6 - 5.13.2.6	CG_PL_INPUT_PORT_ERROR_STATE	CP_PL_INPUT_PORT_ERROR_STATE	Coverage for BFM transitioning to all possible states in input port error state machine
559	Part6 - 5.13.2.6	CG_PL_INPUT_PORT_ERROR_STATE	CP_PL_INPUT_PORT_ERROR_TRANSITION	Coverage for BFM making 0 to 1 and 1 to 0 transition in input port error state machine
560	Part6 - 5.13.2.6	CG_PL_OUTPUT_PORT_ERROR_STATE	CP_PL_OUTPUT_PORT_ERROR_STATE	Coverage for BFM transitioning to all possible states in input port error state machine
561	Part6 - 5.13.2.6	CG_PL_OUTPUT_PORT_ERROR_STATE	CP_PL_OUTPUT_PORT_ERROR_TRANSITION	Coverage for BFM making 0 to 1 and 1 to 0 transition in input port error state machine
562	GEN3 Part6 - 3.1	CG_PL_TX	CP_PL_TX_IDLE3	BFM transmitting IDLE3 sequences.
563	GEN3 Part6 - 3.1	CG_PL_RX	CP_PL_RX_IDLE3	BFM receiving IDLE3 sequences.
564	GEN3 Part6 - 3.4	CG_PL_TX	CP_PL_TIMESTAMP_SUPPORT	Coverage for BFM supporting Timestamp function
565	GEN3 Part6 - 3.4	CG_PL_TX	CR_PL_TIMESTAMP_SUPPORT_CS_TYPE	cross CP_PL_TIMESTAMP_SUPPORT, CP_PL_TX_CS_TYPE
566	GEN3 Part6 - 3.4.4	CG_PL_TX	CP_PL_TIMESTAMP_MASTER_SUPPORT	Coverage for assertion and de-assertion of Timestamp Master Support bit in Timestamp CAR register of DUT

S.No	Spec.Ref	Cover Group	Cover Points	Description
567	GEN3 Part6 - 3.4.4	CG_PL_TX	CP_PL_TIMESTAMP_SLAVE_SUPPORT	Coverage for assertion and de-assertion of Timestamp Slave Support bit in Timestamp CAR register of DUT
568	GEN3 Part6 - 3.4.4	CG_PL_RX	CR_BFM_RX_CS_TIMESTAMP_MASTER_SUPPORT_SLAVE_SUPPORT	cross CP_PL_DUT_TIMESTAMP_MASTER_SUPPORT, CP_PL_DUT_TIMESTAMP_SLAVE_SUPPORT
569	GEN3 Part6 - 3.4.4	CG_PL_TX	CR_BFM_TX_CS_TIMESTAMP_MASTER_SUPPORT_SLAVE_SUPPORT	cross CP_PL_DUT_TIMESTAMP_MASTER_SUPPORT, CP_PL_DUT_TIMESTAMP_SLAVE_SUPPORT
570	GEN3 Part6 - 3.4.7	CG_PL_TX	CP_BFM_TX_LINK_RESPONSE_INPUT_PORT_STATUS	Coverage for BFM transmitting packets with all possible values for Input Port Status field in Link Response Control Symbol
571	GEN3 Part6 - 3.4.7	CG_PL_TX	CP_BFM_TX_LINK_RESPONSE_OUTPUT_PORT_STATUS	Coverage for BFM transmitting packets with all possible values for Output Port Status field in Link Response Control Symbol
572	GEN3 Part6 - 3.4.7	CG_PL_RX	CP_BFM_RX_LINK_RESPONSE_INPUT_PORT_STATUS	Coverage for BFM transmitting packets with all possible values for Input Port Status field in Link Response Control Symbol
573	GEN3 Part6 - 3.4.7	CG_PL_RX	CP_BFM_RX_LINK_RESPONSE_OUTPUT_PORT_STATUS	Coverage for BFM receiving packets with all possible values for Output Port Status field in Link Response Control Symbol
574	GEN3 Part6 - 3.5.1	CG_PL_TX	CP_BFM_TX_SOP_UNPADDED	BFM transmitting Start Of Packet unpadded Control Symbol
575	GEN3 Part6 - 3.5.1	CG_PL_TX	CP_BFM_TX_SOP_PADDED	BFM transmitting Start Of Packet padded Control Symbol
576	Part6 - 3.5.5.2	CG_PL_TX	CP_PL_TX_RESET_PORT_CMD_B2B	BFM transmitting back to back reset port command with the following combinations a) 4 b2b reset port cmds. b) 3 b2b reset port cmds followed by any control symbol other than status control symbol and followed by 1 reset port command. c) 2 b2b reset port cmds followed by any control symbol other than status control symbol and followed by 2 b2b reset port cmds. d) 1 reset port cmds followed by any control symbol other than status control symbol and followed by 3 b2b reset port cmds. e) 3 b2b reset port cmds followed by status control symbol and followed by 1 reset port command. f) 2 b2b reset port cmds followed by status control symbol and followed by 1 reset port command. g) 1 reset port cmds followed by status control symbol and followed by 3 reset device command. h) 4 reset port cmds separated by 4 status control symbols in between each of them.
577	GEN3 Part6 - 3.5.1	CG_PL_RX	CP_BFM_RX_SOP_UNPADDED	BFM receiving Start Of Packet unpadded Control Symbol

S.No	Spec.Ref	Cover Group	Cover Points	Description
578	GEN3 Part6 - 3.5.1	CG_PL_RX	CP_BFM_RX_SOP_P ADDED	BFM receiving Start Of Packet padded Control Symbol
579	Part6 - 5.5	CG_GEN3_PL_TX_ CODE_WORD_LAN En	CP_PL_TX_TYPE_NO T_TYPE	BFM transmitting packets with all possible values for type and !type field In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
580	Part6 - 5.5	CG_GEN3_PL_TX_ CODE_WORD_LAN En	CP_PL_TX_INVERTE D	BFM transmitting packets with all possible values for inverted field In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
581	Part6 - 5.5	CG_GEN3_PL_TX_ CODE_WORD_LAN En	CR_PL_TX_TYPE_NO T_TYPE_INVERTED	cross CP_PL_TX_TYPE_NOT_TYPE. CP_PL_TX_INVERTED In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
582	Part6 - 5.5.3	CG_GEN3_PL_TX_ CODE_WORD_LAN En	CP_PL_TX_CC_TYPE	BFM transmitting packets with all possible values for CC type field In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
583	Part6 - 5.5.3	CG_GEN3_PL_TX_ CODE_WORD_LAN En	CP_PL_TX_DATA_TY PE	BFM transmitting packets with all possible values of Data type field In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
584	Part6 - 5.5.3.6	CG_GEN3_PL_TX_ CODE_WORD_LAN En	CP_PL_TX_CSB_DAT A_00	BFM transmitting data bytes with 0x00 in CSB Control Codeword In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
585	Part6 - 5.5.3.6	CG_GEN3_PL_TX_ CODE_WORD_LAN En	CP_PL_TX_CSE_DAT A_00	BFM transmitting data bytes with 0x00 in CSE Control Codeword In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
586	Part6 - 5.5.3	CG_GEN3_PL_TX_ CODE_WORD_LAN En	CR_PL_TX_CC_TYPE _DATA_TYPE	BFM transmitting packets with all possible values of Data type field In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
587	Part6 - 5.5	CG_GEN3_PL_RX_ CODE_WORD_LAN En	CP_PL_RX_TYPE_NO T_TYPE	BFM receiving packets with all possible values for type and !type field In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
588	Part6 - 5.5	CG_GEN3_PL_RX_ CODE_WORD_LAN En	CP_PL_RX_INVERTE D	BFM receiving packets with all possible values for inverted field In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
589	Part6 - 5.5	CG_GEN3_PL_RX_ CODE_WORD_LAN En	CR_PL_RX_TYPE_N OT_TYPE_INVERTED	cross CP_PL_RX_TYPE_NOT_TYPE. CP_PL_RX_INVERTED In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
590	Part6 - 5.5.3	CG_GEN3_PL_RX_ CODE_WORD_LAN En	CP_PL_RX_CC_TYPE	BFM receiving packets with all possible values for CC type field In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15

S.No	Spec.Ref	Cover Group	Cover Points	Description
591	Part6 - 5.5.3	CG_GEN3_PL_RX_CODE_WORD_LANE	CP_PL_RX_DATA_TY PE	BFM receiving packets with all possible values of Data type field In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
592	Part6 - 5.5.3	CG_GEN3_PL_RX_CODE_WORD_LANE	CR_PL_RX_CC_TYPE _DATA_TYPE	BFM receiving packets with all possible values of Data type field In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
593	Part6 - 5.5.3.6	CG_GEN3_PL_RX_CODE_WORD_LANE	CP_PL_RX_CSB_DATA_00	BFM receiving data bytes with 0x00 in CSB Control Codeword In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
594	Part6 - 5.5.3.6	CG_GEN3_PL_RX_CODE_WORD_LANE	CP_PL_RX_CSE_DATA_00	BFM receiving data bytes with 0x00 in CSE Control Codeword In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
595	Part6 - 5.5.3.4	CG_GEN3_PL_TX_OS_LANE _n	CP_PL_TX_SKIP_OS	BFM transmitting SKIP Ordered Sequence In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
596	Part6 - 5.5.3.4	CG_GEN3_PL_TX_OS_LANE _n	CP_PL_TX_INCORRECT_SKIP	BFM transmitting Incorrect SKIP Ordered Sequence In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
597	Part6 - 5.5.4	CG_GEN3_PL_TX_OS_LANE _n	CP_PL_TX_SEED_OS	BFM transmitting seed ordered sequence In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
598	Part6 - 5.8	CG_GEN3_PL_TX_OS_LANE _n	CP_PL_TX_MULTI_LANE_OS_NONALIGN	BFM transmitting different Ordered Sets on different lanes Note: Error Scenario
599	Part6 - 5.8.2	CG_GEN3_PL_TX_OS_LANE _n	CP_PL_LANE_SYNC_N	Coverage for assertion and de-assertion of lane_sync[n] variable In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
600	Part6 - 5.8.2	CG_GEN3_PL_TX_OS_LANE _n	CP_PL_TX_STATUS_CONTROL_OS	Coverage for transmitting status/control ordered sequence In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
601	Part6 - 5.5.3.4	CG_GEN3_PL_RX_OS_LANE _n	CP_PL_RX_SKIP_OS	BFM receiving SKIP Ordered Sequence In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
602	Part6 - 5.5.4	CG_GEN3_PL_RX_OS_LANE _n	CP_PL_RX_SEED_OS	BFM receiving seed ordered sequence In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
603	Part6 - 5.8.2	CG_GEN3_PL_RX_OS_LANE _n	CP_PL_LANE_SYNC_N	Coverage for assertion and de-assertion of lane_sync[n] variable In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15
604	Part6 - 5.8.2	CG_GEN3_PL_RX_OS_LANE _n	CP_PL_RX_STATUS_CONTROL_OS	Coverage for receiving status/control ordered sequence In CG, LANE _n represents the lane number, where 'n' ranges from 0 to 15

S.No	Spec.Ref	Cover Group	Cover Points	Description
605	Part6 - 5.6	CG_GEN3_PL_TERMINATING_PKT_LENGTH2	CP_PL_TX_PACKET_BOUNDARY	BFM transmitting packets terminating at non-8byte boundary
606	Part6 - 5.5.4	CG_GEN3_PL_TX_SEEDOS_LINKREQ_SEQ	CP_PL_TX_SEED_OS_LINK_REQUEST_CS	BFM transmitting link request Control Symbols with seed ordered sequence seed_os -> link_request
607	Part6 - 5.5.4	CG_GEN3_PL_TX_SEEDOS_LINKREQ_SEQ	CP_PL_LANE_WIDTH	BFM transmits in all possible lane widths
608	Part6 - 5.5.4	CG_GEN3_PL_TX_SEEDOS_LINKREQ_SEQ	CR_PL_SEEDOS_LINKREQ_LANEWIDTH	Cross CP_PL_TX_SEED_OS_LINK_REQUEST_CS, CP_PL_LANE_WIDTH
609	Part6 - 5.6	CG_GEN3_PL_CRC	CP_PL_TX_CORRUPT_CRC32	BFM transmitting packets with corrupted crc32 values.
610	Part6 - 5.6	CG_GEN3_PL_LENGTH	CP_PL_TX_PACKET_LENGTH	BFM transmitting packets of length with/without multiple of 8 bytes a) length = multiple of 8 bytes b) length = not multiple of 8 bytes
611	Part6 - 5.6	CG_GEN3_PL_TERMINATING_PKT_LENGTH1	CP_PL_TX_PACKET_BOUNDARY	BFM transmitting packets terminating at 8 byte boundary
612	Part6 - 5.10.2	CG_GEN3_PL_TX_AET_TAP_CMDSTS_LANE	CP_PL_TX_EQUALIZER_TAP	BFM transmitting all possible values for Transmit Equalizer Tap
613	Part6 - 5.10.2.2	CG_GEN3_PL_TX_AET_TAP_CMDSTS_LANE	CP_PL_TX_EQUALIZER_CMD	BFM transmitting all possible values for Transmit Equalizer Command field
614	Part6 - 5.10.2.2	CG_GEN3_PL_TX_AET_TAP_CMDSTS_LANE	CP_PL_TX_EQUALIZER_STATUS	BFM transmitting all possible values for Transmit Equalizer Status field
615	Part6 - 5.10.2.2	CG_GEN3_PL_TX_AET_TAP_CMDSTS_LANE	CP_PL_TX_EQUALIZER_CMD_STATUS	BFM receiving all possible Rx status for the transmitted Tx CMD
616	Part6 - 5.10.2.2	CG_GEN3_PL_TX_AET_LANE	CP_PL_TX_LANE	BFM transmitting lane ready in the particular lane In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
617	Part6 - 5.10.2.3	CG_GEN3_PL_TX_AET_LANE	CP_PL_LANE_DEGRADED_N	Coverage for the assertion/de-assertion lane degraded signal for a particular lane In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
618	Part6 - 5.10.2.3	CG_GEN3_PL_TX_AET_LANE	CP_PL_LANE_TRAIN_ENABLE_N	Coverage for assertion/de-assertion lane trained signal for a particular lane In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
619	Part6 - 5.10.2.3	CG_GEN3_PL_TX_AET_LANE	CP_PL_10G_RETRAIN_ENABLE_N	Coverage for the assertion/de-assertion of 10G Retrain Enable signal In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15

S.No	Spec.Ref	Cover Group	Cover Points	Description
620	Part6 - 5.10.2.3	CG_GEN3_PL_TX_AET_LANEn	CR_PL_LANE_DEGRADED_TRAINED_10G_RETRAIN_ENABLE_N	cross CP_PL_LANE_DEGRADED_N, CP_PL_LANE_TRAINED_N, CP_PL_10G_RETRAIN_ENABLE_N In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
621	Part6 - 5.10.2	CG_GEN3_PL_RX_AET_TAP_CMDSTS_LANEn	CP_PL_RX_EQUALIZER_TAP	BFM receiving all possible values for Transmit Equalizer Tap
622	Part6 - 5.10.2.2	CG_GEN3_PL_RX_AET_TAP_CMDSTS_LANEn	CP_PL_RX_EQUALIZER_CMD	BFM receiving all possible values for Transmit Equalizer Command field
623	Part6 - 5.10.2.2	CG_GEN3_PL_RX_AET_TAP_CMDSTS_LANEn	CP_PL_RX_EQUALIZER_STATUS	BFM receiving all possible values for Transmit Equalizer Status field
624	Part6 - 5.10.2.2	CG_GEN3_PL_RX_AET_TAP_CMDSTS_LANEn	CR_PL_RX_EQUALIZER_CMD_STATUS	BFM transmitting all possible Tx status for the received Rx CMD
625	Part6 - 5.10.2.2	CG_GEN3_PL_RX_AET_LANEn	CP_PL_RX_LANE	BFM receiving lane ready in the particular lane In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
626	Part6 - 5.10.2.3	CG_GEN3_PL_RX_AET_LANEn	CP_PL_LANE_DEGRADED_N	Coverage for the assertion/de-assertion lane degraded signal for a particular lane In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
627	Part6 - 5.10.2.3	CG_GEN3_PL_RX_AET_LANEn	CP_PL_LANE_TRAINED_N	Coverage for assertion/de-assertion lane trained signal for a particular lane In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
628	Part6 - 5.10.2.3	CG_GEN3_PL_RX_AET_LANEn	CP_PL_10G_RETRAIN_ENABLE_N	Coverage for the assertion/de-assertion of 10G Retrain Enable signal In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
629	Part6 - 5.10.2.3	CG_GEN3_PL_RX_AET_LANEn	CR_PL_LANE_DEGRADED_TRAINED_10G_RETRAIN_ENABLE_N	cross CP_PL_LANE_DEGRADED_N, CP_PL_LANE_TRAINED_N, CP_PL_10G_RETRAIN_ENABLE_N In CG, LANEn represents the lane number, where 'n' ranges from 0 to 15
630	Part6 - 5.16	CG_GEN3_PL_ASYMMETRY	CP_PL_ASYMMETRY_MODE	Coverage for DUT working Asymmetric mode
631	Part6 - 5.16.1.1	CG_GEN3_PL_ASYMMETRY	CP_PL_TX_WIDTH_PORT_REQ	BFM transmitting all possible Transmit Width Port Request
632	Part6 - 5.16.1.1	CG_GEN3_PL_ASYMMETRY	CR_PL_TX_WIDTH_PORT_CMD_ASYM	BFM transmitting all possible Transmit Width Port Request in asymmetric mode
633	Part6 - 5.17.3.1	CG_GEN3_PL_LONG_RUN_LINK_TRAIN_NEXT_STATE_SM	CP_PL_LONG_RUN_LINK_TRAIN_NEXT_STATE	BFM transitioning to following states a) UNTRAINED b) DME_TRAINING0 c) DME_TRAINING1 d) DME_TRAINING2 e) DME_TRAINING_FAIL f) TRAINED

S.No	Spec.Ref	Cover Group	Cover Points	Description
634	Part6 - 5.17.3.1	CG_GEN3_PL_LO NG_RUN_LINK_TRAIN_SM	CP_PL_LONG_RUN_LINK_TRAIN_TO_UNTRAINED	BFM making the following transitions a) UNTRAINED -> UNTRAINED b) DME_TRAINING1 -> UNTRAINED c) DME_TRAINING_FAIL -> UNTRAINED
635	Part6 - 5.17.3.1	CG_GEN3_PL_LO NG_RUN_LINK_TRAIN_SM	CP_PL_LONG_RUN_LINK_TRAIN_TO_DME_TRAINING0	BFM making the following transitions a) UNTRAINED -> DME_TRAINING0
636	Part6 - 5.17.3.1	CG_GEN3_PL_LO NG_RUN_LINK_TRAIN_SM	CP_PL_LONG_RUN_LINK_TRAIN_TO_DME_TRAINING1	BFM making the following transitions a) DME_TRAINING0 -> DME_TRAINING1 b) DME_TRAINING2 -> DME_TRAINING1
637	Part6 - 5.17.3.1	CG_GEN3_PL_LO NG_RUN_LINK_TRAIN_SM	CP_PL_LONG_RUN_LINK_TRAIN_TO_DME_TRAINING2	BFM making the following transitions a) DME_TRAINING1 -> DME_TRAINING2
638	Part6 - 5.17.3.1	CG_GEN3_PL_LO NG_RUN_LINK_TRAIN_SM	CP_PL_LONG_RUN_LINK_TRAIN_TO_DME_TRAINING_FAIL	BFM making the following transitions a) DME_TRAINING0 -> DME_TRAINING_FAIL b) DME_TRAINING1 -> DME_TRAINING_FAIL
639	Part6 - 5.17.3.1	CG_GEN3_PL_LO NG_RUN_LINK_TRAIN_SM	CP_PL_LONG_RUN_LINK_TRAIN_TO_TRAINED	BFM making the following transitions a) DME_TRAINING2 -> TRAINED
640	Part6 - 5.17.3.1	CG_GEN3_PL_LO NG_RUN_LINK_TRAIN_SM	CP_PL_LONG_RUN_LINK_TRAIN_PATH_TRANSITIONS	BFM making the following transitions 1) DME_TRAINING_0 => DME_TRAINING_FAIL => UNTRAINED 2) DME_TRAINING_0 => DME_TRAINING_1 => DME_TRAINING_FAIL 3) DME_TRAINING_0 => DME_TRAINING_1 => UNTRAINED 4) DME_TRAINING_0 => DME_TRAINING_1 => DME_TRAINING_2 => DME_TRAINING_1
641	Part6 - 5.17.3.2	CG_GEN3_PL_SHORT_RUN_LINK_TRAIN_SM	CP_PL_SHORT_RUN_LINK_TRAIN_NEXT_STATE	BFM transitioning to following states a) UNTRAINED b) CW_TRAINING0 c) CW_TRAINING1 d) CW_TRAINING_FAIL e) TRAINED f) KEEP_ALIVE g) RETRAINING0 h) RETRAINING1 i) RETRAINING2 j) RETRAIN_FAIL
642	Part6 - 5.17.3.2	CG_GEN3_PL_SHORT_RUN_LINK_TRAIN_SM	CP_PL_SHORT_RUN_LINK_TRAIN_TO_UNTRAINED	BFM making the following transitions a) UNTRAINED -> UNTRAINED b) CW_TRAINING1 -> UNTRAINED c) CW_TRAINING_FAIL -> UNTRAINED
643	Part6 - 5.17.3.2	CG_GEN3_PL_SHORT_RUN_LINK_TRAIN_SM	CP_PL_SHORT_RUN_LINK_TRAIN_TO_CW_TRAINING0	BFM making the following transitions a) UNTRAINED -> CW_TRAINING0
644	Part6 - 5.17.3.2	CG_GEN3_PL_SHORT_RUN_LINK_TRAIN_SM	CP_PL_SHORT_RUN_LINK_TRAIN_TO_CW_TRAINING1	BFM making the following transitions a) CW_TRAINING0 -> CW_TRAINING1

S.No	Spec.Ref	Cover Group	Cover Points	Description
645	Part6 - 5.17.3.2	CG_GEN3_PL_SH ORT_RUN_LINK_T RAIN_SM	CP_PL_SHORT_RUN _LINK_TRAIN_TO_C W_TRAINING_FAIL	BFM making the following transitions a) CW_TRAINING0 -> CW_TRAINING_FAIL b) CW_TRAINING1 -> CW_TRAINING_FAIL
646	Part6 - 5.17.3.2	CG_GEN3_PL_SH ORT_RUN_LINK_T RAIN_SM	CP_PL_SHORT_RUN _LINK_TRAIN_TO_TR AINED	BFM making the following transitions a) CW_TRAINING1 -> TRAINED b) KEEP_ALIVE -> TRAINED c) RETRAINING2 -> TRAINED
647	Part6 - 5.17.3.2	CG_GEN3_PL_SH ORT_RUN_LINK_T RAIN_SM	CP_PL_SHORT_RUN _LINK_TRAIN_TO_KE EP_ALIVE	BFM making the following transitions a) TRAINED -> KEEP_ALIVE
648	Part6 - 5.17.3.2	CG_GEN3_PL_SH ORT_RUN_LINK_T RAIN_SM	CP_PL_SHORT_RUN _LINK_TRAIN_TO_RE TRAINING0	BFM making the following transitions a) TRAINED -> RETRAINING0 b) KEEP_ALIVE -> RETRAINING0
649	Part6 - 5.17.3.2	CG_GEN3_PL_SH ORT_RUN_LINK_T RAIN_SM	CP_PL_SHORT_RUN _LINK_TRAIN_TO_RE TRAINING1	BFM making the following transitions a) RETRAINING0 -> RETRAINING1
650	Part6 - 5.17.3.2	CG_GEN3_PL_SH ORT_RUN_LINK_T RAIN_SM	CP_PL_SHORT_RUN _LINK_TRAIN_TO_RE TRAINING2	BFM making the following transitions a) RETRAINING1 -> RETRAINING2
651	Part6 - 5.17.3.2	CG_GEN3_PL_SH ORT_RUN_LINK_T RAIN_SM	CP_PL_SHORT_RUN _LINK_TRAIN_TO_RE TRAIN_FAIL	BFM making the following transitions a) RETRAINING0 -> RETRAIN_FAIL b) RETRAINING1 -> RETRAIN_FAIL c) RETRAINING2 -> RETRAIN_FAIL
652	Part6 - 5.17.3.2	CG_GEN3_PL_SH ORT_RUN_LINK_T RAIN_SM	CP_PL_SHORT_RUN _LINK_TRAIN_PATH_ TRANSITIONS	BFM making the following transitions 1) CW_TRAINING_0 => CW_TRAINING_FAIL => UNTRAINED 2) CW_TRAINING_0 => CW_TRAINING_1 => TRAINED 3) TRAINED => KEEP_ALIVE => TRAINED 4) TRAINED => RETRAINING_0 => RETRAIN_FAIL 5) TRAINED => RETRAINING_0 => RETRAINING_1 => RETRAIN_FAIL 6) TRAINED => RETRAINING_0 => RETRAINING_1 => RETRAINING_2 => RETRAIN_FAIL 7) TRAINED => RETRAINING_0 => RETRAINING_1 => RETRAINING_2 => TRAINED

S.No	Spec.Ref	Cover Group	Cover Points	Description
653	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_N EXT_STATE	BFM transitioning to the following states a) NO_LOCK b) SLIP_ALIGNMENT c) NO_LOCK1 d) NO_LOCK2 e) NO_LOCK3 f) LOCK g) LOCK1 h) LOCK2 i) LOCK3
654	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_T O_NO_LOCK	BFM making the following transitions a) NO_LOCK -> NO_LOCK b) LOCK2 -> NO_LOCK
655	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_T O_NO_LOCK1	BFM making the following transitions a) NO_LOCK -> NO_LOCK1 b) SLIP_ALIGNMENT -> NO_LOCK1
656	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_T O_NO_LOCK2	BFM making the following transitions a) NO_LOCK1 -> NO_LOCK2 b) NO_LOCK3 -> NO_LOCK2
657	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_T O_NO_LOCK3	BFM making the following transitions a) NO_LOCK2 -> NO_LOCK3
658	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_T O_LOCK	BFM making the following transitions a) NO_LOCK3 -> LOCK b) LOCK3 -> LOCK
659	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_T O_LOCK1	BFM making the following transitions a) LOCK -> LOCK1 b) LOCK2 -> LOCK1 c) LOCK3 -> LOCK1
660	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_T O_LOCK2	BFM making the following transitions a) LOCK1 -> LOCK2
661	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_T O_LOCK3	BFM making the following transitions a) LOCK1 -> LOCK3
662	Part6 - 5.17.4	CG_GEN3_PL_CW_LOCK_SM	CP_PL_CW_LOCK_P ATH_TRANSITIONS	BFM making the following transitions 1) NO_LOCK => NO_LOCK_1 => NO_LOCK_2 => SLIP_ALIGNMENT 2) NO_LOCK => NO_LOCK_1 => NO_LOCK_2 => NO_LOCK_3 3) NO_LOCK_2 => NO_LOCK_3 => NO_LOCK_2 4) NO_LOCK_2 => NO_LOCK_3 => LOCK 5) LOCK => LOCK_1 => LOCK_2 => LOCK_1 6) LOCK => LOCK_1 => LOCK_3 => LOCK_1 7) LOCK_1 => LOCK_2 => NO_LOCK 8) LOCK_1 => LOCK_3 => LOCK

S.No	Spec.Ref	Cover Group	Cover Points	Description
663	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_NEXT_STATE	BFM transitioning to the following states a) NO_SYNC b) NO_SYNC1 c) NO_SYNC2 d) NO_SYNC3 e) NO_SYNC4 f) SYNC g) SYNC1 h) SYNC2
664	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_RESET	coverage for assertion and de-assertion of reset signal
665	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_CW_LOCK	coverage for assertion and de-assertion of cw_lock signal for the corresponding lane
666	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CR_PL_GEN3_LANE_SYNC_NEXT_STATE_RESET	cross CP_PL_GEN3_LANE_SYNC_NEXT_STATE, CP_PL_RESET
667	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CR_PL_GEN3_LANE_SYNC_NEXT_STATE_CW_LOCK	cross CP_PL_GEN3_LANE_SYNC_NEXT_STATE, CP_PL_CW_LOCK
668	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_TO_NO_SYNC	BFM making the following transitions a) NO_SYNC -> NO_SYNC
669	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_TO_NO_SYNC_1	BFM making the following transitions a) NO_SYNC -> NO_SYNC1 b) NO_SYNC3 -> NO_SYNC1
670	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_TO_NO_SYNC_2	BFM making the following transitions a) NO_SYNC1 -> NO_SYNC2 b) NO_SYNC3 -> NO_SYNC2 c) NO_SYNC4 -> NO_SYNC2
671	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_TO_NO_SYNC_3	BFM making the following transitions a) NO_SYNC2 -> NO_SYNC3
672	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_TO_NO_SYNC_4	BFM making the following transitions a) NO_SYNC3 -> NO_SYNC4
673	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_TO_SYNC	BFM making the following transitions a) NO_SYNC4 -> SYNC
674	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_TO_SYNC1	BFM making the following transitions a) SYNC -> SYNC1
675	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_TO_SYNC2	BFM making the following transitions a) SYNC1 -> SYNC2
676	Part6 - 5.17.5	CG_GEN3_PL_LANE_SYNC_SM	CP_PL_GEN3_LANE_SYNC_PATH_TRANSITIONS	1) NO_SYNC => NO_SYNC_1 => NO_SYNC_2 => NO_SYNC_3 2) NO_SYNC_2 => NO_SYNC_3 => NO_SYNC_2 3) NO_SYNC_2 => NO_SYNC_3 => NO_SYNC_1 4) NO_SYNC_2 => NO_SYNC_3 => NO_SYNC_4 5) NO_SYNC_2 => NO_SYNC_3 => NO_SYNC_4 => NO_SYNC_2 6) NO_SYNC_2 => NO_SYNC_3 => NO_SYNC_4 => SYNC 7) SYNC => SYNC_1 => SYNC_2

S.No	Spec.Ref	Cover Group	Cover Points	Description
677	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_NEXT_STATE	Coverage for BFM transitioning to the following states a) NOT_ALIGNED b) NOT_ALIGNED1 c) NOT_ALIGNED2 d) NOT_ALIGNED3 e) ALIGNED f) ALIGNED1 g) ALIGNED2 h) ALIGNED3 i) ALIGNED4 j) ALIGNED5 k) ALIGNED6 l) ALIGNED7
678	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_RESET	coverage for assertion and de-assertion of reset signal
679	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CR_PL_LANE_ALIGN_2X_NEXT_STATE_RESET	cross CP_PL_LANE_ALIGN_2X_NEXT_STATE, CP_PL_RESET
680	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_NA	BFM making the following transitions a) NOT_ALIGNED -> NOT_ALIGNED b) NOT_ALIGNED2 -> NOT_ALIGNED c) ALIGNED3 -> NOT_ALIGNED
681	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_NA1	BFM making the following transitions a) NOT_ALIGNED -> NOT_ALIGNED1 b) NOT_ALIGNED3 -> NOT_ALIGNED1 c) NOT_ALIGNED1 -> NOT_ALIGNED1
682	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_NA2	BFM making the following transitions a) NOT_ALIGNED1 -> NOT_ALIGNED2
683	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_NA3	BFM making the following transitions a) NOT_ALIGNED2 -> NOT_ALIGNED3
684	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_A	BFM making the following transitions a) NOT_ALIGNED3 -> ALIGNED b) ALIGNED1 -> ALIGNED c) ALIGNED7 -> ALIGNED d) ALIGNED -> ALIGNED
685	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_A1	BFM making the following transitions a) ALIGNED -> ALIGNED1
686	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_A2	BFM making the following transitions a) ALIGNED -> ALIGNED2
687	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_A3	BFM making the following transitions a) ALIGNED1 -> ALIGNED3 b) ALIGNED2 -> ALIGNED3 c) ALIGNED5 -> ALIGNED3 d) ALIGNED6 -> ALIGNED3

S.No	Spec.Ref	Cover Group	Cover Points	Description
688	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_A4	BFM making the following transitions a) ALIGNED3 -> ALIGNED4 b) ALIGNED7 -> ALIGNED4 c) ALIGNED4 -> ALIGNED4
689	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_A5	BFM making the following transitions a) ALIGNED4 -> ALIGNED5
690	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_A6	BFM making the following transitions a) ALIGNED4 -> ALIGNED6
691	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_A7	BFM making the following transitions a) ALIGNED5 -> ALIGNED7
692	Part6 - 5.17.6	CG_GEN3_PL_2X_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_2X_TO_A7	BFM making the following transitions 1) NOT_ALIGNED => NOT_ALIGNED_1 => NOT_ALIGNED_2 2) NOT_ALIGNED_1 => NOT_ALIGNED_2 => NOT_ALIGNED 3) NOT_ALIGNED_1 => NOT_ALIGNED_2 => NOT_ALIGNED_3 => NOT_ALIGNED_1 4) NOT_ALIGNED_1 => NOT_ALIGNED_2 => NOT_ALIGNED_3 => ALIGNED 5) ALIGNED => ALIGNED_1 => ALIGNED 6) ALIGNED => ALIGNED_1 => ALIGNED_3 => ALIGNED_4 7) ALIGNED => ALIGNED_2 => ALIGNED_3 => ALIGNED_4 8) ALIGNED_4 => ALIGNED_5 => ALIGNED_7 => ALIGNED_4 9) ALIGNED_4 => ALIGNED_5 => ALIGNED_3 => ALIGNED_4 10) ALIGNED_4 => ALIGNED_5 => ALIGNED_3 => NOT_ALIGNED 11) ALIGNED_4 => ALIGNED_6 => ALIGNED_3 => ALIGNED_4 12) ALIGNED_4 => ALIGNED_6 => ALIGNED_3 => NOT_ALIGNED 13) ALIGNED_4 => ALIGNED_5 => ALIGNED_7 => ALIGNED
693	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_NEXT_STATE	Coverage for BFM transitioning to the following states a) NOT_ALIGNED b) NOT_ALIGNED1 c) NOT_ALIGNED2 d) NOT_ALIGNED3 e) ALIGNED f) ALIGNED1 g) ALIGNED2 h) ALIGNED3 i) ALIGNED4 j) ALIGNED5 k) ALIGNED6 l) ALIGNED7

S.No	Spec.Ref	Cover Group	Cover Points	Description
694	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_RESET	coverage for assertion and de-assertion of reset signal
695	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CR_PL_LANE_ALIGN_NX_NEXT_STATE_RESET	cross CP_PL_LANE_ALIGN_NEXT_STATE, CP_PL_RESET
696	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_NA	BFM making the following transitions a) NOT_ALIGNED -> NOT_ALIGNED b) NOT_ALIGNED2 -> NOT_ALIGNED c) ALIGNED3 -> NOT_ALIGNED
697	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_NA1	BFM making the following transitions a) NOT_ALIGNED -> NOT_ALIGNED1 b) NOT_ALIGNED3 -> NOT_ALIGNED1 c) NOT_ALIGNED1 -> NOT_ALIGNED1
698	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_NA2	BFM making the following transitions a) NOT_ALIGNED1 -> NOT_ALIGNED2
699	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_NA3	BFM making the following transitions a) NOT_ALIGNED2 -> NOT_ALIGNED3
700	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_A	BFM making the following transitions a) NOT_ALIGNED3 -> ALIGNED b) ALIGNED1 -> ALIGNED c) ALIGNED7 -> ALIGNED d) ALIGNED -> ALIGNED
701	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_A1	BFM making the following transitions a) ALIGNED -> ALIGNED1
702	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_A2	BFM making the following transitions a) ALIGNED -> ALIGNED2
703	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_A3	BFM making the following transitions a) ALIGNED1 -> ALIGNED3 b) ALIGNED2 -> ALIGNED3 c) ALIGNED5 -> ALIGNED3 d) ALIGNED6 -> ALIGNED3
704	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_A4	BFM making the following transitions a) ALIGNED3 -> ALIGNED4 b) ALIGNED7 -> ALIGNED4 c) ALIGNED4 -> ALIGNED4
705	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_A5	BFM making the following transitions a) ALIGNED4 -> ALIGNED5
706	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_A6	BFM making the following transitions a) ALIGNED4 -> ALIGNED6
707	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_TO_A7	BFM making the following transitions a) ALIGNED5 -> ALIGNED7

S.No	Spec.Ref	Cover Group	Cover Points	Description
708	Part6 - 5.17.6	CG_GEN3_PL_NX_LANE_ALIGN_SM	CP_PL_LANE_ALIGN_NX_PATH_TRANSITIONS	<p>BFM making the following transitions</p> <p>1) NOT_ALIGNED => NOT_ALIGNED_1 => NOT_ALIGNED_2</p> <p>2) NOT_ALIGNED_1 => NOT_ALIGNED_2 => NOT_ALIGNED</p> <p>3) NOT_ALIGNED_1 => NOT_ALIGNED_2 => NOT_ALIGNED_3 => NOT_ALIGNED_1</p> <p>4) NOT_ALIGNED_1 => NOT_ALIGNED_2 => NOT_ALIGNED_3 => ALIGNED</p> <p>5) ALIGNED => ALIGNED_1 => ALIGNED</p> <p>6) ALIGNED => ALIGNED_1 => ALIGNED_3 => ALIGNED_4</p> <p>7) ALIGNED => ALIGNED_2 => ALIGNED_3 => ALIGNED_4</p> <p>8) ALIGNED_4 => ALIGNED_5 => ALIGNED_7 => ALIGNED_4</p> <p>9) ALIGNED_4 => ALIGNED_5 => ALIGNED_3 => ALIGNED_4</p> <p>10) ALIGNED_4 => ALIGNED_5 => ALIGNED_3 => NOT_ALIGNED</p> <p>11) ALIGNED_4 => ALIGNED_6 => ALIGNED_3 => ALIGNED_4</p> <p>12) ALIGNED_4 => ALIGNED_6 => ALIGNED_3 => NOT_ALIGNED</p> <p>13) ALIGNED_4 => ALIGNED_5 => ALIGNED_7 => ALIGNED</p>
709	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_INIT_NEXT_STATE	<p>BFM transitioning to the following states</p> <p>a) ASYM_MODE</p> <p>b) SILENT</p> <p>c) SEEK</p> <p>d) DISCOVERY</p> <p>e) 1X_RETRAIN</p> <p>f) 1X_RECOVERY</p> <p>g) 1X_MODE_LANE0</p> <p>h) 1X_MODE_LANE1</p> <p>i) 1X_MODE_LANE2</p> <p>j) NX_RETRAIN</p> <p>k) 2X_RETRAIN</p> <p>l) NX_RECOVERY</p> <p>m) 2X_RECOVERY</p> <p>n) NX_MODE</p> <p>o) 2X_MODE</p>
710	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_RESET	coverage for assertion and de-assertion of reset signal
711	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_FORCE_REINIT	coverage for assertion and de-assertion of force_reinit signal
712	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CR_PL_1X_2X_NX_INIT_NEXT_STATE_RESET	cross CP_PL_PORT_INIT_NEXT_STATE, CP_PL_RESET

S.No	Spec.Ref	Cover Group	Cover Points	Description
713	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CR_PL_1X_2X_NX_IN IT_NEXT_STATE_FO RCE_REINIT	cross CP_PL_PORT_INIT_NEXT_STATE, CP_PL_FORCE_REINIT
714	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_ASYNC_MODE	BFM making the following transitions a) NX_MODE -> ASYM_MODE b) 2X_MODE -> ASYM_MODE
715	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_SILENT	BFM making the following transitions a) ASYM_MODE -> SILENT b) 1X_MODE_LANE0 -> SILENT c) 1X_MODE_LANE1 -> SILENT d) 1X_MODE_LANE2 -> SILENT e) 1X_RECOVERY -> SILENT f) NX_RECOVERY -> SILENT g) NX_MODE -> SILENT h) DISCOVERY -> SILENT
716	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_SEEK	BFM making the following transitions a) SILENT -> SEEK
717	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_DISCOVERY	BFM making the following transitions a) SEEK -> DISCOVERY
718	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_1X_RETRAIN	BFM making the following transitions a) 1X_RECOVERY -> 1X_RETRAIN
719	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_1X_RECOVER Y	BFM making the following transitions a) 1X_RETRAIN -> 1X_RECOVERY b) 1X_MODE_LANE0 -> 1X_RECOVERY c) 1X_MODE_LANE1 -> 1X_RECOVERY d) 1X_MODE_LANE2 -> 1X_RECOVERY
720	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_1X_MODE_LA NE0	BFM making the following transitions a) DISCOVERY -> 1X_MODE_LANE0 b) 1X_RECOVERY -> 1X_MODE_LANE0 c) NX_RECOVERY -> 1X_MODE_LANE0 d) 2X_RECOVERY -> 1X_MODE_LANE0
721	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_1X_MODE_LA NE1	BFM making the following transitions a) DISCOVERY -> 1X_MODE_LANE1 b) 1X_RECOVERY -> 1X_MODE_LANE1 c) NX_RECOVERY -> 1X_MODE_LANE1 d) 2X_RECOVERY -> 1X_MODE_LANE1
722	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_1X_MODE_LA NE2	BFM making the following transitions a) DISCOVERY -> 1X_MODE_LANE2 b) 1X_RECOVERY -> 1X_MODE_LANE2 c) NX_RECOVERY -> 1X_MODE_LANE2
723	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_NX_RETRAIN	BFM making the following transitions a) NX_RECOVERY -> NX_RETRAIN
724	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_2X_RETRAIN	BFM making the following transitions a) 2X_RECOVERY -> 2X_RETRAIN
725	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_NX_RECOVER Y	BFM making the following transitions a) NX_RETRAIN -> NX_RECOVERY b) NX_RECOVERY -> NX_RECOVERY

S.No	Spec.Ref	Cover Group	Cover Points	Description
726	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_2X_RECOVERY	BFM making the following transitions a) 2X_RETRAIN -> 2X_RECOVERY b) 2X_RECOVERY -> 2X_RECOVERY
727	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_NX_MODE	BFM making the following transitions a) DISCOVERY -> NX_MODE b) NX_RECOVERY -> NX_MODE
728	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_TO_2X_MODE	BFM making the following transitions a) DISCOVERY -> 2X_MODE b) NX_RECOVERY -> 2X_MODE c) 2X_RECOVERY -> 2X_MODE
729	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_IN IT_PATH_TRANSITIONS	BFM making the following transitions 1) SILENT => SEEK => DISCOVERY => NX_MODE 2) SILENT => SEEK => DISCOVERY => X2_MODE 3) SILENT => SEEK => DISCOVERY => X1_M0 4) SILENT => SEEK => DISCOVERY => X1_M1 5) SILENT => SEEK => DISCOVERY => X1_M2 6) SILENT => SEEK => DISCOVERY => SILENT 7) X2_MODE => X2_RECOVERY => X2_MODE 8) X2_MODE => X2_RECOVERY => SILENT 9) X2_MODE => X2_RECOVERY => X1_M0 10) X2_MODE => X2_RECOVERY => X1_M1 11) X2_MODE => SILENT 12) X1_M0 => SILENT 13) X1_M1 => SILENT 14) X1_M2 => SILENT 15) X1_M0 => X1_RECOVERY => X1_M0 16) X1_M1 => X1_RECOVERY => X1_M1 17) X1_M2 => X1_RECOVERY => X1_M2 18) X1_M0 => X1_RECOVERY => SILENT 19) X1_M1 => X1_RECOVERY => SILENT 20) X1_M2 => X1_RECOVERY => SILENT 21) NX_MODE => NX_RECOVERY => NX_MODE 22) NX_MODE => NX_RECOVERY => X2_MODE 23) NX_MODE => NX_RECOVERY => X1_M0 24) NX_MODE => NX_RECOVERY => X1_M1 25) NX_MODE => NX_RECOVERY => X1_M2 26) NX_MODE => NX_RECOVERY => SILENT

S.No	Spec.Ref	Cover Group	Cover Points	Description
730	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_1X_2X_NX_INIT_PATH_TRANSITIONS	1) NX_MODE => NX_RECOVERY => NX_RETRAIN => NX_RECOVERY => NX_MODE 2) NX_MODE => NX_RECOVERY => NX_RETRAIN => NX_RECOVERY => X2_MODE 3) NX_MODE => NX_RECOVERY => NX_RETRAIN => NX_RECOVERY => X1_M0 4) NX_MODE => NX_RECOVERY => NX_RETRAIN => NX_RECOVERY => X1_M1 5) NX_MODE => NX_RECOVERY => NX_RETRAIN => NX_RECOVERY => X1_M2 6) NX_MODE => NX_RECOVERY => NX_RETRAIN => NX_RECOVERY => SILENT 7) X2_MODE => X2_RECOVERY => X2_RETRAIN => X2_RECOVERY => X2_MODE 8) X2_MODE => X2_RECOVERY => X2_RETRAIN => X2_RECOVERY => X1_M0 9) X2_MODE => X2_RECOVERY => X2_RETRAIN => X2_RECOVERY => X1_M1 10) X2_MODE => X2_RECOVERY => X2_RETRAIN => X2_RECOVERY => SILENT 11) X1_M0 => X1_RECOVERY => X1_RETRAIN => X1_RECOVERY => X1_M0 12) X1_M1 => X1_RECOVERY => X1_RETRAIN => X1_RECOVERY => X1_M1 13) X1_M2 => X1_RECOVERY => X1_RETRAIN => X1_RECOVERY => X1_M2 14) X1_M0 => X1_RECOVERY => X1_RETRAIN => X1_RECOVERY => SILENT 15) X1_M1 => X1_RECOVERY => X1_RETRAIN => X1_RECOVERY => SILENT 16) X1_M2 => X1_RECOVERY => X1_RETRAIN => X1_RECOVERY => SILENT 17) X2_MODE => ASYM_MODE => SILENT 18) NX_MODE => ASYM_MODE => SILENT
731	Part6 - 5.17.7	CG_GEN3_PL_1X_2X_NX_PORT_INIT_SM	CP_PL_NUM_OF_LANES	Coverage for bfm working in all possible lane widths
732	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_NEXT_STATE	BFM transitioning to the following states a) IDLE b) XMT_WIDTH c) RETRAIN0 d) RETRAIN1 e) RETRAIN2 f) RETRAIN3 g) RETRAIN4 h) RETRAIN5 i) RETRAIN_TIMEOUT
733	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_TO_IDLE	BFM making the following transitions a) RETRAIN5 -> IDLE b) RETRAIN_TIMEOUT -> IDLE c) XMT_WIDTH -> IDLE

S.No	Spec.Ref	Cover Group	Cover Points	Description
734	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_TO_XMT_WIDTH	BFM making the following transitions a) IDLE -> XMT_WIDTH
735	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_TO_RETRAIN0	BFM making the following transitions a) IDLE -> RETRAIN0
736	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_TO_RETRAIN1	BFM making the following transitions a) RETRAIN0 -> RETRAIN1
737	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_TO_RETRAIN2	BFM making the following transitions a) RETRAIN1 -> RETRAIN2
738	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_TO_RETRAIN3	BFM making the following transitions a) RETRAIN2 -> RETRAIN3
739	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_TO_RETRAIN4	BFM making the following transitions a) RETRAIN3 -> RETRAIN4
740	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_TO_RETRAIN5	BFM making the following transitions a) RETRAIN4 -> RETRAIN5
741	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_TO_RETRAIN_TIMEOUT	BFM making the following transitions a) RETRAIN0 -> RETRAIN_TIMEOUT b) RETRAIN1 -> RETRAIN_TIMEOUT c) RETRAIN2 -> RETRAIN_TIMEOUT d) RETRAIN3 -> RETRAIN_TIMEOUT e) RETRAIN4 -> RETRAIN_TIMEOUT f) RETRAIN5 -> RETRAIN_TIMEOUT
742	Part6 - 5.17.8	CG_GEN3_PL_RETRAIN_TRANSMIT_WIDTH_CTRL_SM	CP_PL_RETRAIN_TX_WIDTH_CTRL_PATH_TRANSITIONS	BFM making the following transitions 1) IDLE => XMT_WIDTH => IDLE); 2) IDLE => RETRAIN_0 => RETRAIN_TIMEOUT => IDLE 3) IDLE => RETRAIN_0 => RETRAIN_1 => RETRAIN_TIMEOUT => IDLE 4) IDLE => RETRAIN_0 => RETRAIN_1 => RETRAIN_2 => RETRAIN_TIMEOUT => IDLE 5) IDLE => RETRAIN_0 => RETRAIN_1 => RETRAIN_2 => RETRAIN_3 => RETRAIN_TIMEOUT => IDLE 6) IDLE => RETRAIN_0 => RETRAIN_1 => RETRAIN_2 => RETRAIN_3 => RETRAIN_4 => RETRAIN_TIMEOUT => IDLE 7) IDLE => RETRAIN_0 => RETRAIN_1 => RETRAIN_2 => RETRAIN_3 => RETRAIN_4 => RETRAIN_5 => RETRAIN_TIMEOUT => IDLE 8) IDLE => RETRAIN_0 => RETRAIN_1 => RETRAIN_2 => RETRAIN_3 => RETRAIN_4 => RETRAIN_5 => IDLE

S.No	Spec.Ref	Cover Group	Cover Points	Description
743	Part6 - 5.17.9.1	CG_GEN3_PL_TRANSMIT_WIDTH_CMD_SM	CP_PL_TX_WIDTH_CMD_NEXT_STATE	BFM transitioning to the following states a) XMT_WIDTH_CMD2 b) XMT_WIDTH_CMD3 c) XMT_WIDTH_CMD_IDLE d) XMT_WIDTH_CMD1
744	Part6 - 5.17.9.1	CG_GEN3_PL_TRANSMIT_WIDTH_CMD_SM	CP_PL_TX_WIDTH_CMD_TO_CMD2	BFM making the following transitions a) XMT_WIDTH_CMD1 -> XMT_WIDTH_CMD2 b) XMT_WIDTH_CMD_IDLE -> XMT_WIDTH_CMD2
745	Part6 - 5.17.9.1	CG_GEN3_PL_TRANSMIT_WIDTH_CMD_SM	CP_PL_TX_WIDTH_CMD_TO_CMD3	BFM making the following transitions a) XMT_WIDTH_CMD2 -> XMT_WIDTH_CMD3
746	Part6 - 5.17.9.1	CG_GEN3_PL_TRANSMIT_WIDTH_CMD_SM	CP_PL_TX_WIDTH_CMD_TO_CMD_IDLE	BFM making the following transitions a) XMT_WIDTH_CMD3 -> XMT_WIDTH_CMD_IDLE
747	Part6 - 5.17.9.1	CG_GEN3_PL_TRANSMIT_WIDTH_CMD_SM	CP_PL_TX_WIDTH_CMD_TO_CMD1	BFM making the following transitions a) XMT_WIDTH_CMD_IDLE -> XMT_WIDTH_CMD1
748	Part6 - 5.17.9.1	CG_GEN3_PL_TRANSMIT_WIDTH_CMD_SM	CP_PL_TX_WIDTH_CMD_PATH_TRANSITIONS	BFM making the following transitions 1) XMT_WIDTH_CMD_3 => XMT_WIDTH_CMD_IDLE => XMT_WIDTH_CMD_2 => XMT_WIDTH_CMD_3 2) XMT_WIDTH_CMD_3 => XMT_WIDTH_CMD_IDLE => XMT_WIDTH_CMD_1 => XMT_WIDTH_CMD_2 => XMT_WIDTH_CMD_3
749	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_NEXT_STATE	BFM transitioning to the following states a) ASYM_XMT_EXIT b) ASYM_XMT_IDLE c) XMT_WIDTH_NACK d) SEEK_1X_MODE_XMT e) SEEK_1X_MODE_XMT1 f) SEEK_1X_MODE_XMT2 g) SEEK_1X_MODE_XMT3 h) 1X_MODE_XMT_ACK i) 1X_MODE_XMT j) SEEK_2X_MODE_XMT k) SEEK_2X_MODE_XMT1 l) SEEK_2X_MODE_XMT2 m) SEEK_2X_MODE_XMT3 n) 2X_MODE_XMT_ACK o) 2X_MODE_XMT p) SEEK_NX_MODE_XMT q) SEEK_NX_MODE_XMT1 r) SEEK_NX_MODE_XMT2 s) SEEK_NX_MODE_XMT3 t) NX_MODE_XMT_ACK u) NX_MODE_XMT
750	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_ASYM_MODE	Coverage for assertion or de-assertion of asym_mode

S.No	Spec.Ref	Cover Group	Cover Points	Description
751	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_PISM_SILENT	Coverage for Port Initialization state machine transitioning SILENT state
752	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_ASYM_XMT_EXIT	BFM making the following transitions a) SEEK_1X_MODE_XMT2 -> ASYM_XMT_EXIT b) SEEK_2X_MODE_XMT2 -> ASYM_XMT_EXIT c) SEEK_NX_MODE_XMT2 -> ASYM_XMT_EXIT
753	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_ASYM_XMT_IDLE	BFM making the following transitions a) ASYM_XMT_EXIT -> ASYM_XMT_IDLE
754	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_XMT_WIDTH_NACK	BFM making the following transitions a) SEEK_1X_MODE_XMT3 -> XMT_WIDTH_NACK b) SEEK_2X_MODE_XMT3 -> XMT_WIDTH_NACK
755	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_1X_MODE_XMT	BFM making the following transitions a) 2X_MODE_XMT -> SEEK_1X_MODE_XMT b) NX_MODE_XMT -> SEEK_1X_MODE_XMT
756	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_1X_MODE_XMT1	BFM making the following transitions a) SEEK_1X_MODE_XMT -> SEEK_1X_MODE_XMT1
757	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_1X_MODE_XMT2	BFM making the following transitions a) SEEK_1X_MODE_XMT1 -> SEEK_1X_MODE_XMT2
758	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_1X_MODE_XMT3	BFM making the following transitions a) SEEK_1X_MODE_XMT -> SEEK_1X_MODE_XMT3 b) SEEK_1X_MODE_XMT1 -> SEEK_1X_MODE_XMT3 c) SEEK_1X_MODE_XMT2 -> SEEK_1X_MODE_XMT3
759	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_1X_MODE_XMT_ACK	BFM making the following transitions a) SEEK_1X_MODE_XMT2 -> SEEK_1X_MODE_XMT_ACK b) 1X_MODE_XMT -> SEEK_1X_MODE_XMT_ACK
760	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_1X_MODE_XMT	BFM making the following transitions a) 1X_MODE_XMT_ACK -> 1X_MODE_XMT b) XMT_WIDTH_NACK -> 1X_MODE_XMT
761	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_2X_MODE_XMT	BFM making the following transitions a) 1X_MODE_XMT -> SEEK_2X_MODE_XMT b) NX_MODE_XMT -> SEEK_2X_MODE_XMT
762	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_2X_MODE_XMT1	BFM making the following transitions a) SEEK_2X_MODE_XMT -> SEEK_2X_MODE_XMT1
763	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_2X_MODE_XMT2	BFM making the following transitions a) SEEK_2X_MODE_XMT1 -> SEEK_2X_MODE_XMT2

S.No	Spec.Ref	Cover Group	Cover Points	Description
764	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_2X_MODE_XMT3	BFM making the following transitions a) SEEK_2X_MODE_XMT -> SEEK_2X_MODE_XMT3 b) SEEK_2X_MODE_XMT1 -> SEEK_2X_MODE_XMT3 c) SEEK_2X_MODE_XMT2 -> SEEK_2X_MODE_XMT3
765	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_2X_MODE_XMT_ACK	BFM making the following transitions a) SEEK_2X_MODE_XMT2 -> SEEK_2X_MODE_XMT_ACK b) 2X_MODE_XMT -> SEEK_2X_MODE_XMT_ACK
766	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_2X_MODE_XMT	BFM making the following transitions a) 2X_MODE_XMT_ACK -> 2X_MODE_XMT b) XMT_WIDTH_NACK -> 2X_MODE_XMT
767	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_NX_MODE_XMT	BFM making the following transitions a) 1X_MODE_XMT -> SEEK_NX_MODE_XMT b) 2X_MODE_XMT -> SEEK_NX_MODE_XMT
768	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_NX_MODE_XMT1	BFM making the following transitions a) SEEK_NX_MODE_XMT -> SEEK_NX_MODE_XMT1
769	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_NX_MODE_XMT2	BFM making the following transitions a) SEEK_NX_MODE_XMT1 -> SEEK_NX_MODE_XMT2
770	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_SEEK_NX_MODE_XMT3	BFM making the following transitions a) SEEK_NX_MODE_XMT -> SEEK_NX_MODE_XMT3 b) SEEK_NX_MODE_XMT1 -> SEEK_NX_MODE_XMT3 c) SEEK_NX_MODE_XMT2 -> SEEK_NX_MODE_XMT3
771	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_NX_MODE_XMT_ACK	BFM making the following transitions a) SEEK_NX_MODE_XMT2 -> SEEK_NX_MODE_XMT_ACK b) NX_MODE_XMT -> SEEK_NX_MODE_XMT_ACK
772	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_T O_NX_MODE_XMT	BFM making the following transitions a) NX_MODE_XMT_ACK -> NX_MODE_XMT b) XMT_WIDTH_NACK -> NX_MODE_XMT

S.No	Spec.Ref	Cover Group	Cover Points	Description
773	Part6 - 5.17.9.2	CG_GEN3_PL_TRA NSMIT_WIDTH_SM	CP_PL_TX_WIDTH_P ATH_TRANSITIONS	<p>BFM making the following transitions</p> <p>1) ASYM_XMT_IDLE => NX_MODE_XMT 2) ASYM_XMT_IDLE => X2_MODE_XMT 3) ASYM_XMT_IDLE => X1_MODE_XMT 4) NX_MODE_XMT => NX_MODE_XMT_ACK NX_MODE_XMT => SEEK_2X_MODE_XMT => SEEK_2X_MODE_XMT_1 => SEEK_2X_MODE_XMT_2 => X2_MODE_XMT_ACK => X2_MODE_XMT 5) NX_MODE_XMT => SEEK_2X_MODE_XMT => SEEK_2X_MODE_XMT_3 => XMT_WIDTH_NACK => NX_MODE_XMT 6) NX_MODE_XMT => SEEK_2X_MODE_XMT => SEEK_2X_MODE_XMT_1 => SEEK_2X_MODE_XMT_3 => XMT_WIDTH_NACK => NX_MODE_XMT 7) NX_MODE_XMT => SEEK_2X_MODE_XMT => SEEK_2X_MODE_XMT_3 => XMT_WIDTH_NACK => NX_MODE_XMT 8) NX_MODE_XMT => SEEK_2X_MODE_XMT => SEEK_2X_MODE_XMT_1 => SEEK_2X_MODE_XMT_2 => ASYM_XMT_EXIT => ASYM_XMT_IDLE 9) NX_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_1 => SEEK_1X_MODE_XMT_2 => X1_MODE_XMT_ACK => X1_MODE_XMT 10) NX_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_3 => XMT_WIDTH_NACK => NX_MODE_XMT 11) NX_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_1 => SEEK_1X_MODE_XMT_3 => XMT_WIDTH_NACK => NX_MODE_XMT 12) NX_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_3 => XMT_WIDTH_NACK => NX_MODE_XMT 13) NX_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_1 => SEEK_1X_MODE_XMT_2 => ASYM_XMT_EXIT => ASYM_XMT_IDLE 14) X2_MODE_XMT => X2_MODE_XMT_ACK 15) X1_MODE_XMT => X1_MODE_XMT_ACK</p>

S.No	Spec.Ref	Cover Group	Cover Points	Description
774	Part6 - 5.17.9.2	CG_GEN3_PL_TRA NSMIT_WIDTH_SM	CP_PL_TX_WIDTH_P ATH_TRANSITIONS	16) X2_MODE_XMT => SEEK_NX_MODE_XMT => SEEK_NX_MODE_XMT_1 => SEEK_NX_MODE_XMT_2 => NX_MODE_XMT_ACK => NX_MODE_XMT 17) X2_MODE_XMT => SEEK_NX_MODE_XMT => SEEK_NX_MODE_XMT_3 => XMT_WIDTH_NACK => X2_MODE_XMT 18) X2_MODE_XMT => SEEK_NX_MODE_XMT => SEEK_NX_MODE_XMT_1 => SEEK_NX_MODE_XMT_3 => XMT_WIDTH_NACK => X2_MODE_XMT 19) X2_MODE_XMT => SEEK_NX_MODE_XMT => SEEK_NX_MODE_XMT_2 => SEEK_NX_MODE_XMT_3 => XMT_WIDTH_NACK => X2_MODE_XMT 20) X2_MODE_XMT => SEEK_NX_MODE_XMT => SEEK_NX_MODE_XMT_1 => SEEK_NX_MODE_XMT_2 => ASYM_XMT_EXIT => ASYM_XMT_IDLE) 21) X2_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_1 => SEEK_1X_MODE_XMT_2 => X1_MODE_XMT_ACK => X1_MODE_XMT 22) X2_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_3 => XMT_WIDTH_NACK => X1_MODE_XMT 23) X2_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_1 => SEEK_1X_MODE_XMT_3 => XMT_WIDTH_NACK => X1_MODE_XMT 24) X2_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_1 => SEEK_1X_MODE_XMT_2 => SEEK_1X_MODE_XMT_3 => XMT_WIDTH_NACK => X2_MODE_XMT) 25) X2_MODE_XMT => SEEK_1X_MODE_XMT => SEEK_1X_MODE_XMT_1 => SEEK_1X_MODE_XMT_2 => ASYM_XMT_EXIT => ASYM_XMT_IDLE 26) X1_MODE_XMT => SEEK_NX_MODE_XMT => SEEK_NX_MODE_XMT_3 => XMT_WIDTH_NACK => X1_MODE_XMT); 27) X1_MODE_XMT => SEEK_NX_MODE_XMT => SEEK_NX_MODE_XMT_1 => SEEK_NX_MODE_XMT_3 => XMT_WIDTH_NACK => X1_MODE_XMT

S.No	Spec.Ref	Cover Group	Cover Points	Description
775	Part6 - 5.17.9.2	CG_GEN3_PL_TRANSMIT_WIDTH_SM	CP_PL_TX_WIDTH_PATH_TRANSITIONS	28) X1_MODE_XMT => SEEK_NX_MODE_XMT => SEEK_NX_MODE_XMT_2 => SEEK_NX_MODE_XMT_3 => XMT_WIDTH_NACK => X1_MODE_XMT 29) X1_MODE_XMT => SEEK_NX_MODE_XMT => SEEK_NX_MODE_XMT_1=> SEEK_NX_MODE_XMT_2 => ASYM_XMT_EXIT => ASYM_XMT_IDLE 30) X1_MODE_XMT => SEEK_2X_MODE_XMT => SEEK_2X_MODE_XMT_3 => XMT_WIDTH_NACK => X1_MODE_XMT 31) X1_MODE_XMT => SEEK_2X_MODE_XMT => SEEK_2X_MODE_XMT_1 => SEEK_2X_MODE_XMT_3 => XMT_WIDTH_NACK => X1_MODE_XMT 32) X1_MODE_XMT => SEEK_2X_MODE_XMT => SEEK_2X_MODE_XMT_2 => SEEK_2X_MODE_XMT_3 => XMT_WIDTH_NACK => X1_MODE_XMT 33) X1_MODE_XMT => SEEK_2X_MODE_XMT => SEEK_2X_MODE_XMT_1=> SEEK_2X_MODE_XMT_2 => ASYM_XMT_EXIT => ASYM_XMT_IDLE
776	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_CMD_SM	CP_PL_RX_WIDTH_CMD_NEXT_STATE	BFM transitioning to the following states a) RCV_WIDTH_CMD2 b) RCV_WIDTH_CMD3 c) RCV_WIDTH_CMD_IDLE d) RCV_WIDTH_CMD1
777	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_CMD_SM	CP_PL_RX_WIDTH_CMD_TO_RCV_WIDTH_CMD2	BFM making the following transitions a) RCV_WIDTH_CMD1 -> RCV_WIDTH_CMD2 b) RCV_WIDTH_IDLE -> RCV_WIDTH_CMD2
778	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_CMD_SM	CP_PL_RX_WIDTH_CMD_TO_RCV_WIDTH_CMD3	BFM making the following transitions a) RCV_WIDTH_CMD2 -> RCV_WIDTH_CMD3
779	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_CMD_SM	CP_PL_RX_WIDTH_CMD_TO_RCV_WIDTH_CMD_IDLE	BFM making the following transitions a) RCV_WIDTH_CMD3 -> RCV_WIDTH_CMD_IDLE
780	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_CMD_SM	CP_PL_RX_WIDTH_CMD_TO_RCV_WIDTH_CMD1	BFM making the following transitions a) RCV_WIDTH_CMD_IDLE -> RCV_WIDTH_CMD1
781	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_CMD_SM	CP_PL_RX_WIDTH_CMD_PATH_TRANSITIONS	BFM making the following transitions 1) RCV_WIDTH_CMD_3 => RCV_WIDTH_CMD_IDLE => RCV_WIDTH_CMD_2 => RCV_WIDTH_CMD_3 2) RCV_WIDTH_CMD_3 => RCV_WIDTH_CMD_IDLE => RCV_WIDTH_CMD_1 => RCV_WIDTH_CMD_2 => RCV_WIDTH_CMD_3

S.No	Spec.Ref	Cover Group	Cover Points	Description
782	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_N EXT_STATE	BFM transitioning to the following states a) ASYM_RCV_EXIT b) ASYM_RCV_IDLE c) RCV_WIDTH_NACK d) SEEK_1X_MODE_RCV e) 1X_MODE_RCV_ACK f) 1X_RETRAIN g) 1X_RECOVERY h) 1X_MODE_RCV i) SEEK_2X_MODE_RCV j) 2X_MODE_RCV_ACK k) 2X_RETRAIN l) 2X_RECOVERY m) 2X_MODE_RCV n) SEEK_NX_MODE_RCV o) NX_MODE_RCV_ACK p) NX_RETRAIN q) NX_RECOVERY r) NX_MODE_RCV
783	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_ASYM_MODE	coverage for assertion and de-assertion of asym_mode
784	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_PISM_SILENT	Coverage for Port Initialization state machine transitioning SILENT state
785	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_T O_ASYM_RCV_EXIT	BFM making the following transitions a) SEEK_1X_MODE_RCV -> ASYM_RCV_EXIT b) 1X_RECOVERY -> ASYM_RCV_EXIT c) 1X_MODE_RCV -> ASYM_RCV_EXIT d) SEEK_2X_MODE_RCV -> ASYM_RCV_EXIT e) 2X_RECOVERY -> ASYM_RCV_EXIT f) 2X_MODE_RCV -> ASYM_RCV_EXIT g) SEEK_NX_MODE_RCV -> ASYM_RCV_EXIT h) NX_RECOVERY -> ASYM_RCV_EXIT i) NX_MODE_RCV -> ASYM_RCV_EXIT
786	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_T O_ASYM_RCV_IDLE	BFM making the following transitions a) ASYM_RCV_EXIT -> ASYM_RCV_IDLE
787	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_T O_RCV_WIDTH_NACK	BFM making the following transitions a) SEEK_1X_MODE_RCV -> RCV_WIDTH_NACK b) SEEK_2X_MODE_RCV -> RCV_WIDTH_NACK c) SEEK_NX_MODE_RCV -> RCV_WIDTH_NACK
788	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_T O_SEEK_1X_MODE_RCV	BFM making the following transitions a) 2X_MODE_RCV -> SEEK_1X_MODE_RCV b) NX_MODE_RCV -> SEEK_1X_MODE_RCV
789	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_T O_1X_MODE_RCV_ACK	BFM making the following transitions a) SEEK_1X_MODE_RCV -> 1X_MODE_RCV_ACK b) 1X_MODE_RCV -> 1X_MODE_RCV_ACK
790	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_T O_SEEK_1X_RETRAIN	BFM making the following transitions a) 1X_RECOVERY -> 1X_RETRAIN

S.No	Spec.Ref	Cover Group	Cover Points	Description
791	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_1X_RECOVERY	BFM making the following transitions a) 1X_RETRAIN -> 1X_RECOVERY b) RCV_WIDTH_NACK -> 1X_RECOVERY c) 1X_MODE_RCV -> 1X_RECOVERY
792	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_1X_MODE_RCV	BFM making the following transitions a) SEEK_1X_MODE_RCV -> 1X_MODE_RCV b) 1X_RECOVERY -> 1X_MODE_RCV
793	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_SEEK_2X_MODE_RCV	BFM making the following transitions a) 1X_MODE_RCV -> SEEK_2X_MODE_RCV b) NX_MODE_RCV -> SEEK_2X_MODE_RCV
794	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_2X_MODE_RCV_ACK	BFM making the following transitions a) SEEK_2X_MODE_RCV -> 2X_MODE_RCV_ACK b) 2X_MODE_RCV -> 2X_MODE_RCV_ACK
795	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_SEEK_2X_RETRAIN	BFM making the following transitions a) 2X_RECOVERY -> 2X_RETRAIN
796	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_SEEK_2X_RECOVERY	BFM making the following transitions a) 2X_RETRAIN -> 2X_RECOVERY b) RCV_WIDTH_NACK -> 2X_RECOVERY c) 2X_MODE_RCV -> 2X_RECOVERY
797	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_SEEK_2X_MODE_RCV	BFM making the following transitions a) SEEK_2X_MODE_RCV -> 2X_MODE_RCV b) 2X_RECOVERY -> 2X_MODE_RCV c) ASYM_RCV_IDLE -> 2X_MODE_RCV
798	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_SEEK_NX_MODE_RCV	BFM making the following transitions a) 1X_MODE_RCV -> SEEK_NX_MODE_RCV b) 2X_MODE_RCV -> SEEK_NX_MODE_RCV
799	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_NX_MODE_RCV_ACK	BFM making the following transitions a) SEEK_NX_MODE_RCV -> NX_MODE_RCV_ACK b) NX_MODE_RCV -> NX_MODE_RCV_ACK
800	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_NX_RETRAIN	BFM making the following transitions a) NX_RECOVERY -> NX_RETRAIN
801	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_NX_RECOVERY	BFM making the following transitions a) NX_RETRAIN -> NX_RECOVERY b) RCV_WIDTH_NACK -> NX_RECOVERY c) NX_MODE_RCV -> NX_RECOVERY
802	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_TO_NX_MODE_RCV	BFM making the following transitions a) NX_MODE_RCV_ACK -> NX_MODE_RCV b) NX_RECOVERY -> NX_MODE_RCV c) ASYM_RCV_IDLE -> NX_MODE_RCV

S.No	Spec.Ref	Cover Group	Cover Points	Description
803	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_PATH_TRANSITIONS	<p>BFM making the following path transitions - 1</p> <p>a)ASYM_RCV_IDLE=>NX_MODE_RCV</p> <p>b) ASYM_RCV_IDLE => X2_MODE_RCV</p> <p>c) NX_MODE_RCV => NX_RECOVERY_RCV => NX_MODE_RCV</p> <p>d) NX_MODE_RCV => NX_RECOVERY_RCV => NX_RETRAIN_RCV => NX_RECOVERY_RCV => NX_MODE_RCV</p> <p>e) NX_MODE_RCV => NX_RECOVERY_RCV => NX_RETRAIN_RCV => NX_RECOVERY_RCV => ASYM_RCV_EXIT => ASYM_RCV_IDLE</p> <p>f) X2_MODE_RCV => X2_RECOVERY_RCV => X2_MODE_RCV</p> <p>g) X2_MODE_RCV => X2_RECOVERY_RCV => X2_RETRAIN_RCV => X2_RECOVERY_RCV => X2_MODE_RCV</p> <p>h)X2_MODE_RCV => X2_RECOVERY_RCV => X2_RETRAIN_RCV => X2_RECOVERY_RCV => ASYM_RCV_EXIT=>ASYM_RCV_IDLE</p> <p>i) X1_MODE_RCV => X1_RECOVERY_RCV => X1_MODE_RCV</p> <p>j) X1_MODE_RCV => X1_RECOVERY_RCV => X1_RETRAIN_RCV => X1_RECOVERY_RCV => X1_MODE_RCV</p> <p>k) X1_MODE_RCV => X1_RECOVERY_RCV => X1_RETRAIN_RCV => X1_RECOVERY_RCV => ASYM_RCV_EXIT => ASYM_RCV_IDLE</p> <p>l) NX_MODE_RCV => SEEK_2X_MODE_RCV => RCV_WIDTH_NACK => NX_RECOVERY_RCV => NX_MODE_RCV</p> <p>m) NX_MODE_RCV => SEEK_2X_MODE_RCV => ASYM_RCV_EXIT => ASYM_RCV_IDLE</p> <p>n) NX_MODE_RCV => SEEK_2X_MODE_RCV =>X2_MODE_RCV_ACK=>X2_MODE_RCV</p> <p>o) NX_MODE_RCV => SEEK_1X_MODE_RCV => RCV_WIDTH_NACK => NX_RECOVERY_RCV=>NX_MODE_RCV</p> <p>p) NX_MODE_RCV => SEEK_1X_MODE_RCV => ASYM_RCV_EXIT => ASYM_RCV_IDLE</p>

S.No	Spec.Ref	Cover Group	Cover Points	Description
804	Part6 - 5.17.10.1	CG_GEN3_PL_RECEIVE_WIDTH_SM	CP_PL_RX_WIDTH_PATH_TRANSITIONS	<p>BFM making the following path transitions - 2 Continuation of the above CP transition</p> <p>q) NX_MODE_RCV => SEEK_1X_MODE_RCV => X1_MODE_RCV_ACK => X1_MODE_RCV</p> <p>r) X2_MODE_RCV => SEEK_NX_MODE_RCV => RCV_WIDTH_NACK => X2_RECOVERY_RCV => X2_MODE_RCV</p> <p>s) X2_MODE_RCV => SEEK_NX_MODE_RCV => ASYM_RCV_EXIT => ASYM_RCV_IDLE</p> <p>t) X2_MODE_RCV => SEEK_NX_MODE_RCV => NX_MODE_RCV_ACK => NX_MODE_RCV</p> <p>u) X2_MODE_RCV => SEEK_1X_MODE_RCV => RCV_WIDTH_NACK => X2_RECOVERY_RCV => X2_MODE_RCV</p> <p>v) X2_MODE_RCV => SEEK_1X_MODE_RCV => ASYM_RCV_EXIT => ASYM_RCV_IDLE</p> <p>w) X2_MODE_RCV => SEEK_1X_MODE_RCV => X1_MODE_RCV_ACK => X1_MODE_RCV</p> <p>x) X1_MODE_RCV => SEEK_NX_MODE_RCV => RCV_WIDTH_NACK => X1_RECOVERY_RCV => X1_MODE_RCV</p> <p>y) X1_MODE_RCV => SEEK_NX_MODE_RCV => ASYM_RCV_EXIT => ASYM_RCV_IDLE</p> <p>z) X1_MODE_RCV => SEEK_NX_MODE_RCV => NX_MODE_RCV_ACK => NX_MODE_RCV</p> <p>1) X1_MODE_RCV => SEEK_2X_MODE_RCV => RCV_WIDTH_NACK => X1_RECOVERY_RCV => X1_MODE_RCV</p> <p>2) X1_MODE_RCV => SEEK_2X_MODE_RCV => ASYM_RCV_EXIT => ASYM_RCV_IDLE</p> <p>3) X1_MODE_RCV => SEEK_2X_MODE_RCV => X2_MODE_RCV_ACK => X2_MODE_RCV</p>
805	GEN3 Part6 - 6.5.3.5.1	CG_GEN3_PL_TSG	CP_PL_TSG_UNINTERRUPTED	Coverage for BFM transmitting uninterrupted Timestamp Control Symbols
806	IEEE Standard 302.3-2008	CG_GEN3_PL_FRAME_LOCK_SM	CP_PL_GEN3_FRAME_LOCK_NEXT_STATE	<p>BFM transitioning to the following states</p> <p>1) OUT_OF_FRAME</p> <p>2) RESET_COUNT</p> <p>3) GET_NEW_MARKER</p> <p>4) TEST_MARKER</p> <p>5) VALID_MARKER</p> <p>6) INVALID_MARKER</p> <p>7) IN_FRAME, SLIP</p>
807	IEEE Standard 302.3-2008	CG_GEN3_PL_FRAME_LOCK_SM	CP_PL_GEN3_FRAME_LOCK_TO_RSTCNT	<p>BFM making the following transitions</p> <p>1) OUT_OF_FRAME => RESET_COUNT</p> <p>2) IN_FRAME => RESET_COUNT</p> <p>3) SLIP => RESET_COUNT</p>

S.No	Spec.Ref	Cover Group	Cover Points	Description
808	IEEE Standard 302.3-2008	CG_GEN3_PL_FRA ME_LOCK_SM	CP_PL_GEN3_FR AME_LOCK_TO_ GNM	BFM making the following transitions RESET_COUNT => GET_NEW_MARKER VALID_MARKER => GET_NEW_MARKER INVALID_MARKER => GET_NEW_MARKER
809	IEEE Standard 302.3-2008	CG_GEN3_PL_FRA ME_LOCK_SM	CP_PL_GEN3_FRAM E_LOCK_TO_TM	BFM making the following transitions GET_NEW_MARKER => TEST_MARKER
810	IEEE Standard 302.3-2008	CG_GEN3_PL_FRA ME_LOCK_SM	CP_PL_GEN3_FRAM E_LOCK_TO_VM_IVM	BFM making the following transitions TEST_MARKER => VALID_MARKER TEST_MARKER => INVALID_MARKER
811	IEEE Standard 302.3-2008	CG_GEN3_PL_FRA ME_LOCK_SM	CP_PL_GEN3_FRAM E_LOCK_TO_INF	BFM making the following transitions VALID_MARKER => IN_FRAME
812	IEEE Standard 302.3-2008	CG_GEN3_PL_FRA ME_LOCK_SM	CP_PL_GEN3_FRAM E_LOCK_TO_SLIP	BFM making the following transitions INVALID_MARKER => SLIP
813	IEEE Standard 302.3-2008	CG_GEN3_PL_FRA ME_LOCK_SM	CP_PL_GEN3_FRAM E_LOCK_PATH_TRA NSITIONS	BFM making the following transitions 1) OUT_OF_FRAME => RESET_COUNT => GET_NEW_MARKER 2) GET_NEW_MARKER => TEST_MARKER => VALID_MARKER => GET_NEW_MARKER 3) GET_NEW_MARKER => TEST_MARKER => INVALID_MARKER => GET_NEW_MARKER 4) GET_NEW_MARKER => TEST_MARKER => VALID_MARKER => IN_FRAME 5) GET_NEW_MARKER => TEST_MARKER => INVALID_MARKER => SLIP 6) IN_FRAME => RESET_COUNT => GET_NEW_MARKER 7) SLIP => RESET_COUNT => GET_NEW_MARKER
814	IEEE Standard 302.3-2008	CG_GEN3_PL_C0_ COEFF_UPDATE_ SM	CP_PL_GEN3_PL_C0 _COEFF_UPDATE_N EXT_STATE	BFM transitioning to the following states 1) NOT_UPDATED 2) UPDATE_COEFF 3) MAXIMUM 4) UPDATED 5) MINIMUM
815	IEEE Standard 302.3-2008	CG_GEN3_PL_C0_ COEFF_UPDATE_ SM	CP_PL_GEN3_PL_C0 _COEFF_UPDATE_N U	BFM making the following transitions 1) MAXIMUM => NOT_UPDATED 2) MINIMUM => NOT_UPDATED 3) UPDATED => NOT_UPDATED

S.No	Spec.Ref	Cover Group	Cover Points	Description
816	IEEE Standard 302.3-2008	CG_GEN3_PL_C0_COEFF_UPDATE_SM	CP_PL_GEN3_PL_C0_COEFF_UPDATE_UPCOEFF	BFM making the following transitions NOT_UPDATED => UPDATE_COEFF
817	IEEE Standard 302.3-2008	CG_GEN3_PL_C0_COEFF_UPDATE_SM	CP_PL_GEN3_PL_C0_COEFF_UPDATE_UP	BFM making the following transitions UPDATE_COEFF => UPDATED
818	IEEE Standard 302.3-2008	CG_GEN3_PL_C0_COEFF_UPDATE_SM	CP_PL_GEN3_PL_C0_COEFF_UPDATE_MAX	BFM making the following transitions UPDATE_COEFF => MAXIMUM
819	IEEE Standard 302.3-2008	CG_GEN3_PL_C0_COEFF_UPDATE_SM	CP_PL_GEN3_PL_C0_COEFF_UPDATE_MIN	BFM making the following transitions UPDATE_COEFF => MINIMUM
820	IEEE Standard 302.3-2008	CG_GEN3_PL_C0_COEFF_UPDATE_SM	CP_PL_GEN3_PL_C0_COEFF_UPDATE_PATH	BFM making the following transitions 1) NOT_UPDATED => UPDATE_COEFF => MAXIMUM => NOT_UPDATED 2) NOT_UPDATED => UPDATE_COEFF => MINIMUM => NOT_UPDATED 3) NOT_UPDATED => UPDATE_COEFF => UPDATED => NOT_UPDATED
821	IEEE Standard 302.3-2008	CG_GEN3_PL_CP1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CP1_COEFF_UPDATE_NEXT_STATE	BFM transitioning to the following states 1) NOT_UPDATED 2) UPDATE_COEFF 3) MAXIMUM 4) UPDATED 5) MINIMUM
822	IEEE Standard 302.3-2008	CG_GEN3_PL_CP1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CP1_COEFF_UPDATE_NU	BFM making the following transitions 1) MAXIMUM => NOT_UPDATED 2) MINIMUM => NOT_UPDATED 3) UPDATED => NOT_UPDATED
823	IEEE Standard 302.3-2008	CG_GEN3_PL_CP1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CP1_COEFF_UPDATE_UPCOEFF	BFM making the following transitions NOT_UPDATED => UPDATE_COEFF
824	IEEE Standard 302.3-2008	CG_GEN3_PL_CP1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CP1_COEFF_UPDATE_UP	BFM making the following transitions UPDATE_COEFF => UPDATED

S.No	Spec.Ref	Cover Group	Cover Points	Description
825	IEEE Standard 302.3-2008	CG_GEN3_PL_CP1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CP1_COEFF_UPDATE_MAX	BFM making the following transitions UPDATE_COEFF => MAXIMUM
826	IEEE Standard 302.3-2008	CG_GEN3_PL_CP1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_C0_COEFF_UPDATE_MIN	BFM making the following transitions UPDATE_COEFF => MINIMUM
827	IEEE Standard 302.3-2008	CG_GEN3_PL_CP1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CP1_COEFF_UPDATE_PATH	BFM making the following transitions 1) NOT_UPDATED => UPDATE_COEFF => MAXIMUM => NOT_UPDATED 2) NOT_UPDATED => UPDATE_COEFF => MINIMUM => NOT_UPDATED 3) NOT_UPDATED => UPDATE_COEFF => UPDATED => NOT_UPDATED
828	IEEE Standard 302.3-2008	CG_GEN3_PL_CN1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CN1_COEFF_UPDATE_NEXT_STATE	BFM transitioning to the following states 1) NOT_UPDATED 2) UPDATE_COEFF 3) MAXIMUM 4) UPDATED 5) MINIMUM
829	IEEE Standard 302.3-2008	CG_GEN3_PL_CN1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CN1_COEFF_UPDATE_NU	BFM making the following transitions 1) MAXIMUM => NOT_UPDATED 2) MINIMUM => NOT_UPDATED 3) UPDATED => NOT_UPDATED
830	IEEE Standard 302.3-2008	CG_GEN3_PL_CN1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CN1_COEFF_UPDATE_UPCOEFF	BFM making the following transitions NOT_UPDATED => UPDATE_COEFF
831	IEEE Standard 302.3-2008	CG_GEN3_PL_CN1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CN1_COEFF_UPDATE_UP	BFM making the following transitions UPDATE_COEFF => UPDATED
832	IEEE Standard 302.3-2008	CG_GEN3_PL_CN1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_CN1_COEFF_UPDATE_MAX	BFM making the following transitions UPDATE_COEFF => MAXIMUM
833	IEEE Standard 302.3-2008	CG_GEN3_PL_CN1_COEFF_UPDATE_SM	CP_PL_GEN3_PL_C0_COEFF_UPDATE_MIN	BFM making the following transitions UPDATE_COEFF => MINIMUM

S.No	Spec.Ref	Cover Group	Cover Points	Description
834	IEEE Standard 302.3- 2008	CG_GEN3_PL_CN1 _COEFF_UPDATE_ SM	CP_PL_GEN3_PL_CN 1_COEFF_UPDATE_P ATH	BFM making the following transitions 1) NOT_UPDATED => UPDATE_COEFF => MAXIMUM => NOT_UPDATED 2) NOT_UPDATED => UPDATE_COEFF => MINIMUM => NOT_UPDATED 3) NOT_UPDATED => UPDATE_COEFF => UPDATED => NOT_UPDATED

* Specification reference for PL GEN3 Cover Points are wrt to version 3.0.10. Other Cover Points are wrt Rev.2.2.