Sel = 0 doing a full cycle

& 1 →	Msgs																				
/problem1/clk	0																				
/problem1/rst	0																				
/problem1/sel	0				_																
+ 🔷 /problem1/led	10101010	00000000	01010101	10101	010	0011001	1	1100110	0	000011	1	1111000	0	111111	1	000000	00	010101	1	1010101	0
																					_

Sel = 0 being interrupted by rst = 1

/problem1/dk	0																				
/problem1/rst	0										7-										
/problem1/sel	0																				
+ 1/led /problem1/led	11001100	0000000	0 0	01010101	101010	10	001100	1	1100110	0	(00	000000		0101010	1	1010101	.0	0011001	1	11001100	00001111
<u> </u>																					

Sel = 1 doing a full cycle

/problem1/clk	0																					
/problem1/rst	0																					
/problem1/sel	1																					
+ /problem1/led	00000011	(0000000	00	0000001	(000000	1	0000011	1	000011	10	0001110	0	0001100	0	0001000	00	0000000	0	0000000	1	0000001	1

Sel = 1 being interrupted by rst = 1

/problem1/clk	0																						
/problem1/rst	0													<u> </u>									
/problem1/sel	1																						
→ /problem 1/led	00001110	00000	000	0000000	1	000000	1	000001	1	0000111	.0	(0	000000		0000000	1	000000	11	000001	11	0000111	.0	000
												l											

Sel switching between 0 and 1 and continuing moving through states

111													-
±	problem 1/led	10101010	00000000	01010101	10101010	00000111	00001110	00011100	00001111	11110000	11111111	00000000	3
4	> /problem1/sel	0											
4	> /problem1/rst	0											
4	> /problem1/clk	0											