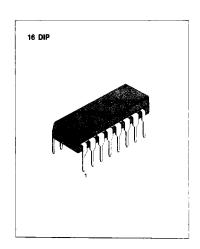
# 5-BAND GRAPHIC EQUALIZER AMPLIFIER

The KA2223 is a monolithic integrated circuit consisting of an operational amplifier with five resonant circuits and a active filter, and it is suitable for radio-cassette tape recorders, car stereos or music center audio systems.

### **FEATURES**

- Tone control with independent adjustment of each band through an external capacitor.
- · Gain control through an external variable resistor.
- Increasing the bands by adding resonant circuit or using two KA2223 in series.
- Low noise ( $V_{NO} = 7\mu V$ : Typ. Flat).
- Low distortion (THD=0.02% Typ. f=1KHz Flat).
- Large allowable input (V<sub>I</sub> = 2.3V: Typ, V<sub>CC</sub> = 9V, f = 1KHz Flat).
- Operating supply voltage range:  $V_{cc} = 5V \sim 13V$



### ORDERING INFORMATION

Device	Package	Operating T	emperature
KA2223	16 DIP	- 20°C ~	+ 70°C

## **BLOCK DIAGRAM**

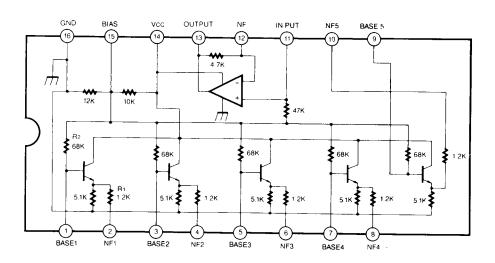


Fig. 1

## ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Characteristic	Symbol	Value	Unit	
Supply Voltage Power Dissipation Operating Temperature Storage Temperature	V <sub>CC</sub> P <sub>D</sub> T <sub>OPR</sub> T <sub>STG</sub>	20 700 - 20 ~ + 70 - 55 ~ + 125	V mW °C °C	

## **ELECTRICAL CHARACTERISTICS**

 $(T_a = 25^{\circ}C, V_{CC} = 9V \text{ unless otherwise specified})$ 

Characteristic  Quiescent Circuit Current		Symbol	Test				B4	11-14
			f(Hz)	Conditions	Min	Тур	Max	Unit
				V <sub>1</sub> = 0	3.0	5.2	8.0	mA
	Flat	G <sub>√</sub> (Flat)	1K	$V_1 = -10 dBm$	- 3.8	- 0.8	2.2	dB
Voltage Gain Cut	G <sub>v</sub> (Boost)	108	V <sub>1</sub> = - 10dBm	8	10.5	12	dB	
		343					dB	
		1.08K					dB	
			3.43K			[		dB
		10.8K	1				dB	
			108			-		dB
	G <sub>v</sub> (Cut)	343	V <sub>1</sub> = - 10dBm	- 12	- 10.5	-8	dB	
		1.08K					dB	
		3.43K					dB	
			10.8K	1				dB
Total Harmoni	ic Distortion	THD	1K	V₁ ≈ 1V		0.02	0.1	%
Output Noise Voltage		V <sub>NO</sub>	Flat, Input Short BW( - 3dB) = 10Hz ~30KHz			7.0	30	μ۷

## **TEST CIRCUIT**

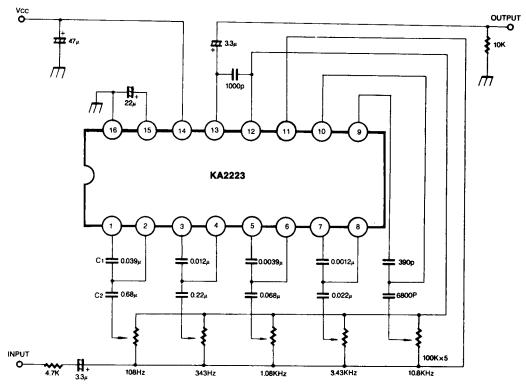


Fig. 2

Resonant frequency 
$$f_0 = \frac{1}{2\pi \sqrt{R_1 R_2 C_1 C_2}}$$

( $R_1 = 1.2K$ ,  $R_2 = 68K$  on-chip resistor)

## **APPLICATION CIRCUIT**

### 1. 7 BAND

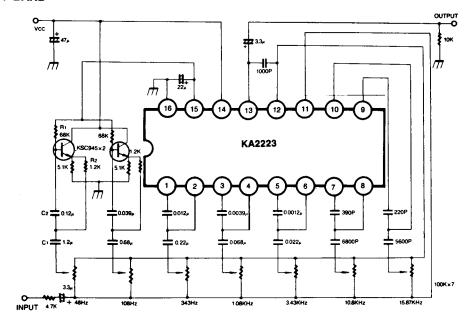


Fig. 3

### 2. 10 BAND

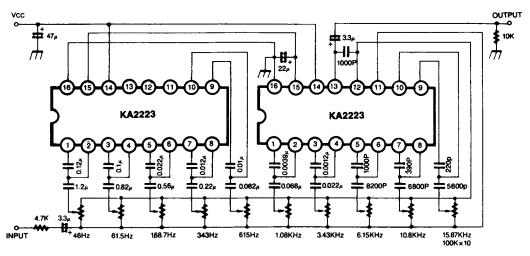


Fig. 4

