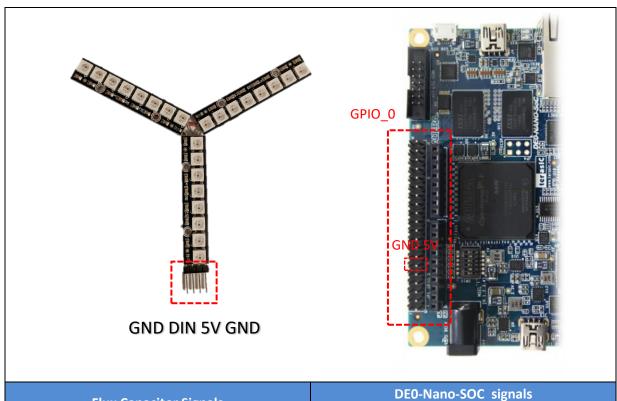


Time Circuit Display signals (J1 connector)	DE0-Nano-SOC signals (JP7 and J17 connectors)
+5V	5V – GPIO_1[11] (JP7)
GND	GND – GPIO_1[12] (JP7)
SDA	HPS_I2C1_SDAT LTC_2x7[9] (J17)
SCL	HPS_I2C1_SCLK LTC_2x7[11] (J17)
Figure : Connection for the Time Circuit Display	



Flux Capacitor Signals	DEO-Nano-SOC signals (JP1 connector)
VCC	5V – GPIO_0[11] (JP1)
GND	GND – GPIO_0[12] (JP1)
DIN	GPIO_0[0] (JP1)
Figure: Connection for the Flux Capacitor	