ctarget: file format elf64-x86-64

Disassembly of section .init:

0000000000400bc8 <\_init>:

400bc8: 48 83 ec 08 sub $0x8,%rsp

400bcc: 48 8b 05 25 34 20 00 mov 0x203425(%rip),%rax # 603ff8 <\_DYNAMIC+0x1d0>

400bd3: 48 85 c0 test %rax,%rax

400bd6: 74 05 je 400bdd <\_init+0x15>

400bd8: e8 33 01 00 00 callq 400d10 <\_\_gmon\_start\_\_@plt>

400bdd: 48 83 c4 08 add $0x8,%rsp

400be1: c3 retq

Disassembly of section .plt:

0000000000400bf0 <strcasecmp@plt-0x10>:

400bf0: ff 35 12 34 20 00 pushq 0x203412(%rip) # 604008 <\_GLOBAL\_OFFSET\_TABLE\_+0x8>

400bf6: ff 25 14 34 20 00 jmpq \*0x203414(%rip) # 604010 <\_GLOBAL\_OFFSET\_TABLE\_+0x10>

400bfc: 0f 1f 40 00 nopl 0x0(%rax)

0000000000400c00 <strcasecmp@plt>:

400c00: ff 25 12 34 20 00 jmpq \*0x203412(%rip) # 604018 <\_GLOBAL\_OFFSET\_TABLE\_+0x18>

400c06: 68 00 00 00 00 pushq $0x0

400c0b: e9 e0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c10 <\_\_errno\_location@plt>:

400c10: ff 25 0a 34 20 00 jmpq \*0x20340a(%rip) # 604020 <\_GLOBAL\_OFFSET\_TABLE\_+0x20>

400c16: 68 01 00 00 00 pushq $0x1

400c1b: e9 d0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c20 <srandom@plt>:

400c20: ff 25 02 34 20 00 jmpq \*0x203402(%rip) # 604028 <\_GLOBAL\_OFFSET\_TABLE\_+0x28>

400c26: 68 02 00 00 00 pushq $0x2

400c2b: e9 c0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c30 <strncmp@plt>:

400c30: ff 25 fa 33 20 00 jmpq \*0x2033fa(%rip) # 604030 <\_GLOBAL\_OFFSET\_TABLE\_+0x30>

400c36: 68 03 00 00 00 pushq $0x3

400c3b: e9 b0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c40 <strcpy@plt>:

400c40: ff 25 f2 33 20 00 jmpq \*0x2033f2(%rip) # 604038 <\_GLOBAL\_OFFSET\_TABLE\_+0x38>

400c46: 68 04 00 00 00 pushq $0x4

400c4b: e9 a0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c50 <puts@plt>:

400c50: ff 25 ea 33 20 00 jmpq \*0x2033ea(%rip) # 604040 <\_GLOBAL\_OFFSET\_TABLE\_+0x40>

400c56: 68 05 00 00 00 pushq $0x5

400c5b: e9 90 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c60 <write@plt>:

400c60: ff 25 e2 33 20 00 jmpq \*0x2033e2(%rip) # 604048 <\_GLOBAL\_OFFSET\_TABLE\_+0x48>

400c66: 68 06 00 00 00 pushq $0x6

400c6b: e9 80 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c70 <mmap@plt>:

400c70: ff 25 da 33 20 00 jmpq \*0x2033da(%rip) # 604050 <\_GLOBAL\_OFFSET\_TABLE\_+0x50>

400c76: 68 07 00 00 00 pushq $0x7

400c7b: e9 70 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c80 <printf@plt>:

400c80: ff 25 d2 33 20 00 jmpq \*0x2033d2(%rip) # 604058 <\_GLOBAL\_OFFSET\_TABLE\_+0x58>

400c86: 68 08 00 00 00 pushq $0x8

400c8b: e9 60 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c90 <memset@plt>:

400c90: ff 25 ca 33 20 00 jmpq \*0x2033ca(%rip) # 604060 <\_GLOBAL\_OFFSET\_TABLE\_+0x60>

400c96: 68 09 00 00 00 pushq $0x9

400c9b: e9 50 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400ca0 <alarm@plt>:

400ca0: ff 25 c2 33 20 00 jmpq \*0x2033c2(%rip) # 604068 <\_GLOBAL\_OFFSET\_TABLE\_+0x68>

400ca6: 68 0a 00 00 00 pushq $0xa

400cab: e9 40 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400cb0 <close@plt>:

400cb0: ff 25 ba 33 20 00 jmpq \*0x2033ba(%rip) # 604070 <\_GLOBAL\_OFFSET\_TABLE\_+0x70>

400cb6: 68 0b 00 00 00 pushq $0xb

400cbb: e9 30 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400cc0 <read@plt>:

400cc0: ff 25 b2 33 20 00 jmpq \*0x2033b2(%rip) # 604078 <\_GLOBAL\_OFFSET\_TABLE\_+0x78>

400cc6: 68 0c 00 00 00 pushq $0xc

400ccb: e9 20 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400cd0 <\_\_libc\_start\_main@plt>:

400cd0: ff 25 aa 33 20 00 jmpq \*0x2033aa(%rip) # 604080 <\_GLOBAL\_OFFSET\_TABLE\_+0x80>

400cd6: 68 0d 00 00 00 pushq $0xd

400cdb: e9 10 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400ce0 <signal@plt>:

400ce0: ff 25 a2 33 20 00 jmpq \*0x2033a2(%rip) # 604088 <\_GLOBAL\_OFFSET\_TABLE\_+0x88>

400ce6: 68 0e 00 00 00 pushq $0xe

400ceb: e9 00 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400cf0 <gethostbyname@plt>:

400cf0: ff 25 9a 33 20 00 jmpq \*0x20339a(%rip) # 604090 <\_GLOBAL\_OFFSET\_TABLE\_+0x90>

400cf6: 68 0f 00 00 00 pushq $0xf

400cfb: e9 f0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d00 <fprintf@plt>:

400d00: ff 25 92 33 20 00 jmpq \*0x203392(%rip) # 604098 <\_GLOBAL\_OFFSET\_TABLE\_+0x98>

400d06: 68 10 00 00 00 pushq $0x10

400d0b: e9 e0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d10 <\_\_gmon\_start\_\_@plt>:

400d10: ff 25 8a 33 20 00 jmpq \*0x20338a(%rip) # 6040a0 <\_GLOBAL\_OFFSET\_TABLE\_+0xa0>

400d16: 68 11 00 00 00 pushq $0x11

400d1b: e9 d0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d20 <strtol@plt>:

400d20: ff 25 82 33 20 00 jmpq \*0x203382(%rip) # 6040a8 <\_GLOBAL\_OFFSET\_TABLE\_+0xa8>

400d26: 68 12 00 00 00 pushq $0x12

400d2b: e9 c0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d30 <memcpy@plt>:

400d30: ff 25 7a 33 20 00 jmpq \*0x20337a(%rip) # 6040b0 <\_GLOBAL\_OFFSET\_TABLE\_+0xb0>

400d36: 68 13 00 00 00 pushq $0x13

400d3b: e9 b0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d40 <time@plt>:

400d40: ff 25 72 33 20 00 jmpq \*0x203372(%rip) # 6040b8 <\_GLOBAL\_OFFSET\_TABLE\_+0xb8>

400d46: 68 14 00 00 00 pushq $0x14

400d4b: e9 a0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d50 <random@plt>:

400d50: ff 25 6a 33 20 00 jmpq \*0x20336a(%rip) # 6040c0 <\_GLOBAL\_OFFSET\_TABLE\_+0xc0>

400d56: 68 15 00 00 00 pushq $0x15

400d5b: e9 90 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d60 <\_IO\_getc@plt>:

400d60: ff 25 62 33 20 00 jmpq \*0x203362(%rip) # 6040c8 <\_GLOBAL\_OFFSET\_TABLE\_+0xc8>

400d66: 68 16 00 00 00 pushq $0x16

400d6b: e9 80 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d70 <\_\_isoc99\_sscanf@plt>:

400d70: ff 25 5a 33 20 00 jmpq \*0x20335a(%rip) # 6040d0 <\_GLOBAL\_OFFSET\_TABLE\_+0xd0>

400d76: 68 17 00 00 00 pushq $0x17

400d7b: e9 70 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d80 <munmap@plt>:

400d80: ff 25 52 33 20 00 jmpq \*0x203352(%rip) # 6040d8 <\_GLOBAL\_OFFSET\_TABLE\_+0xd8>

400d86: 68 18 00 00 00 pushq $0x18

400d8b: e9 60 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d90 <bcopy@plt>:

400d90: ff 25 4a 33 20 00 jmpq \*0x20334a(%rip) # 6040e0 <\_GLOBAL\_OFFSET\_TABLE\_+0xe0>

400d96: 68 19 00 00 00 pushq $0x19

400d9b: e9 50 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400da0 <fopen@plt>:

400da0: ff 25 42 33 20 00 jmpq \*0x203342(%rip) # 6040e8 <\_GLOBAL\_OFFSET\_TABLE\_+0xe8>

400da6: 68 1a 00 00 00 pushq $0x1a

400dab: e9 40 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400db0 <getopt@plt>:

400db0: ff 25 3a 33 20 00 jmpq \*0x20333a(%rip) # 6040f0 <\_GLOBAL\_OFFSET\_TABLE\_+0xf0>

400db6: 68 1b 00 00 00 pushq $0x1b

400dbb: e9 30 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400dc0 <strtoul@plt>:

400dc0: ff 25 32 33 20 00 jmpq \*0x203332(%rip) # 6040f8 <\_GLOBAL\_OFFSET\_TABLE\_+0xf8>

400dc6: 68 1c 00 00 00 pushq $0x1c

400dcb: e9 20 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400dd0 <gethostname@plt>:

400dd0: ff 25 2a 33 20 00 jmpq \*0x20332a(%rip) # 604100 <\_GLOBAL\_OFFSET\_TABLE\_+0x100>

400dd6: 68 1d 00 00 00 pushq $0x1d

400ddb: e9 10 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400de0 <sprintf@plt>:

400de0: ff 25 22 33 20 00 jmpq \*0x203322(%rip) # 604108 <\_GLOBAL\_OFFSET\_TABLE\_+0x108>

400de6: 68 1e 00 00 00 pushq $0x1e

400deb: e9 00 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400df0 <exit@plt>:

400df0: ff 25 1a 33 20 00 jmpq \*0x20331a(%rip) # 604110 <\_GLOBAL\_OFFSET\_TABLE\_+0x110>

400df6: 68 1f 00 00 00 pushq $0x1f

400dfb: e9 f0 fd ff ff jmpq 400bf0 <\_init+0x28>

0000000000400e00 <connect@plt>:

400e00: ff 25 12 33 20 00 jmpq \*0x203312(%rip) # 604118 <\_GLOBAL\_OFFSET\_TABLE\_+0x118>

400e06: 68 20 00 00 00 pushq $0x20

400e0b: e9 e0 fd ff ff jmpq 400bf0 <\_init+0x28>

0000000000400e10 <socket@plt>:

400e10: ff 25 0a 33 20 00 jmpq \*0x20330a(%rip) # 604120 <\_GLOBAL\_OFFSET\_TABLE\_+0x120>

400e16: 68 21 00 00 00 pushq $0x21

400e1b: e9 d0 fd ff ff jmpq 400bf0 <\_init+0x28>

Disassembly of section .text:

0000000000400e20 <\_start>:

400e20: 31 ed xor %ebp,%ebp

400e22: 49 89 d1 mov %rdx,%r9

400e25: 5e pop %rsi

400e26: 48 89 e2 mov %rsp,%rdx

400e29: 48 83 e4 f0 and $0xfffffffffffffff0,%rsp

400e2d: 50 push %rax

400e2e: 54 push %rsp

400e2f: 49 c7 c0 e0 2b 40 00 mov $0x402be0,%r8

400e36: 48 c7 c1 70 2b 40 00 mov $0x402b70,%rcx

400e3d: 48 c7 c7 e0 10 40 00 mov $0x4010e0,%rdi

400e44: e8 87 fe ff ff callq 400cd0 <\_\_libc\_start\_main@plt>

400e49: f4 hlt

400e4a: 66 0f 1f 44 00 00 nopw 0x0(%rax,%rax,1)

0000000000400e50 <deregister\_tm\_clones>:

400e50: b8 b7 44 60 00 mov $0x6044b7,%eax

400e55: 55 push %rbp

400e56: 48 2d b0 44 60 00 sub $0x6044b0,%rax

400e5c: 48 83 f8 0e cmp $0xe,%rax

400e60: 48 89 e5 mov %rsp,%rbp

400e63: 77 02 ja 400e67 <deregister\_tm\_clones+0x17>

400e65: 5d pop %rbp

400e66: c3 retq

400e67: b8 00 00 00 00 mov $0x0,%eax

400e6c: 48 85 c0 test %rax,%rax

400e6f: 74 f4 je 400e65 <deregister\_tm\_clones+0x15>

400e71: 5d pop %rbp

400e72: bf b0 44 60 00 mov $0x6044b0,%edi

400e77: ff e0 jmpq \*%rax

400e79: 0f 1f 80 00 00 00 00 nopl 0x0(%rax)

0000000000400e80 <register\_tm\_clones>:

400e80: b8 b0 44 60 00 mov $0x6044b0,%eax

400e85: 55 push %rbp

400e86: 48 2d b0 44 60 00 sub $0x6044b0,%rax

400e8c: 48 c1 f8 03 sar $0x3,%rax

400e90: 48 89 e5 mov %rsp,%rbp

400e93: 48 89 c2 mov %rax,%rdx

400e96: 48 c1 ea 3f shr $0x3f,%rdx

400e9a: 48 01 d0 add %rdx,%rax

400e9d: 48 d1 f8 sar %rax

400ea0: 75 02 jne 400ea4 <register\_tm\_clones+0x24>

400ea2: 5d pop %rbp

400ea3: c3 retq

400ea4: ba 00 00 00 00 mov $0x0,%edx

400ea9: 48 85 d2 test %rdx,%rdx

400eac: 74 f4 je 400ea2 <register\_tm\_clones+0x22>

400eae: 5d pop %rbp

400eaf: 48 89 c6 mov %rax,%rsi

400eb2: bf b0 44 60 00 mov $0x6044b0,%edi

400eb7: ff e2 jmpq \*%rdx

400eb9: 0f 1f 80 00 00 00 00 nopl 0x0(%rax)

0000000000400ec0 <\_\_do\_global\_dtors\_aux>:

400ec0: 80 3d 11 36 20 00 00 cmpb $0x0,0x203611(%rip) # 6044d8 <completed.6344>

400ec7: 75 11 jne 400eda <\_\_do\_global\_dtors\_aux+0x1a>

400ec9: 55 push %rbp

400eca: 48 89 e5 mov %rsp,%rbp

400ecd: e8 7e ff ff ff callq 400e50 <deregister\_tm\_clones>

400ed2: 5d pop %rbp

400ed3: c6 05 fe 35 20 00 01 movb $0x1,0x2035fe(%rip) # 6044d8 <completed.6344>

400eda: f3 c3 repz retq

400edc: 0f 1f 40 00 nopl 0x0(%rax)

0000000000400ee0 <frame\_dummy>:

400ee0: 48 83 3d 38 2f 20 00 cmpq $0x0,0x202f38(%rip) # 603e20 <\_\_JCR\_END\_\_>

400ee7: 00

400ee8: 74 1e je 400f08 <frame\_dummy+0x28>

400eea: b8 00 00 00 00 mov $0x0,%eax

400eef: 48 85 c0 test %rax,%rax

400ef2: 74 14 je 400f08 <frame\_dummy+0x28>

400ef4: 55 push %rbp

400ef5: bf 20 3e 60 00 mov $0x603e20,%edi

400efa: 48 89 e5 mov %rsp,%rbp

400efd: ff d0 callq \*%rax

400eff: 5d pop %rbp

400f00: e9 7b ff ff ff jmpq 400e80 <register\_tm\_clones>

400f05: 0f 1f 00 nopl (%rax)

400f08: e9 73 ff ff ff jmpq 400e80 <register\_tm\_clones>

400f0d: 0f 1f 00 nopl (%rax)

0000000000400f10 <usage>:

400f10: 48 83 ec 08 sub $0x8,%rsp

400f14: 48 89 fe mov %rdi,%rsi

400f17: 83 3d ea 35 20 00 00 cmpl $0x0,0x2035ea(%rip) # 604508 <is\_checker>

400f1e: 74 39 je 400f59 <usage+0x49>

400f20: bf 00 2c 40 00 mov $0x402c00,%edi

400f25: b8 00 00 00 00 mov $0x0,%eax

400f2a: e8 51 fd ff ff callq 400c80 <printf@plt>

400f2f: bf 38 2c 40 00 mov $0x402c38,%edi

400f34: e8 17 fd ff ff callq 400c50 <puts@plt>

400f39: bf b0 2d 40 00 mov $0x402db0,%edi

400f3e: e8 0d fd ff ff callq 400c50 <puts@plt>

400f43: bf 60 2c 40 00 mov $0x402c60,%edi

400f48: e8 03 fd ff ff callq 400c50 <puts@plt>

400f4d: bf ca 2d 40 00 mov $0x402dca,%edi

400f52: e8 f9 fc ff ff callq 400c50 <puts@plt>

400f57: eb 2d jmp 400f86 <usage+0x76>

400f59: bf e6 2d 40 00 mov $0x402de6,%edi

400f5e: b8 00 00 00 00 mov $0x0,%eax

400f63: e8 18 fd ff ff callq 400c80 <printf@plt>

400f68: bf 88 2c 40 00 mov $0x402c88,%edi

400f6d: e8 de fc ff ff callq 400c50 <puts@plt>

400f72: bf b0 2c 40 00 mov $0x402cb0,%edi

400f77: e8 d4 fc ff ff callq 400c50 <puts@plt>

400f7c: bf 04 2e 40 00 mov $0x402e04,%edi

400f81: e8 ca fc ff ff callq 400c50 <puts@plt>

400f86: bf 00 00 00 00 mov $0x0,%edi

400f8b: e8 60 fe ff ff callq 400df0 <exit@plt>

0000000000400f90 <initialize\_target>:

400f90: 55 push %rbp

400f91: 53 push %rbx

400f92: 48 81 ec 08 21 00 00 sub $0x2108,%rsp

400f99: 89 f5 mov %esi,%ebp

400f9b: 89 3d 57 35 20 00 mov %edi,0x203557(%rip) # 6044f8 <check\_level>

400fa1: 8b 3d c1 31 20 00 mov 0x2031c1(%rip),%edi # 604168 <target\_id>

400fa7: e8 97 1b 00 00 callq 402b43 <gencookie>

400fac: 89 05 52 35 20 00 mov %eax,0x203552(%rip) # 604504 <cookie>

400fb2: 89 c7 mov %eax,%edi

400fb4: e8 8a 1b 00 00 callq 402b43 <gencookie>

400fb9: 89 05 41 35 20 00 mov %eax,0x203541(%rip) # 604500 <authkey>

400fbf: 8b 05 a3 31 20 00 mov 0x2031a3(%rip),%eax # 604168 <target\_id>

400fc5: 8d 78 01 lea 0x1(%rax),%edi

400fc8: e8 53 fc ff ff callq 400c20 <srandom@plt>

400fcd: e8 7e fd ff ff callq 400d50 <random@plt>

400fd2: 89 c7 mov %eax,%edi

400fd4: e8 ca 02 00 00 callq 4012a3 <scramble>

400fd9: 89 c3 mov %eax,%ebx

400fdb: 85 ed test %ebp,%ebp

400fdd: 74 18 je 400ff7 <initialize\_target+0x67>

400fdf: bf 00 00 00 00 mov $0x0,%edi

400fe4: e8 57 fd ff ff callq 400d40 <time@plt>

400fe9: 89 c7 mov %eax,%edi

400feb: e8 30 fc ff ff callq 400c20 <srandom@plt>

400ff0: e8 5b fd ff ff callq 400d50 <random@plt>

400ff5: eb 05 jmp 400ffc <initialize\_target+0x6c>

400ff7: b8 00 00 00 00 mov $0x0,%eax

400ffc: 01 c3 add %eax,%ebx

400ffe: 0f b7 db movzwl %bx,%ebx

401001: 8d 04 dd 00 01 00 00 lea 0x100(,%rbx,8),%eax

401008: 89 c0 mov %eax,%eax

40100a: 48 89 05 8f 34 20 00 mov %rax,0x20348f(%rip) # 6044a0 <buf\_offset>

401011: c6 05 10 41 20 00 63 movb $0x63,0x204110(%rip) # 605128 <target\_prefix>

401018: 83 3d 89 34 20 00 00 cmpl $0x0,0x203489(%rip) # 6044a8 <notify>

40101f: 0f 84 b1 00 00 00 je 4010d6 <initialize\_target+0x146>

401025: 83 3d dc 34 20 00 00 cmpl $0x0,0x2034dc(%rip) # 604508 <is\_checker>

40102c: 0f 85 a4 00 00 00 jne 4010d6 <initialize\_target+0x146>

401032: be 00 01 00 00 mov $0x100,%esi

401037: 48 89 e7 mov %rsp,%rdi

40103a: e8 91 fd ff ff callq 400dd0 <gethostname@plt>

40103f: 85 c0 test %eax,%eax

401041: 74 25 je 401068 <initialize\_target+0xd8>

401043: bf e0 2c 40 00 mov $0x402ce0,%edi

401048: e8 03 fc ff ff callq 400c50 <puts@plt>

40104d: bf 08 00 00 00 mov $0x8,%edi

401052: e8 99 fd ff ff callq 400df0 <exit@plt>

401057: 48 89 e6 mov %rsp,%rsi

40105a: e8 a1 fb ff ff callq 400c00 <strcasecmp@plt>

40105f: 85 c0 test %eax,%eax

401061: 74 21 je 401084 <initialize\_target+0xf4>

401063: 83 c3 01 add $0x1,%ebx

401066: eb 05 jmp 40106d <initialize\_target+0xdd>

401068: bb 00 00 00 00 mov $0x0,%ebx

40106d: 48 63 c3 movslq %ebx,%rax

401070: 48 8b 3c c5 80 41 60 mov 0x604180(,%rax,8),%rdi

401077: 00

401078: 48 85 ff test %rdi,%rdi

40107b: 75 da jne 401057 <initialize\_target+0xc7>

40107d: b8 00 00 00 00 mov $0x0,%eax

401082: eb 05 jmp 401089 <initialize\_target+0xf9>

401084: b8 01 00 00 00 mov $0x1,%eax

401089: 85 c0 test %eax,%eax

40108b: 75 17 jne 4010a4 <initialize\_target+0x114>

40108d: 48 89 e6 mov %rsp,%rsi

401090: bf 18 2d 40 00 mov $0x402d18,%edi

401095: e8 e6 fb ff ff callq 400c80 <printf@plt>

40109a: bf 08 00 00 00 mov $0x8,%edi

40109f: e8 4c fd ff ff callq 400df0 <exit@plt>

4010a4: 48 8d bc 24 00 01 00 lea 0x100(%rsp),%rdi

4010ab: 00

4010ac: e8 29 18 00 00 callq 4028da <init\_driver>

4010b1: 85 c0 test %eax,%eax

4010b3: 79 21 jns 4010d6 <initialize\_target+0x146>

4010b5: 48 8d b4 24 00 01 00 lea 0x100(%rsp),%rsi

4010bc: 00

4010bd: bf 58 2d 40 00 mov $0x402d58,%edi

4010c2: b8 00 00 00 00 mov $0x0,%eax

4010c7: e8 b4 fb ff ff callq 400c80 <printf@plt>

4010cc: bf 08 00 00 00 mov $0x8,%edi

4010d1: e8 1a fd ff ff callq 400df0 <exit@plt>

4010d6: 48 81 c4 08 21 00 00 add $0x2108,%rsp

4010dd: 5b pop %rbx

4010de: 5d pop %rbp

4010df: c3 retq

00000000004010e0 <main>:

4010e0: 41 56 push %r14

4010e2: 41 55 push %r13

4010e4: 41 54 push %r12

4010e6: 55 push %rbp

4010e7: 53 push %rbx

4010e8: 41 89 fc mov %edi,%r12d

4010eb: 48 89 f3 mov %rsi,%rbx

4010ee: be 01 1d 40 00 mov $0x401d01,%esi

4010f3: bf 0b 00 00 00 mov $0xb,%edi

4010f8: e8 e3 fb ff ff callq 400ce0 <signal@plt>

4010fd: be b3 1c 40 00 mov $0x401cb3,%esi

401102: bf 07 00 00 00 mov $0x7,%edi

401107: e8 d4 fb ff ff callq 400ce0 <signal@plt>

40110c: be 4f 1d 40 00 mov $0x401d4f,%esi

401111: bf 04 00 00 00 mov $0x4,%edi

401116: e8 c5 fb ff ff callq 400ce0 <signal@plt>

40111b: 83 3d e6 33 20 00 00 cmpl $0x0,0x2033e6(%rip) # 604508 <is\_checker>

401122: 74 20 je 401144 <main+0x64>

401124: be 9d 1d 40 00 mov $0x401d9d,%esi

401129: bf 0e 00 00 00 mov $0xe,%edi

40112e: e8 ad fb ff ff callq 400ce0 <signal@plt>

401133: bf 05 00 00 00 mov $0x5,%edi

401138: e8 63 fb ff ff callq 400ca0 <alarm@plt>

40113d: bd 22 2e 40 00 mov $0x402e22,%ebp

401142: eb 05 jmp 401149 <main+0x69>

401144: bd 1d 2e 40 00 mov $0x402e1d,%ebp

401149: 48 8b 05 70 33 20 00 mov 0x203370(%rip),%rax # 6044c0 <stdin@@GLIBC\_2.2.5>

401150: 48 89 05 99 33 20 00 mov %rax,0x203399(%rip) # 6044f0 <infile>

401157: 41 bd 00 00 00 00 mov $0x0,%r13d

40115d: 41 be 00 00 00 00 mov $0x0,%r14d

401163: e9 b9 00 00 00 jmpq 401221 <main+0x141>

401168: 83 e8 61 sub $0x61,%eax

40116b: 3c 10 cmp $0x10,%al

40116d: 0f 87 93 00 00 00 ja 401206 <main+0x126>

401173: 0f b6 c0 movzbl %al,%eax

401176: ff 24 c5 68 2e 40 00 jmpq \*0x402e68(,%rax,8)

40117d: 48 8b 3b mov (%rbx),%rdi

401180: e8 8b fd ff ff callq 400f10 <usage>

401185: be c5 30 40 00 mov $0x4030c5,%esi

40118a: 48 8b 3d 37 33 20 00 mov 0x203337(%rip),%rdi # 6044c8 <optarg@@GLIBC\_2.2.5>

401191: e8 0a fc ff ff callq 400da0 <fopen@plt>

401196: 48 89 05 53 33 20 00 mov %rax,0x203353(%rip) # 6044f0 <infile>

40119d: 48 85 c0 test %rax,%rax

4011a0: 75 7f jne 401221 <main+0x141>

4011a2: 48 8b 15 1f 33 20 00 mov 0x20331f(%rip),%rdx # 6044c8 <optarg@@GLIBC\_2.2.5>

4011a9: be 2a 2e 40 00 mov $0x402e2a,%esi

4011ae: 48 8b 3d 1b 33 20 00 mov 0x20331b(%rip),%rdi # 6044d0 <stderr@@GLIBC\_2.2.5>

4011b5: e8 46 fb ff ff callq 400d00 <fprintf@plt>

4011ba: b8 01 00 00 00 mov $0x1,%eax

4011bf: e9 d6 00 00 00 jmpq 40129a <main+0x1ba>

4011c4: ba 10 00 00 00 mov $0x10,%edx

4011c9: be 00 00 00 00 mov $0x0,%esi

4011ce: 48 8b 3d f3 32 20 00 mov 0x2032f3(%rip),%rdi # 6044c8 <optarg@@GLIBC\_2.2.5>

4011d5: e8 e6 fb ff ff callq 400dc0 <strtoul@plt>

4011da: 41 89 c6 mov %eax,%r14d

4011dd: eb 42 jmp 401221 <main+0x141>

4011df: ba 0a 00 00 00 mov $0xa,%edx

4011e4: be 00 00 00 00 mov $0x0,%esi

4011e9: 48 8b 3d d8 32 20 00 mov 0x2032d8(%rip),%rdi # 6044c8 <optarg@@GLIBC\_2.2.5>

4011f0: e8 2b fb ff ff callq 400d20 <strtol@plt>

4011f5: 41 89 c5 mov %eax,%r13d

4011f8: eb 27 jmp 401221 <main+0x141>

4011fa: c7 05 a4 32 20 00 00 movl $0x0,0x2032a4(%rip) # 6044a8 <notify>

401201: 00 00 00

401204: eb 1b jmp 401221 <main+0x141>

401206: 40 0f be f6 movsbl %sil,%esi

40120a: bf 47 2e 40 00 mov $0x402e47,%edi

40120f: b8 00 00 00 00 mov $0x0,%eax

401214: e8 67 fa ff ff callq 400c80 <printf@plt>

401219: 48 8b 3b mov (%rbx),%rdi

40121c: e8 ef fc ff ff callq 400f10 <usage>

401221: 48 89 ea mov %rbp,%rdx

401224: 48 89 de mov %rbx,%rsi

401227: 44 89 e7 mov %r12d,%edi

40122a: e8 81 fb ff ff callq 400db0 <getopt@plt>

40122f: 89 c6 mov %eax,%esi

401231: 3c ff cmp $0xff,%al

401233: 0f 85 2f ff ff ff jne 401168 <main+0x88>

401239: be 00 00 00 00 mov $0x0,%esi

40123e: 44 89 ef mov %r13d,%edi

401241: e8 4a fd ff ff callq 400f90 <initialize\_target>

401246: 83 3d bb 32 20 00 00 cmpl $0x0,0x2032bb(%rip) # 604508 <is\_checker>

40124d: 74 25 je 401274 <main+0x194>

40124f: 44 3b 35 aa 32 20 00 cmp 0x2032aa(%rip),%r14d # 604500 <authkey>

401256: 74 1c je 401274 <main+0x194>

401258: 44 89 f6 mov %r14d,%esi

40125b: bf 80 2d 40 00 mov $0x402d80,%edi

401260: b8 00 00 00 00 mov $0x0,%eax

401265: e8 16 fa ff ff callq 400c80 <printf@plt>

40126a: b8 00 00 00 00 mov $0x0,%eax

40126f: e8 47 07 00 00 callq 4019bb <check\_fail>

401274: 8b 35 8a 32 20 00 mov 0x20328a(%rip),%esi # 604504 <cookie>

40127a: bf 5a 2e 40 00 mov $0x402e5a,%edi

40127f: b8 00 00 00 00 mov $0x0,%eax

401284: e8 f7 f9 ff ff callq 400c80 <printf@plt>

401289: 48 8b 3d 10 32 20 00 mov 0x203210(%rip),%rdi # 6044a0 <buf\_offset>

401290: e8 d7 0b 00 00 callq 401e6c <stable\_launch>

401295: b8 00 00 00 00 mov $0x0,%eax

40129a: 5b pop %rbx

40129b: 5d pop %rbp

40129c: 41 5c pop %r12

40129e: 41 5d pop %r13

4012a0: 41 5e pop %r14

4012a2: c3 retq

00000000004012a3 <scramble>:

4012a3: b8 00 00 00 00 mov $0x0,%eax

4012a8: eb 11 jmp 4012bb <scramble+0x18>

4012aa: 69 c8 73 30 00 00 imul $0x3073,%eax,%ecx

4012b0: 01 f9 add %edi,%ecx

4012b2: 89 c2 mov %eax,%edx

4012b4: 89 4c 94 d0 mov %ecx,-0x30(%rsp,%rdx,4)

4012b8: 83 c0 01 add $0x1,%eax

4012bb: 83 f8 09 cmp $0x9,%eax

4012be: 76 ea jbe 4012aa <scramble+0x7>

4012c0: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4012c4: 69 c0 9a 75 00 00 imul $0x759a,%eax,%eax

4012ca: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4012ce: 8b 44 24 f4 mov -0xc(%rsp),%eax

4012d2: 69 c0 1c 73 00 00 imul $0x731c,%eax,%eax

4012d8: 89 44 24 f4 mov %eax,-0xc(%rsp)

4012dc: 8b 44 24 d0 mov -0x30(%rsp),%eax

4012e0: 69 c0 1f af 00 00 imul $0xaf1f,%eax,%eax

4012e6: 89 44 24 d0 mov %eax,-0x30(%rsp)

4012ea: 8b 44 24 e0 mov -0x20(%rsp),%eax

4012ee: 69 c0 0b 5a 00 00 imul $0x5a0b,%eax,%eax

4012f4: 89 44 24 e0 mov %eax,-0x20(%rsp)

4012f8: 8b 44 24 f0 mov -0x10(%rsp),%eax

4012fc: 69 c0 3e 17 00 00 imul $0x173e,%eax,%eax

401302: 89 44 24 f0 mov %eax,-0x10(%rsp)

401306: 8b 44 24 f0 mov -0x10(%rsp),%eax

40130a: 69 c0 c1 fd 00 00 imul $0xfdc1,%eax,%eax

401310: 89 44 24 f0 mov %eax,-0x10(%rsp)

401314: 8b 44 24 d8 mov -0x28(%rsp),%eax

401318: 69 c0 6e 05 00 00 imul $0x56e,%eax,%eax

40131e: 89 44 24 d8 mov %eax,-0x28(%rsp)

401322: 8b 44 24 d8 mov -0x28(%rsp),%eax

401326: 69 c0 a5 90 00 00 imul $0x90a5,%eax,%eax

40132c: 89 44 24 d8 mov %eax,-0x28(%rsp)

401330: 8b 44 24 d0 mov -0x30(%rsp),%eax

401334: 69 c0 a3 0c 00 00 imul $0xca3,%eax,%eax

40133a: 89 44 24 d0 mov %eax,-0x30(%rsp)

40133e: 8b 44 24 d0 mov -0x30(%rsp),%eax

401342: 69 c0 33 cd 00 00 imul $0xcd33,%eax,%eax

401348: 89 44 24 d0 mov %eax,-0x30(%rsp)

40134c: 8b 44 24 e8 mov -0x18(%rsp),%eax

401350: 69 c0 82 3b 00 00 imul $0x3b82,%eax,%eax

401356: 89 44 24 e8 mov %eax,-0x18(%rsp)

40135a: 8b 44 24 e8 mov -0x18(%rsp),%eax

40135e: 69 c0 d3 6c 00 00 imul $0x6cd3,%eax,%eax

401364: 89 44 24 e8 mov %eax,-0x18(%rsp)

401368: 8b 44 24 dc mov -0x24(%rsp),%eax

40136c: 69 c0 2c 08 00 00 imul $0x82c,%eax,%eax

401372: 89 44 24 dc mov %eax,-0x24(%rsp)

401376: 8b 44 24 f0 mov -0x10(%rsp),%eax

40137a: 69 c0 2b 41 00 00 imul $0x412b,%eax,%eax

401380: 89 44 24 f0 mov %eax,-0x10(%rsp)

401384: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401388: 69 c0 05 30 00 00 imul $0x3005,%eax,%eax

40138e: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401392: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401396: 69 c0 ef 6a 00 00 imul $0x6aef,%eax,%eax

40139c: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4013a0: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4013a4: 69 c0 2e 16 00 00 imul $0x162e,%eax,%eax

4013aa: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4013ae: 8b 44 24 ec mov -0x14(%rsp),%eax

4013b2: 69 c0 4d 7b 00 00 imul $0x7b4d,%eax,%eax

4013b8: 89 44 24 ec mov %eax,-0x14(%rsp)

4013bc: 8b 44 24 d0 mov -0x30(%rsp),%eax

4013c0: 69 c0 b6 fe 00 00 imul $0xfeb6,%eax,%eax

4013c6: 89 44 24 d0 mov %eax,-0x30(%rsp)

4013ca: 8b 44 24 d8 mov -0x28(%rsp),%eax

4013ce: 69 c0 e6 5f 00 00 imul $0x5fe6,%eax,%eax

4013d4: 89 44 24 d8 mov %eax,-0x28(%rsp)

4013d8: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4013dc: 69 c0 72 e1 00 00 imul $0xe172,%eax,%eax

4013e2: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4013e6: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4013ea: 69 c0 58 cd 00 00 imul $0xcd58,%eax,%eax

4013f0: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4013f4: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4013f8: 69 c0 28 77 00 00 imul $0x7728,%eax,%eax

4013fe: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401402: 8b 44 24 f4 mov -0xc(%rsp),%eax

401406: 69 c0 67 c3 00 00 imul $0xc367,%eax,%eax

40140c: 89 44 24 f4 mov %eax,-0xc(%rsp)

401410: 8b 44 24 d8 mov -0x28(%rsp),%eax

401414: 69 c0 0c 9a 00 00 imul $0x9a0c,%eax,%eax

40141a: 89 44 24 d8 mov %eax,-0x28(%rsp)

40141e: 8b 44 24 e0 mov -0x20(%rsp),%eax

401422: 69 c0 e8 0a 00 00 imul $0xae8,%eax,%eax

401428: 89 44 24 e0 mov %eax,-0x20(%rsp)

40142c: 8b 44 24 dc mov -0x24(%rsp),%eax

401430: 69 c0 da 9a 00 00 imul $0x9ada,%eax,%eax

401436: 89 44 24 dc mov %eax,-0x24(%rsp)

40143a: 8b 44 24 d0 mov -0x30(%rsp),%eax

40143e: 69 c0 c4 86 00 00 imul $0x86c4,%eax,%eax

401444: 89 44 24 d0 mov %eax,-0x30(%rsp)

401448: 8b 44 24 e0 mov -0x20(%rsp),%eax

40144c: 69 c0 02 59 00 00 imul $0x5902,%eax,%eax

401452: 89 44 24 e0 mov %eax,-0x20(%rsp)

401456: 8b 44 24 d4 mov -0x2c(%rsp),%eax

40145a: 69 c0 82 dd 00 00 imul $0xdd82,%eax,%eax

401460: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401464: 8b 44 24 ec mov -0x14(%rsp),%eax

401468: 69 c0 55 31 00 00 imul $0x3155,%eax,%eax

40146e: 89 44 24 ec mov %eax,-0x14(%rsp)

401472: 8b 44 24 f4 mov -0xc(%rsp),%eax

401476: 69 c0 a7 2e 00 00 imul $0x2ea7,%eax,%eax

40147c: 89 44 24 f4 mov %eax,-0xc(%rsp)

401480: 8b 44 24 f0 mov -0x10(%rsp),%eax

401484: 69 c0 7d 26 00 00 imul $0x267d,%eax,%eax

40148a: 89 44 24 f0 mov %eax,-0x10(%rsp)

40148e: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401492: 69 c0 d0 0c 00 00 imul $0xcd0,%eax,%eax

401498: 89 44 24 d4 mov %eax,-0x2c(%rsp)

40149c: 8b 44 24 d8 mov -0x28(%rsp),%eax

4014a0: 69 c0 4c 7f 00 00 imul $0x7f4c,%eax,%eax

4014a6: 89 44 24 d8 mov %eax,-0x28(%rsp)

4014aa: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4014ae: 69 c0 27 3b 00 00 imul $0x3b27,%eax,%eax

4014b4: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4014b8: 8b 44 24 dc mov -0x24(%rsp),%eax

4014bc: 69 c0 5e ca 00 00 imul $0xca5e,%eax,%eax

4014c2: 89 44 24 dc mov %eax,-0x24(%rsp)

4014c6: 8b 44 24 d8 mov -0x28(%rsp),%eax

4014ca: 69 c0 a0 47 00 00 imul $0x47a0,%eax,%eax

4014d0: 89 44 24 d8 mov %eax,-0x28(%rsp)

4014d4: 8b 44 24 ec mov -0x14(%rsp),%eax

4014d8: 69 c0 7c 66 00 00 imul $0x667c,%eax,%eax

4014de: 89 44 24 ec mov %eax,-0x14(%rsp)

4014e2: 8b 44 24 d0 mov -0x30(%rsp),%eax

4014e6: 69 c0 51 c5 00 00 imul $0xc551,%eax,%eax

4014ec: 89 44 24 d0 mov %eax,-0x30(%rsp)

4014f0: 8b 44 24 dc mov -0x24(%rsp),%eax

4014f4: 69 c0 a1 9b 00 00 imul $0x9ba1,%eax,%eax

4014fa: 89 44 24 dc mov %eax,-0x24(%rsp)

4014fe: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401502: 69 c0 17 d6 00 00 imul $0xd617,%eax,%eax

401508: 89 44 24 d4 mov %eax,-0x2c(%rsp)

40150c: 8b 44 24 ec mov -0x14(%rsp),%eax

401510: 69 c0 f4 47 00 00 imul $0x47f4,%eax,%eax

401516: 89 44 24 ec mov %eax,-0x14(%rsp)

40151a: 8b 44 24 d8 mov -0x28(%rsp),%eax

40151e: 69 c0 81 d9 00 00 imul $0xd981,%eax,%eax

401524: 89 44 24 d8 mov %eax,-0x28(%rsp)

401528: 8b 44 24 dc mov -0x24(%rsp),%eax

40152c: 69 c0 cb 24 00 00 imul $0x24cb,%eax,%eax

401532: 89 44 24 dc mov %eax,-0x24(%rsp)

401536: 8b 44 24 d0 mov -0x30(%rsp),%eax

40153a: 69 c0 fe 29 00 00 imul $0x29fe,%eax,%eax

401540: 89 44 24 d0 mov %eax,-0x30(%rsp)

401544: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401548: 69 c0 74 ff 00 00 imul $0xff74,%eax,%eax

40154e: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401552: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401556: 69 c0 a3 9d 00 00 imul $0x9da3,%eax,%eax

40155c: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401560: 8b 44 24 d8 mov -0x28(%rsp),%eax

401564: 69 c0 99 02 00 00 imul $0x299,%eax,%eax

40156a: 89 44 24 d8 mov %eax,-0x28(%rsp)

40156e: 8b 44 24 f4 mov -0xc(%rsp),%eax

401572: 69 c0 24 4b 00 00 imul $0x4b24,%eax,%eax

401578: 89 44 24 f4 mov %eax,-0xc(%rsp)

40157c: 8b 44 24 dc mov -0x24(%rsp),%eax

401580: 69 c0 2a 17 00 00 imul $0x172a,%eax,%eax

401586: 89 44 24 dc mov %eax,-0x24(%rsp)

40158a: 8b 44 24 d0 mov -0x30(%rsp),%eax

40158e: 69 c0 a4 e2 00 00 imul $0xe2a4,%eax,%eax

401594: 89 44 24 d0 mov %eax,-0x30(%rsp)

401598: 8b 44 24 d0 mov -0x30(%rsp),%eax

40159c: 69 c0 ca 8e 00 00 imul $0x8eca,%eax,%eax

4015a2: 89 44 24 d0 mov %eax,-0x30(%rsp)

4015a6: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4015aa: 69 c0 0e 23 00 00 imul $0x230e,%eax,%eax

4015b0: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4015b4: 8b 44 24 e8 mov -0x18(%rsp),%eax

4015b8: 69 c0 4b 35 00 00 imul $0x354b,%eax,%eax

4015be: 89 44 24 e8 mov %eax,-0x18(%rsp)

4015c2: 8b 44 24 f0 mov -0x10(%rsp),%eax

4015c6: 69 c0 cf f5 00 00 imul $0xf5cf,%eax,%eax

4015cc: 89 44 24 f0 mov %eax,-0x10(%rsp)

4015d0: 8b 44 24 d8 mov -0x28(%rsp),%eax

4015d4: 69 c0 99 b8 00 00 imul $0xb899,%eax,%eax

4015da: 89 44 24 d8 mov %eax,-0x28(%rsp)

4015de: 8b 44 24 e8 mov -0x18(%rsp),%eax

4015e2: 69 c0 67 07 00 00 imul $0x767,%eax,%eax

4015e8: 89 44 24 e8 mov %eax,-0x18(%rsp)

4015ec: 8b 44 24 d8 mov -0x28(%rsp),%eax

4015f0: 69 c0 c2 28 00 00 imul $0x28c2,%eax,%eax

4015f6: 89 44 24 d8 mov %eax,-0x28(%rsp)

4015fa: 8b 44 24 ec mov -0x14(%rsp),%eax

4015fe: 69 c0 84 a1 00 00 imul $0xa184,%eax,%eax

401604: 89 44 24 ec mov %eax,-0x14(%rsp)

401608: 8b 44 24 f0 mov -0x10(%rsp),%eax

40160c: 69 c0 eb 97 00 00 imul $0x97eb,%eax,%eax

401612: 89 44 24 f0 mov %eax,-0x10(%rsp)

401616: 8b 44 24 f0 mov -0x10(%rsp),%eax

40161a: 69 c0 ae a3 00 00 imul $0xa3ae,%eax,%eax

401620: 89 44 24 f0 mov %eax,-0x10(%rsp)

401624: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401628: 69 c0 27 24 00 00 imul $0x2427,%eax,%eax

40162e: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401632: 8b 44 24 d8 mov -0x28(%rsp),%eax

401636: 69 c0 51 bd 00 00 imul $0xbd51,%eax,%eax

40163c: 89 44 24 d8 mov %eax,-0x28(%rsp)

401640: 8b 44 24 e0 mov -0x20(%rsp),%eax

401644: 69 c0 c5 eb 00 00 imul $0xebc5,%eax,%eax

40164a: 89 44 24 e0 mov %eax,-0x20(%rsp)

40164e: 8b 44 24 d0 mov -0x30(%rsp),%eax

401652: 69 c0 a6 a8 00 00 imul $0xa8a6,%eax,%eax

401658: 89 44 24 d0 mov %eax,-0x30(%rsp)

40165c: 8b 44 24 e8 mov -0x18(%rsp),%eax

401660: 69 c0 c1 8e 00 00 imul $0x8ec1,%eax,%eax

401666: 89 44 24 e8 mov %eax,-0x18(%rsp)

40166a: 8b 44 24 f0 mov -0x10(%rsp),%eax

40166e: 69 c0 78 e4 00 00 imul $0xe478,%eax,%eax

401674: 89 44 24 f0 mov %eax,-0x10(%rsp)

401678: 8b 44 24 ec mov -0x14(%rsp),%eax

40167c: 69 c0 8f b5 00 00 imul $0xb58f,%eax,%eax

401682: 89 44 24 ec mov %eax,-0x14(%rsp)

401686: 8b 44 24 e8 mov -0x18(%rsp),%eax

40168a: 69 c0 d3 12 00 00 imul $0x12d3,%eax,%eax

401690: 89 44 24 e8 mov %eax,-0x18(%rsp)

401694: 8b 44 24 d0 mov -0x30(%rsp),%eax

401698: 69 c0 e7 ef 00 00 imul $0xefe7,%eax,%eax

40169e: 89 44 24 d0 mov %eax,-0x30(%rsp)

4016a2: 8b 44 24 e0 mov -0x20(%rsp),%eax

4016a6: 69 c0 c7 bb 00 00 imul $0xbbc7,%eax,%eax

4016ac: 89 44 24 e0 mov %eax,-0x20(%rsp)

4016b0: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016b4: 69 c0 89 d4 00 00 imul $0xd489,%eax,%eax

4016ba: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016be: 8b 44 24 ec mov -0x14(%rsp),%eax

4016c2: 69 c0 0e a9 00 00 imul $0xa90e,%eax,%eax

4016c8: 89 44 24 ec mov %eax,-0x14(%rsp)

4016cc: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4016d0: 69 c0 17 a4 00 00 imul $0xa417,%eax,%eax

4016d6: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4016da: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016de: 69 c0 d8 4a 00 00 imul $0x4ad8,%eax,%eax

4016e4: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016e8: 8b 44 24 e0 mov -0x20(%rsp),%eax

4016ec: 69 c0 15 16 00 00 imul $0x1615,%eax,%eax

4016f2: 89 44 24 e0 mov %eax,-0x20(%rsp)

4016f6: 8b 44 24 ec mov -0x14(%rsp),%eax

4016fa: 69 c0 62 d3 00 00 imul $0xd362,%eax,%eax

401700: 89 44 24 ec mov %eax,-0x14(%rsp)

401704: 8b 44 24 d8 mov -0x28(%rsp),%eax

401708: 69 c0 d8 2f 00 00 imul $0x2fd8,%eax,%eax

40170e: 89 44 24 d8 mov %eax,-0x28(%rsp)

401712: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401716: 69 c0 fd 60 00 00 imul $0x60fd,%eax,%eax

40171c: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401720: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401724: 69 c0 2a 99 00 00 imul $0x992a,%eax,%eax

40172a: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40172e: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401732: 69 c0 8d 1e 00 00 imul $0x1e8d,%eax,%eax

401738: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40173c: 8b 44 24 f0 mov -0x10(%rsp),%eax

401740: 69 c0 ee 94 00 00 imul $0x94ee,%eax,%eax

401746: 89 44 24 f0 mov %eax,-0x10(%rsp)

40174a: 8b 44 24 dc mov -0x24(%rsp),%eax

40174e: 69 c0 77 45 00 00 imul $0x4577,%eax,%eax

401754: 89 44 24 dc mov %eax,-0x24(%rsp)

401758: 8b 44 24 f0 mov -0x10(%rsp),%eax

40175c: 69 c0 8d 0a 00 00 imul $0xa8d,%eax,%eax

401762: 89 44 24 f0 mov %eax,-0x10(%rsp)

401766: 8b 44 24 d0 mov -0x30(%rsp),%eax

40176a: 69 c0 57 35 00 00 imul $0x3557,%eax,%eax

401770: 89 44 24 d0 mov %eax,-0x30(%rsp)

401774: 8b 44 24 f0 mov -0x10(%rsp),%eax

401778: 69 c0 ed 60 00 00 imul $0x60ed,%eax,%eax

40177e: 89 44 24 f0 mov %eax,-0x10(%rsp)

401782: 8b 44 24 e8 mov -0x18(%rsp),%eax

401786: 69 c0 f4 a2 00 00 imul $0xa2f4,%eax,%eax

40178c: 89 44 24 e8 mov %eax,-0x18(%rsp)

401790: ba 00 00 00 00 mov $0x0,%edx

401795: b8 00 00 00 00 mov $0x0,%eax

40179a: eb 0b jmp 4017a7 <scramble+0x504>

40179c: 89 d1 mov %edx,%ecx

40179e: 8b 4c 8c d0 mov -0x30(%rsp,%rcx,4),%ecx

4017a2: 01 c8 add %ecx,%eax

4017a4: 83 c2 01 add $0x1,%edx

4017a7: 83 fa 09 cmp $0x9,%edx

4017aa: 76 f0 jbe 40179c <scramble+0x4f9>

4017ac: f3 c3 repz retq

00000000004017ae <getbuf>:

4017ae: 48 83 ec 18 sub $0x18,%rsp

4017b2: 48 89 e7 mov %rsp,%rdi

4017b5: e8 30 02 00 00 callq 4019ea <Gets>

4017ba: b8 01 00 00 00 mov $0x1,%eax

4017bf: 48 83 c4 18 add $0x18,%rsp

4017c3: c3 retq

00000000004017c4 <touch1>:

4017c4: 48 83 ec 08 sub $0x8,%rsp

4017c8: c7 05 2a 2d 20 00 01 movl $0x1,0x202d2a(%rip) # 6044fc <vlevel>

4017cf: 00 00 00

4017d2: bf 1e 2f 40 00 mov $0x402f1e,%edi

4017d7: e8 74 f4 ff ff callq 400c50 <puts@plt>

4017dc: bf 01 00 00 00 mov $0x1,%edi

4017e1: e8 f3 03 00 00 callq 401bd9 <validate>

4017e6: bf 00 00 00 00 mov $0x0,%edi

4017eb: e8 00 f6 ff ff callq 400df0 <exit@plt>

00000000004017f0 <touch2>:

4017f0: 48 83 ec 08 sub $0x8,%rsp

4017f4: 89 fe mov %edi,%esi

4017f6: c7 05 fc 2c 20 00 02 movl $0x2,0x202cfc(%rip) # 6044fc <vlevel>

4017fd: 00 00 00

401800: 3b 3d fe 2c 20 00 cmp 0x202cfe(%rip),%edi # 604504 <cookie>

401806: 75 1b jne 401823 <touch2+0x33>

401808: bf 40 2f 40 00 mov $0x402f40,%edi

40180d: b8 00 00 00 00 mov $0x0,%eax

401812: e8 69 f4 ff ff callq 400c80 <printf@plt>

401817: bf 02 00 00 00 mov $0x2,%edi

40181c: e8 b8 03 00 00 callq 401bd9 <validate>

401821: eb 19 jmp 40183c <touch2+0x4c>

401823: bf 68 2f 40 00 mov $0x402f68,%edi

401828: b8 00 00 00 00 mov $0x0,%eax

40182d: e8 4e f4 ff ff callq 400c80 <printf@plt>

401832: bf 02 00 00 00 mov $0x2,%edi

401837: e8 4f 04 00 00 callq 401c8b <fail>

40183c: bf 00 00 00 00 mov $0x0,%edi

401841: e8 aa f5 ff ff callq 400df0 <exit@plt>

0000000000401846 <hexmatch>:

401846: 41 54 push %r12

401848: 55 push %rbp

401849: 53 push %rbx

40184a: 48 83 ec 70 sub $0x70,%rsp

40184e: 41 89 fc mov %edi,%r12d

401851: 48 89 f5 mov %rsi,%rbp

401854: e8 f7 f4 ff ff callq 400d50 <random@plt>

401859: 48 89 c1 mov %rax,%rcx

40185c: 48 ba 0b d7 a3 70 3d movabs $0xa3d70a3d70a3d70b,%rdx

401863: 0a d7 a3

401866: 48 f7 ea imul %rdx

401869: 48 8d 04 0a lea (%rdx,%rcx,1),%rax

40186d: 48 c1 f8 06 sar $0x6,%rax

401871: 48 89 ce mov %rcx,%rsi

401874: 48 c1 fe 3f sar $0x3f,%rsi

401878: 48 29 f0 sub %rsi,%rax

40187b: 48 8d 04 80 lea (%rax,%rax,4),%rax

40187f: 48 8d 04 80 lea (%rax,%rax,4),%rax

401883: 48 c1 e0 02 shl $0x2,%rax

401887: 48 29 c1 sub %rax,%rcx

40188a: 48 8d 1c 0c lea (%rsp,%rcx,1),%rbx

40188e: 44 89 e2 mov %r12d,%edx

401891: be 3b 2f 40 00 mov $0x402f3b,%esi

401896: 48 89 df mov %rbx,%rdi

401899: b8 00 00 00 00 mov $0x0,%eax

40189e: e8 3d f5 ff ff callq 400de0 <sprintf@plt>

4018a3: ba 09 00 00 00 mov $0x9,%edx

4018a8: 48 89 de mov %rbx,%rsi

4018ab: 48 89 ef mov %rbp,%rdi

4018ae: e8 7d f3 ff ff callq 400c30 <strncmp@plt>

4018b3: 85 c0 test %eax,%eax

4018b5: 0f 94 c0 sete %al

4018b8: 0f b6 c0 movzbl %al,%eax

4018bb: 48 83 c4 70 add $0x70,%rsp

4018bf: 5b pop %rbx

4018c0: 5d pop %rbp

4018c1: 41 5c pop %r12

4018c3: c3 retq

00000000004018c4 <touch3>:

4018c4: 53 push %rbx

4018c5: 48 89 fb mov %rdi,%rbx

4018c8: c7 05 2a 2c 20 00 03 movl $0x3,0x202c2a(%rip) # 6044fc <vlevel>

4018cf: 00 00 00

4018d2: 48 89 fe mov %rdi,%rsi

4018d5: 8b 3d 29 2c 20 00 mov 0x202c29(%rip),%edi # 604504 <cookie>

4018db: e8 66 ff ff ff callq 401846 <hexmatch>

4018e0: 85 c0 test %eax,%eax

4018e2: 74 1e je 401902 <touch3+0x3e>

4018e4: 48 89 de mov %rbx,%rsi

4018e7: bf 90 2f 40 00 mov $0x402f90,%edi

4018ec: b8 00 00 00 00 mov $0x0,%eax

4018f1: e8 8a f3 ff ff callq 400c80 <printf@plt>

4018f6: bf 03 00 00 00 mov $0x3,%edi

4018fb: e8 d9 02 00 00 callq 401bd9 <validate>

401900: eb 1c jmp 40191e <touch3+0x5a>

401902: 48 89 de mov %rbx,%rsi

401905: bf b8 2f 40 00 mov $0x402fb8,%edi

40190a: b8 00 00 00 00 mov $0x0,%eax

40190f: e8 6c f3 ff ff callq 400c80 <printf@plt>

401914: bf 03 00 00 00 mov $0x3,%edi

401919: e8 6d 03 00 00 callq 401c8b <fail>

40191e: bf 00 00 00 00 mov $0x0,%edi

401923: e8 c8 f4 ff ff callq 400df0 <exit@plt>

0000000000401928 <test>:

401928: 48 83 ec 08 sub $0x8,%rsp

40192c: b8 00 00 00 00 mov $0x0,%eax

401931: e8 78 fe ff ff callq 4017ae <getbuf>

401936: 89 c6 mov %eax,%esi

401938: bf e0 2f 40 00 mov $0x402fe0,%edi

40193d: b8 00 00 00 00 mov $0x0,%eax

401942: e8 39 f3 ff ff callq 400c80 <printf@plt>

401947: 48 83 c4 08 add $0x8,%rsp

40194b: c3 retq

40194c: 0f 1f 40 00 nopl 0x0(%rax)

0000000000401950 <save\_char>:

401950: 8b 05 ce 37 20 00 mov 0x2037ce(%rip),%eax # 605124 <gets\_cnt>

401956: 3d ff 03 00 00 cmp $0x3ff,%eax

40195b: 7f 49 jg 4019a6 <save\_char+0x56>

40195d: 8d 14 40 lea (%rax,%rax,2),%edx

401960: 89 f9 mov %edi,%ecx

401962: c0 e9 04 shr $0x4,%cl

401965: 83 e1 0f and $0xf,%ecx

401968: 0f b6 b1 00 33 40 00 movzbl 0x403300(%rcx),%esi

40196f: 48 63 ca movslq %edx,%rcx

401972: 40 88 b1 20 45 60 00 mov %sil,0x604520(%rcx)

401979: 8d 4a 01 lea 0x1(%rdx),%ecx

40197c: 83 e7 0f and $0xf,%edi

40197f: 0f b6 b7 00 33 40 00 movzbl 0x403300(%rdi),%esi

401986: 48 63 c9 movslq %ecx,%rcx

401989: 40 88 b1 20 45 60 00 mov %sil,0x604520(%rcx)

401990: 83 c2 02 add $0x2,%edx

401993: 48 63 d2 movslq %edx,%rdx

401996: c6 82 20 45 60 00 20 movb $0x20,0x604520(%rdx)

40199d: 83 c0 01 add $0x1,%eax

4019a0: 89 05 7e 37 20 00 mov %eax,0x20377e(%rip) # 605124 <gets\_cnt>

4019a6: f3 c3 repz retq

00000000004019a8 <save\_term>:

4019a8: 8b 05 76 37 20 00 mov 0x203776(%rip),%eax # 605124 <gets\_cnt>

4019ae: 8d 04 40 lea (%rax,%rax,2),%eax

4019b1: 48 98 cltq

4019b3: c6 80 20 45 60 00 00 movb $0x0,0x604520(%rax)

4019ba: c3 retq

00000000004019bb <check\_fail>:

4019bb: 48 83 ec 08 sub $0x8,%rsp

4019bf: 0f be 35 62 37 20 00 movsbl 0x203762(%rip),%esi # 605128 <target\_prefix>

4019c6: b9 20 45 60 00 mov $0x604520,%ecx

4019cb: 8b 15 27 2b 20 00 mov 0x202b27(%rip),%edx # 6044f8 <check\_level>

4019d1: bf 03 30 40 00 mov $0x403003,%edi

4019d6: b8 00 00 00 00 mov $0x0,%eax

4019db: e8 a0 f2 ff ff callq 400c80 <printf@plt>

4019e0: bf 01 00 00 00 mov $0x1,%edi

4019e5: e8 06 f4 ff ff callq 400df0 <exit@plt>

00000000004019ea <Gets>:

4019ea: 41 54 push %r12

4019ec: 55 push %rbp

4019ed: 53 push %rbx

4019ee: 49 89 fc mov %rdi,%r12

4019f1: c7 05 29 37 20 00 00 movl $0x0,0x203729(%rip) # 605124 <gets\_cnt>

4019f8: 00 00 00

4019fb: 48 89 fb mov %rdi,%rbx

4019fe: eb 11 jmp 401a11 <Gets+0x27>

401a00: 48 8d 6b 01 lea 0x1(%rbx),%rbp

401a04: 88 03 mov %al,(%rbx)

401a06: 0f b6 f8 movzbl %al,%edi

401a09: e8 42 ff ff ff callq 401950 <save\_char>

401a0e: 48 89 eb mov %rbp,%rbx

401a11: 48 8b 3d d8 2a 20 00 mov 0x202ad8(%rip),%rdi # 6044f0 <infile>

401a18: e8 43 f3 ff ff callq 400d60 <\_IO\_getc@plt>

401a1d: 83 f8 ff cmp $0xffffffff,%eax

401a20: 74 05 je 401a27 <Gets+0x3d>

401a22: 83 f8 0a cmp $0xa,%eax

401a25: 75 d9 jne 401a00 <Gets+0x16>

401a27: c6 03 00 movb $0x0,(%rbx)

401a2a: b8 00 00 00 00 mov $0x0,%eax

401a2f: e8 74 ff ff ff callq 4019a8 <save\_term>

401a34: 4c 89 e0 mov %r12,%rax

401a37: 5b pop %rbx

401a38: 5d pop %rbp

401a39: 41 5c pop %r12

401a3b: c3 retq

0000000000401a3c <notify\_server>:

401a3c: 83 3d c5 2a 20 00 00 cmpl $0x0,0x202ac5(%rip) # 604508 <is\_checker>

401a43: 0f 85 8e 01 00 00 jne 401bd7 <notify\_server+0x19b>

401a49: 53 push %rbx

401a4a: 48 81 ec 10 40 00 00 sub $0x4010,%rsp

401a51: 89 fb mov %edi,%ebx

401a53: 8b 05 cb 36 20 00 mov 0x2036cb(%rip),%eax # 605124 <gets\_cnt>

401a59: 83 c0 64 add $0x64,%eax

401a5c: 3d 00 20 00 00 cmp $0x2000,%eax

401a61: 7e 19 jle 401a7c <notify\_server+0x40>

401a63: bf 38 31 40 00 mov $0x403138,%edi

401a68: b8 00 00 00 00 mov $0x0,%eax

401a6d: e8 0e f2 ff ff callq 400c80 <printf@plt>

401a72: bf 01 00 00 00 mov $0x1,%edi

401a77: e8 74 f3 ff ff callq 400df0 <exit@plt>

401a7c: 44 0f be 0d a4 36 20 movsbl 0x2036a4(%rip),%r9d # 605128 <target\_prefix>

401a83: 00

401a84: 83 3d 1d 2a 20 00 00 cmpl $0x0,0x202a1d(%rip) # 6044a8 <notify>

401a8b: 74 09 je 401a96 <notify\_server+0x5a>

401a8d: 44 8b 05 6c 2a 20 00 mov 0x202a6c(%rip),%r8d # 604500 <authkey>

401a94: eb 06 jmp 401a9c <notify\_server+0x60>

401a96: 41 b8 ff ff ff ff mov $0xffffffff,%r8d

401a9c: 85 db test %ebx,%ebx

401a9e: 74 07 je 401aa7 <notify\_server+0x6b>

401aa0: b9 19 30 40 00 mov $0x403019,%ecx

401aa5: eb 05 jmp 401aac <notify\_server+0x70>

401aa7: b9 1e 30 40 00 mov $0x40301e,%ecx

401aac: 48 c7 44 24 08 20 45 movq $0x604520,0x8(%rsp)

401ab3: 60 00

401ab5: 89 34 24 mov %esi,(%rsp)

401ab8: 8b 15 aa 26 20 00 mov 0x2026aa(%rip),%edx # 604168 <target\_id>

401abe: be 23 30 40 00 mov $0x403023,%esi

401ac3: 48 8d bc 24 10 20 00 lea 0x2010(%rsp),%rdi

401aca: 00

401acb: b8 00 00 00 00 mov $0x0,%eax

401ad0: e8 0b f3 ff ff callq 400de0 <sprintf@plt>

401ad5: 83 3d cc 29 20 00 00 cmpl $0x0,0x2029cc(%rip) # 6044a8 <notify>

401adc: 74 78 je 401b56 <notify\_server+0x11a>

401ade: 85 db test %ebx,%ebx

401ae0: 74 68 je 401b4a <notify\_server+0x10e>

401ae2: 4c 8d 4c 24 10 lea 0x10(%rsp),%r9

401ae7: 41 b8 00 00 00 00 mov $0x0,%r8d

401aed: 48 8d 8c 24 10 20 00 lea 0x2010(%rsp),%rcx

401af4: 00

401af5: 48 8b 15 74 26 20 00 mov 0x202674(%rip),%rdx # 604170 <lab>

401afc: 48 8b 35 75 26 20 00 mov 0x202675(%rip),%rsi # 604178 <course>

401b03: 48 8b 3d 56 26 20 00 mov 0x202656(%rip),%rdi # 604160 <user\_id>

401b0a: e8 94 0f 00 00 callq 402aa3 <driver\_post>

401b0f: 85 c0 test %eax,%eax

401b11: 79 1e jns 401b31 <notify\_server+0xf5>

401b13: 48 8d 74 24 10 lea 0x10(%rsp),%rsi

401b18: bf 3f 30 40 00 mov $0x40303f,%edi

401b1d: b8 00 00 00 00 mov $0x0,%eax

401b22: e8 59 f1 ff ff callq 400c80 <printf@plt>

401b27: bf 01 00 00 00 mov $0x1,%edi

401b2c: e8 bf f2 ff ff callq 400df0 <exit@plt>

401b31: bf 68 31 40 00 mov $0x403168,%edi

401b36: e8 15 f1 ff ff callq 400c50 <puts@plt>

401b3b: bf 4b 30 40 00 mov $0x40304b,%edi

401b40: e8 0b f1 ff ff callq 400c50 <puts@plt>

401b45: e9 85 00 00 00 jmpq 401bcf <notify\_server+0x193>

401b4a: bf 55 30 40 00 mov $0x403055,%edi

401b4f: e8 fc f0 ff ff callq 400c50 <puts@plt>

401b54: eb 79 jmp 401bcf <notify\_server+0x193>

401b56: 85 db test %ebx,%ebx

401b58: 74 08 je 401b62 <notify\_server+0x126>

401b5a: be 19 30 40 00 mov $0x403019,%esi

401b5f: 90 nop

401b60: eb 05 jmp 401b67 <notify\_server+0x12b>

401b62: be 1e 30 40 00 mov $0x40301e,%esi

401b67: bf a0 31 40 00 mov $0x4031a0,%edi

401b6c: b8 00 00 00 00 mov $0x0,%eax

401b71: e8 0a f1 ff ff callq 400c80 <printf@plt>

401b76: 48 8b 35 e3 25 20 00 mov 0x2025e3(%rip),%rsi # 604160 <user\_id>

401b7d: bf 5c 30 40 00 mov $0x40305c,%edi

401b82: b8 00 00 00 00 mov $0x0,%eax

401b87: e8 f4 f0 ff ff callq 400c80 <printf@plt>

401b8c: 48 8b 35 e5 25 20 00 mov 0x2025e5(%rip),%rsi # 604178 <course>

401b93: bf 69 30 40 00 mov $0x403069,%edi

401b98: b8 00 00 00 00 mov $0x0,%eax

401b9d: e8 de f0 ff ff callq 400c80 <printf@plt>

401ba2: 48 8b 35 c7 25 20 00 mov 0x2025c7(%rip),%rsi # 604170 <lab>

401ba9: bf 75 30 40 00 mov $0x403075,%edi

401bae: b8 00 00 00 00 mov $0x0,%eax

401bb3: e8 c8 f0 ff ff callq 400c80 <printf@plt>

401bb8: 48 8d b4 24 10 20 00 lea 0x2010(%rsp),%rsi

401bbf: 00

401bc0: bf 7e 30 40 00 mov $0x40307e,%edi

401bc5: b8 00 00 00 00 mov $0x0,%eax

401bca: e8 b1 f0 ff ff callq 400c80 <printf@plt>

401bcf: 48 81 c4 10 40 00 00 add $0x4010,%rsp

401bd6: 5b pop %rbx

401bd7: f3 c3 repz retq

0000000000401bd9 <validate>:

401bd9: 53 push %rbx

401bda: 89 fb mov %edi,%ebx

401bdc: 83 3d 25 29 20 00 00 cmpl $0x0,0x202925(%rip) # 604508 <is\_checker>

401be3: 74 60 je 401c45 <validate+0x6c>

401be5: 39 3d 11 29 20 00 cmp %edi,0x202911(%rip) # 6044fc <vlevel>

401beb: 74 14 je 401c01 <validate+0x28>

401bed: bf 8a 30 40 00 mov $0x40308a,%edi

401bf2: e8 59 f0 ff ff callq 400c50 <puts@plt>

401bf7: b8 00 00 00 00 mov $0x0,%eax

401bfc: e8 ba fd ff ff callq 4019bb <check\_fail>

401c01: 8b 35 f1 28 20 00 mov 0x2028f1(%rip),%esi # 6044f8 <check\_level>

401c07: 39 fe cmp %edi,%esi

401c09: 74 1b je 401c26 <validate+0x4d>

401c0b: 89 fa mov %edi,%edx

401c0d: bf c8 31 40 00 mov $0x4031c8,%edi

401c12: b8 00 00 00 00 mov $0x0,%eax

401c17: e8 64 f0 ff ff callq 400c80 <printf@plt>

401c1c: b8 00 00 00 00 mov $0x0,%eax

401c21: e8 95 fd ff ff callq 4019bb <check\_fail>

401c26: 0f be 35 fb 34 20 00 movsbl 0x2034fb(%rip),%esi # 605128 <target\_prefix>

401c2d: b9 20 45 60 00 mov $0x604520,%ecx

401c32: 89 fa mov %edi,%edx

401c34: bf a8 30 40 00 mov $0x4030a8,%edi

401c39: b8 00 00 00 00 mov $0x0,%eax

401c3e: e8 3d f0 ff ff callq 400c80 <printf@plt>

401c43: eb 44 jmp 401c89 <validate+0xb0>

401c45: 39 3d b1 28 20 00 cmp %edi,0x2028b1(%rip) # 6044fc <vlevel>

401c4b: 74 18 je 401c65 <validate+0x8c>

401c4d: bf 8a 30 40 00 mov $0x40308a,%edi

401c52: e8 f9 ef ff ff callq 400c50 <puts@plt>

401c57: 89 de mov %ebx,%esi

401c59: bf 00 00 00 00 mov $0x0,%edi

401c5e: e8 d9 fd ff ff callq 401a3c <notify\_server>

401c63: eb 24 jmp 401c89 <validate+0xb0>

401c65: 0f be 15 bc 34 20 00 movsbl 0x2034bc(%rip),%edx # 605128 <target\_prefix>

401c6c: 89 fe mov %edi,%esi

401c6e: bf f0 31 40 00 mov $0x4031f0,%edi

401c73: b8 00 00 00 00 mov $0x0,%eax

401c78: e8 03 f0 ff ff callq 400c80 <printf@plt>

401c7d: 89 de mov %ebx,%esi

401c7f: bf 01 00 00 00 mov $0x1,%edi

401c84: e8 b3 fd ff ff callq 401a3c <notify\_server>

401c89: 5b pop %rbx

401c8a: c3 retq

0000000000401c8b <fail>:

401c8b: 48 83 ec 08 sub $0x8,%rsp

401c8f: 83 3d 72 28 20 00 00 cmpl $0x0,0x202872(%rip) # 604508 <is\_checker>

401c96: 74 0a je 401ca2 <fail+0x17>

401c98: b8 00 00 00 00 mov $0x0,%eax

401c9d: e8 19 fd ff ff callq 4019bb <check\_fail>

401ca2: 89 fe mov %edi,%esi

401ca4: bf 00 00 00 00 mov $0x0,%edi

401ca9: e8 8e fd ff ff callq 401a3c <notify\_server>

401cae: 48 83 c4 08 add $0x8,%rsp

401cb2: c3 retq

0000000000401cb3 <bushandler>:

401cb3: 48 83 ec 08 sub $0x8,%rsp

401cb7: 83 3d 4a 28 20 00 00 cmpl $0x0,0x20284a(%rip) # 604508 <is\_checker>

401cbe: 74 14 je 401cd4 <bushandler+0x21>

401cc0: bf bd 30 40 00 mov $0x4030bd,%edi

401cc5: e8 86 ef ff ff callq 400c50 <puts@plt>

401cca: b8 00 00 00 00 mov $0x0,%eax

401ccf: e8 e7 fc ff ff callq 4019bb <check\_fail>

401cd4: bf 28 32 40 00 mov $0x403228,%edi

401cd9: e8 72 ef ff ff callq 400c50 <puts@plt>

401cde: bf c7 30 40 00 mov $0x4030c7,%edi

401ce3: e8 68 ef ff ff callq 400c50 <puts@plt>

401ce8: be 00 00 00 00 mov $0x0,%esi

401ced: bf 00 00 00 00 mov $0x0,%edi

401cf2: e8 45 fd ff ff callq 401a3c <notify\_server>

401cf7: bf 01 00 00 00 mov $0x1,%edi

401cfc: e8 ef f0 ff ff callq 400df0 <exit@plt>

0000000000401d01 <seghandler>:

401d01: 48 83 ec 08 sub $0x8,%rsp

401d05: 83 3d fc 27 20 00 00 cmpl $0x0,0x2027fc(%rip) # 604508 <is\_checker>

401d0c: 74 14 je 401d22 <seghandler+0x21>

401d0e: bf dd 30 40 00 mov $0x4030dd,%edi

401d13: e8 38 ef ff ff callq 400c50 <puts@plt>

401d18: b8 00 00 00 00 mov $0x0,%eax

401d1d: e8 99 fc ff ff callq 4019bb <check\_fail>

401d22: bf 48 32 40 00 mov $0x403248,%edi

401d27: e8 24 ef ff ff callq 400c50 <puts@plt>

401d2c: bf c7 30 40 00 mov $0x4030c7,%edi

401d31: e8 1a ef ff ff callq 400c50 <puts@plt>

401d36: be 00 00 00 00 mov $0x0,%esi

401d3b: bf 00 00 00 00 mov $0x0,%edi

401d40: e8 f7 fc ff ff callq 401a3c <notify\_server>

401d45: bf 01 00 00 00 mov $0x1,%edi

401d4a: e8 a1 f0 ff ff callq 400df0 <exit@plt>

0000000000401d4f <illegalhandler>:

401d4f: 48 83 ec 08 sub $0x8,%rsp

401d53: 83 3d ae 27 20 00 00 cmpl $0x0,0x2027ae(%rip) # 604508 <is\_checker>

401d5a: 74 14 je 401d70 <illegalhandler+0x21>

401d5c: bf f0 30 40 00 mov $0x4030f0,%edi

401d61: e8 ea ee ff ff callq 400c50 <puts@plt>

401d66: b8 00 00 00 00 mov $0x0,%eax

401d6b: e8 4b fc ff ff callq 4019bb <check\_fail>

401d70: bf 70 32 40 00 mov $0x403270,%edi

401d75: e8 d6 ee ff ff callq 400c50 <puts@plt>

401d7a: bf c7 30 40 00 mov $0x4030c7,%edi

401d7f: e8 cc ee ff ff callq 400c50 <puts@plt>

401d84: be 00 00 00 00 mov $0x0,%esi

401d89: bf 00 00 00 00 mov $0x0,%edi

401d8e: e8 a9 fc ff ff callq 401a3c <notify\_server>

401d93: bf 01 00 00 00 mov $0x1,%edi

401d98: e8 53 f0 ff ff callq 400df0 <exit@plt>

0000000000401d9d <sigalrmhandler>:

401d9d: 48 83 ec 08 sub $0x8,%rsp

401da1: 83 3d 60 27 20 00 00 cmpl $0x0,0x202760(%rip) # 604508 <is\_checker>

401da8: 74 14 je 401dbe <sigalrmhandler+0x21>

401daa: bf 04 31 40 00 mov $0x403104,%edi

401daf: e8 9c ee ff ff callq 400c50 <puts@plt>

401db4: b8 00 00 00 00 mov $0x0,%eax

401db9: e8 fd fb ff ff callq 4019bb <check\_fail>

401dbe: be 05 00 00 00 mov $0x5,%esi

401dc3: bf a0 32 40 00 mov $0x4032a0,%edi

401dc8: b8 00 00 00 00 mov $0x0,%eax

401dcd: e8 ae ee ff ff callq 400c80 <printf@plt>

401dd2: be 00 00 00 00 mov $0x0,%esi

401dd7: bf 00 00 00 00 mov $0x0,%edi

401ddc: e8 5b fc ff ff callq 401a3c <notify\_server>

401de1: bf 01 00 00 00 mov $0x1,%edi

401de6: e8 05 f0 ff ff callq 400df0 <exit@plt>

0000000000401deb <launch>:

401deb: 55 push %rbp

401dec: 48 89 e5 mov %rsp,%rbp

401def: 48 89 fa mov %rdi,%rdx

401df2: 48 8d 47 1e lea 0x1e(%rdi),%rax

401df6: 48 83 e0 f0 and $0xfffffffffffffff0,%rax

401dfa: 48 29 c4 sub %rax,%rsp

401dfd: 48 8d 7c 24 0f lea 0xf(%rsp),%rdi

401e02: 48 83 e7 f0 and $0xfffffffffffffff0,%rdi

401e06: be f4 00 00 00 mov $0xf4,%esi

401e0b: e8 80 ee ff ff callq 400c90 <memset@plt>

401e10: 48 8b 05 a9 26 20 00 mov 0x2026a9(%rip),%rax # 6044c0 <stdin@@GLIBC\_2.2.5>

401e17: 48 39 05 d2 26 20 00 cmp %rax,0x2026d2(%rip) # 6044f0 <infile>

401e1e: 75 0f jne 401e2f <launch+0x44>

401e20: bf 0c 31 40 00 mov $0x40310c,%edi

401e25: b8 00 00 00 00 mov $0x0,%eax

401e2a: e8 51 ee ff ff callq 400c80 <printf@plt>

401e2f: c7 05 c3 26 20 00 00 movl $0x0,0x2026c3(%rip) # 6044fc <vlevel>

401e36: 00 00 00

401e39: b8 00 00 00 00 mov $0x0,%eax

401e3e: e8 e5 fa ff ff callq 401928 <test>

401e43: 83 3d be 26 20 00 00 cmpl $0x0,0x2026be(%rip) # 604508 <is\_checker>

401e4a: 74 14 je 401e60 <launch+0x75>

401e4c: bf 19 31 40 00 mov $0x403119,%edi

401e51: e8 fa ed ff ff callq 400c50 <puts@plt>

401e56: b8 00 00 00 00 mov $0x0,%eax

401e5b: e8 5b fb ff ff callq 4019bb <check\_fail>

401e60: bf 24 31 40 00 mov $0x403124,%edi

401e65: e8 e6 ed ff ff callq 400c50 <puts@plt>

401e6a: c9 leaveq

401e6b: c3 retq

0000000000401e6c <stable\_launch>:

401e6c: 53 push %rbx

401e6d: 48 89 3d 74 26 20 00 mov %rdi,0x202674(%rip) # 6044e8 <global\_offset>

401e74: 41 b9 00 00 00 00 mov $0x0,%r9d

401e7a: 41 b8 00 00 00 00 mov $0x0,%r8d

401e80: b9 32 01 00 00 mov $0x132,%ecx

401e85: ba 07 00 00 00 mov $0x7,%edx

401e8a: be 00 00 10 00 mov $0x100000,%esi

401e8f: bf 00 60 58 55 mov $0x55586000,%edi

401e94: e8 d7 ed ff ff callq 400c70 <mmap@plt>

401e99: 48 89 c3 mov %rax,%rbx

401e9c: 48 3d 00 60 58 55 cmp $0x55586000,%rax

401ea2: 74 32 je 401ed6 <stable\_launch+0x6a>

401ea4: be 00 00 10 00 mov $0x100000,%esi

401ea9: 48 89 c7 mov %rax,%rdi

401eac: e8 cf ee ff ff callq 400d80 <munmap@plt>

401eb1: ba 00 60 58 55 mov $0x55586000,%edx

401eb6: be d8 32 40 00 mov $0x4032d8,%esi

401ebb: 48 8b 3d 0e 26 20 00 mov 0x20260e(%rip),%rdi # 6044d0 <stderr@@GLIBC\_2.2.5>

401ec2: b8 00 00 00 00 mov $0x0,%eax

401ec7: e8 34 ee ff ff callq 400d00 <fprintf@plt>

401ecc: bf 01 00 00 00 mov $0x1,%edi

401ed1: e8 1a ef ff ff callq 400df0 <exit@plt>

401ed6: 48 8d 90 f8 ff 0f 00 lea 0xffff8(%rax),%rdx

401edd: 48 89 15 4c 32 20 00 mov %rdx,0x20324c(%rip) # 605130 <stack\_top>

401ee4: 48 89 e0 mov %rsp,%rax

401ee7: 48 89 d4 mov %rdx,%rsp

401eea: 48 89 c2 mov %rax,%rdx

401eed: 48 89 15 ec 25 20 00 mov %rdx,0x2025ec(%rip) # 6044e0 <global\_save\_stack>

401ef4: 48 8b 3d ed 25 20 00 mov 0x2025ed(%rip),%rdi # 6044e8 <global\_offset>

401efb: e8 eb fe ff ff callq 401deb <launch>

401f00: 48 8b 05 d9 25 20 00 mov 0x2025d9(%rip),%rax # 6044e0 <global\_save\_stack>

401f07: 48 89 c4 mov %rax,%rsp

401f0a: be 00 00 10 00 mov $0x100000,%esi

401f0f: 48 89 df mov %rbx,%rdi

401f12: e8 69 ee ff ff callq 400d80 <munmap@plt>

401f17: 5b pop %rbx

401f18: c3 retq

401f19: 0f 1f 80 00 00 00 00 nopl 0x0(%rax)

0000000000401f20 <rio\_readinitb>:

401f20: 89 37 mov %esi,(%rdi)

401f22: c7 47 04 00 00 00 00 movl $0x0,0x4(%rdi)

401f29: 48 8d 47 10 lea 0x10(%rdi),%rax

401f2d: 48 89 47 08 mov %rax,0x8(%rdi)

401f31: c3 retq

0000000000401f32 <sigalrm\_handler>:

401f32: 48 83 ec 08 sub $0x8,%rsp

401f36: ba 00 00 00 00 mov $0x0,%edx

401f3b: be 10 33 40 00 mov $0x403310,%esi

401f40: 48 8b 3d 89 25 20 00 mov 0x202589(%rip),%rdi # 6044d0 <stderr@@GLIBC\_2.2.5>

401f47: b8 00 00 00 00 mov $0x0,%eax

401f4c: e8 af ed ff ff callq 400d00 <fprintf@plt>

401f51: bf 01 00 00 00 mov $0x1,%edi

401f56: e8 95 ee ff ff callq 400df0 <exit@plt>

0000000000401f5b <urlencode>:

401f5b: 41 54 push %r12

401f5d: 55 push %rbp

401f5e: 53 push %rbx

401f5f: 48 83 ec 10 sub $0x10,%rsp

401f63: 48 89 fb mov %rdi,%rbx

401f66: 48 89 f5 mov %rsi,%rbp

401f69: b8 00 00 00 00 mov $0x0,%eax

401f6e: 48 c7 c1 ff ff ff ff mov $0xffffffffffffffff,%rcx

401f75: f2 ae repnz scas %es:(%rdi),%al

401f77: 48 f7 d1 not %rcx

401f7a: 8d 41 ff lea -0x1(%rcx),%eax

401f7d: e9 93 00 00 00 jmpq 402015 <urlencode+0xba>

401f82: 0f b6 13 movzbl (%rbx),%edx

401f85: 80 fa 2a cmp $0x2a,%dl

401f88: 0f 94 c1 sete %cl

401f8b: 80 fa 2d cmp $0x2d,%dl

401f8e: 0f 94 c0 sete %al

401f91: 08 c1 or %al,%cl

401f93: 75 1f jne 401fb4 <urlencode+0x59>

401f95: 80 fa 2e cmp $0x2e,%dl

401f98: 74 1a je 401fb4 <urlencode+0x59>

401f9a: 80 fa 5f cmp $0x5f,%dl

401f9d: 74 15 je 401fb4 <urlencode+0x59>

401f9f: 8d 42 d0 lea -0x30(%rdx),%eax

401fa2: 3c 09 cmp $0x9,%al

401fa4: 76 0e jbe 401fb4 <urlencode+0x59>

401fa6: 8d 42 bf lea -0x41(%rdx),%eax

401fa9: 3c 19 cmp $0x19,%al

401fab: 76 07 jbe 401fb4 <urlencode+0x59>

401fad: 8d 42 9f lea -0x61(%rdx),%eax

401fb0: 3c 19 cmp $0x19,%al

401fb2: 77 09 ja 401fbd <urlencode+0x62>

401fb4: 88 55 00 mov %dl,0x0(%rbp)

401fb7: 48 8d 6d 01 lea 0x1(%rbp),%rbp

401fbb: eb 51 jmp 40200e <urlencode+0xb3>

401fbd: 80 fa 20 cmp $0x20,%dl

401fc0: 75 0a jne 401fcc <urlencode+0x71>

401fc2: c6 45 00 2b movb $0x2b,0x0(%rbp)

401fc6: 48 8d 6d 01 lea 0x1(%rbp),%rbp

401fca: eb 42 jmp 40200e <urlencode+0xb3>

401fcc: 8d 42 e0 lea -0x20(%rdx),%eax

401fcf: 3c 5f cmp $0x5f,%al

401fd1: 0f 96 c1 setbe %cl

401fd4: 80 fa 09 cmp $0x9,%dl

401fd7: 0f 94 c0 sete %al

401fda: 08 c1 or %al,%cl

401fdc: 74 45 je 402023 <urlencode+0xc8>

401fde: 0f b6 d2 movzbl %dl,%edx

401fe1: be a8 33 40 00 mov $0x4033a8,%esi

401fe6: 48 89 e7 mov %rsp,%rdi

401fe9: b8 00 00 00 00 mov $0x0,%eax

401fee: e8 ed ed ff ff callq 400de0 <sprintf@plt>

401ff3: 0f b6 04 24 movzbl (%rsp),%eax

401ff7: 88 45 00 mov %al,0x0(%rbp)

401ffa: 0f b6 44 24 01 movzbl 0x1(%rsp),%eax

401fff: 88 45 01 mov %al,0x1(%rbp)

402002: 0f b6 44 24 02 movzbl 0x2(%rsp),%eax

402007: 88 45 02 mov %al,0x2(%rbp)

40200a: 48 8d 6d 03 lea 0x3(%rbp),%rbp

40200e: 48 83 c3 01 add $0x1,%rbx

402012: 44 89 e0 mov %r12d,%eax

402015: 44 8d 60 ff lea -0x1(%rax),%r12d

402019: 85 c0 test %eax,%eax

40201b: 0f 85 61 ff ff ff jne 401f82 <urlencode+0x27>

402021: eb 05 jmp 402028 <urlencode+0xcd>

402023: b8 ff ff ff ff mov $0xffffffff,%eax

402028: 48 83 c4 10 add $0x10,%rsp

40202c: 5b pop %rbx

40202d: 5d pop %rbp

40202e: 41 5c pop %r12

402030: c3 retq

0000000000402031 <rio\_writen>:

402031: 41 55 push %r13

402033: 41 54 push %r12

402035: 55 push %rbp

402036: 53 push %rbx

402037: 48 83 ec 08 sub $0x8,%rsp

40203b: 41 89 fc mov %edi,%r12d

40203e: 48 89 f5 mov %rsi,%rbp

402041: 49 89 d5 mov %rdx,%r13

402044: 48 89 d3 mov %rdx,%rbx

402047: eb 28 jmp 402071 <rio\_writen+0x40>

402049: 48 89 da mov %rbx,%rdx

40204c: 48 89 ee mov %rbp,%rsi

40204f: 44 89 e7 mov %r12d,%edi

402052: e8 09 ec ff ff callq 400c60 <write@plt>

402057: 48 85 c0 test %rax,%rax

40205a: 7f 0f jg 40206b <rio\_writen+0x3a>

40205c: e8 af eb ff ff callq 400c10 <\_\_errno\_location@plt>

402061: 83 38 04 cmpl $0x4,(%rax)

402064: 75 15 jne 40207b <rio\_writen+0x4a>

402066: b8 00 00 00 00 mov $0x0,%eax

40206b: 48 29 c3 sub %rax,%rbx

40206e: 48 01 c5 add %rax,%rbp

402071: 48 85 db test %rbx,%rbx

402074: 75 d3 jne 402049 <rio\_writen+0x18>

402076: 4c 89 e8 mov %r13,%rax

402079: eb 07 jmp 402082 <rio\_writen+0x51>

40207b: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

402082: 48 83 c4 08 add $0x8,%rsp

402086: 5b pop %rbx

402087: 5d pop %rbp

402088: 41 5c pop %r12

40208a: 41 5d pop %r13

40208c: c3 retq

000000000040208d <rio\_read>:

40208d: 41 56 push %r14

40208f: 41 55 push %r13

402091: 41 54 push %r12

402093: 55 push %rbp

402094: 53 push %rbx

402095: 48 89 fb mov %rdi,%rbx

402098: 49 89 f6 mov %rsi,%r14

40209b: 49 89 d5 mov %rdx,%r13

40209e: 4c 8d 67 10 lea 0x10(%rdi),%r12

4020a2: eb 2a jmp 4020ce <rio\_read+0x41>

4020a4: ba 00 20 00 00 mov $0x2000,%edx

4020a9: 4c 89 e6 mov %r12,%rsi

4020ac: 8b 3b mov (%rbx),%edi

4020ae: e8 0d ec ff ff callq 400cc0 <read@plt>

4020b3: 89 43 04 mov %eax,0x4(%rbx)

4020b6: 85 c0 test %eax,%eax

4020b8: 79 0c jns 4020c6 <rio\_read+0x39>

4020ba: e8 51 eb ff ff callq 400c10 <\_\_errno\_location@plt>

4020bf: 83 38 04 cmpl $0x4,(%rax)

4020c2: 74 0a je 4020ce <rio\_read+0x41>

4020c4: eb 37 jmp 4020fd <rio\_read+0x70>

4020c6: 85 c0 test %eax,%eax

4020c8: 74 3c je 402106 <rio\_read+0x79>

4020ca: 4c 89 63 08 mov %r12,0x8(%rbx)

4020ce: 8b 6b 04 mov 0x4(%rbx),%ebp

4020d1: 85 ed test %ebp,%ebp

4020d3: 7e cf jle 4020a4 <rio\_read+0x17>

4020d5: 89 e8 mov %ebp,%eax

4020d7: 4c 39 e8 cmp %r13,%rax

4020da: 72 03 jb 4020df <rio\_read+0x52>

4020dc: 44 89 ed mov %r13d,%ebp

4020df: 4c 63 e5 movslq %ebp,%r12

4020e2: 48 8b 73 08 mov 0x8(%rbx),%rsi

4020e6: 4c 89 e2 mov %r12,%rdx

4020e9: 4c 89 f7 mov %r14,%rdi

4020ec: e8 3f ec ff ff callq 400d30 <memcpy@plt>

4020f1: 4c 01 63 08 add %r12,0x8(%rbx)

4020f5: 29 6b 04 sub %ebp,0x4(%rbx)

4020f8: 4c 89 e0 mov %r12,%rax

4020fb: eb 0e jmp 40210b <rio\_read+0x7e>

4020fd: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

402104: eb 05 jmp 40210b <rio\_read+0x7e>

402106: b8 00 00 00 00 mov $0x0,%eax

40210b: 5b pop %rbx

40210c: 5d pop %rbp

40210d: 41 5c pop %r12

40210f: 41 5d pop %r13

402111: 41 5e pop %r14

402113: c3 retq

0000000000402114 <rio\_readlineb>:

402114: 41 55 push %r13

402116: 41 54 push %r12

402118: 55 push %rbp

402119: 53 push %rbx

40211a: 48 83 ec 18 sub $0x18,%rsp

40211e: 49 89 fd mov %rdi,%r13

402121: 48 89 f5 mov %rsi,%rbp

402124: 49 89 d4 mov %rdx,%r12

402127: bb 01 00 00 00 mov $0x1,%ebx

40212c: eb 3d jmp 40216b <rio\_readlineb+0x57>

40212e: ba 01 00 00 00 mov $0x1,%edx

402133: 48 8d 74 24 0f lea 0xf(%rsp),%rsi

402138: 4c 89 ef mov %r13,%rdi

40213b: e8 4d ff ff ff callq 40208d <rio\_read>

402140: 83 f8 01 cmp $0x1,%eax

402143: 75 12 jne 402157 <rio\_readlineb+0x43>

402145: 48 8d 55 01 lea 0x1(%rbp),%rdx

402149: 0f b6 44 24 0f movzbl 0xf(%rsp),%eax

40214e: 88 45 00 mov %al,0x0(%rbp)

402151: 3c 0a cmp $0xa,%al

402153: 75 0f jne 402164 <rio\_readlineb+0x50>

402155: eb 1b jmp 402172 <rio\_readlineb+0x5e>

402157: 85 c0 test %eax,%eax

402159: 75 23 jne 40217e <rio\_readlineb+0x6a>

40215b: 48 83 fb 01 cmp $0x1,%rbx

40215f: 90 nop

402160: 75 13 jne 402175 <rio\_readlineb+0x61>

402162: eb 23 jmp 402187 <rio\_readlineb+0x73>

402164: 48 83 c3 01 add $0x1,%rbx

402168: 48 89 d5 mov %rdx,%rbp

40216b: 4c 39 e3 cmp %r12,%rbx

40216e: 72 be jb 40212e <rio\_readlineb+0x1a>

402170: eb 03 jmp 402175 <rio\_readlineb+0x61>

402172: 48 89 d5 mov %rdx,%rbp

402175: c6 45 00 00 movb $0x0,0x0(%rbp)

402179: 48 89 d8 mov %rbx,%rax

40217c: eb 0e jmp 40218c <rio\_readlineb+0x78>

40217e: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

402185: eb 05 jmp 40218c <rio\_readlineb+0x78>

402187: b8 00 00 00 00 mov $0x0,%eax

40218c: 48 83 c4 18 add $0x18,%rsp

402190: 5b pop %rbx

402191: 5d pop %rbp

402192: 41 5c pop %r12

402194: 41 5d pop %r13

402196: c3 retq

0000000000402197 <submitr>:

402197: 41 57 push %r15

402199: 41 56 push %r14

40219b: 41 55 push %r13

40219d: 41 54 push %r12

40219f: 55 push %rbp

4021a0: 53 push %rbx

4021a1: 48 81 ec 48 a0 00 00 sub $0xa048,%rsp

4021a8: 49 89 fc mov %rdi,%r12

4021ab: 89 74 24 04 mov %esi,0x4(%rsp)

4021af: 49 89 d7 mov %rdx,%r15

4021b2: 49 89 ce mov %rcx,%r14

4021b5: 4c 89 44 24 08 mov %r8,0x8(%rsp)

4021ba: 4d 89 cd mov %r9,%r13

4021bd: 48 8b 9c 24 80 a0 00 mov 0xa080(%rsp),%rbx

4021c4: 00

4021c5: c7 84 24 1c 20 00 00 movl $0x0,0x201c(%rsp)

4021cc: 00 00 00 00

4021d0: ba 00 00 00 00 mov $0x0,%edx

4021d5: be 01 00 00 00 mov $0x1,%esi

4021da: bf 02 00 00 00 mov $0x2,%edi

4021df: e8 2c ec ff ff callq 400e10 <socket@plt>

4021e4: 89 c5 mov %eax,%ebp

4021e6: 85 c0 test %eax,%eax

4021e8: 79 4e jns 402238 <submitr+0xa1>

4021ea: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

4021f1: 3a 20 43

4021f4: 48 89 03 mov %rax,(%rbx)

4021f7: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

4021fe: 20 75 6e

402201: 48 89 43 08 mov %rax,0x8(%rbx)

402205: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

40220c: 74 6f 20

40220f: 48 89 43 10 mov %rax,0x10(%rbx)

402213: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax

40221a: 65 20 73

40221d: 48 89 43 18 mov %rax,0x18(%rbx)

402221: c7 43 20 6f 63 6b 65 movl $0x656b636f,0x20(%rbx)

402228: 66 c7 43 24 74 00 movw $0x74,0x24(%rbx)

40222e: b8 ff ff ff ff mov $0xffffffff,%eax

402233: e9 68 06 00 00 jmpq 4028a0 <submitr+0x709>

402238: 4c 89 e7 mov %r12,%rdi

40223b: e8 b0 ea ff ff callq 400cf0 <gethostbyname@plt>

402240: 48 85 c0 test %rax,%rax

402243: 75 67 jne 4022ac <submitr+0x115>

402245: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax

40224c: 3a 20 44

40224f: 48 89 03 mov %rax,(%rbx)

402252: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax

402259: 20 75 6e

40225c: 48 89 43 08 mov %rax,0x8(%rbx)

402260: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402267: 74 6f 20

40226a: 48 89 43 10 mov %rax,0x10(%rbx)

40226e: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax

402275: 76 65 20

402278: 48 89 43 18 mov %rax,0x18(%rbx)

40227c: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax

402283: 72 20 61

402286: 48 89 43 20 mov %rax,0x20(%rbx)

40228a: c7 43 28 64 64 72 65 movl $0x65726464,0x28(%rbx)

402291: 66 c7 43 2c 73 73 movw $0x7373,0x2c(%rbx)

402297: c6 43 2e 00 movb $0x0,0x2e(%rbx)

40229b: 89 ef mov %ebp,%edi

40229d: e8 0e ea ff ff callq 400cb0 <close@plt>

4022a2: b8 ff ff ff ff mov $0xffffffff,%eax

4022a7: e9 f4 05 00 00 jmpq 4028a0 <submitr+0x709>

4022ac: 48 c7 84 24 30 a0 00 movq $0x0,0xa030(%rsp)

4022b3: 00 00 00 00 00

4022b8: 48 c7 84 24 38 a0 00 movq $0x0,0xa038(%rsp)

4022bf: 00 00 00 00 00

4022c4: 66 c7 84 24 30 a0 00 movw $0x2,0xa030(%rsp)

4022cb: 00 02 00

4022ce: 48 8b 48 18 mov 0x18(%rax),%rcx

4022d2: 48 63 50 14 movslq 0x14(%rax),%rdx

4022d6: 48 8d b4 24 34 a0 00 lea 0xa034(%rsp),%rsi

4022dd: 00

4022de: 48 8b 39 mov (%rcx),%rdi

4022e1: e8 aa ea ff ff callq 400d90 <bcopy@plt>

4022e6: 0f b7 44 24 04 movzwl 0x4(%rsp),%eax

4022eb: 66 c1 c8 08 ror $0x8,%ax

4022ef: 66 89 84 24 32 a0 00 mov %ax,0xa032(%rsp)

4022f6: 00

4022f7: ba 10 00 00 00 mov $0x10,%edx

4022fc: 48 8d b4 24 30 a0 00 lea 0xa030(%rsp),%rsi

402303: 00

402304: 89 ef mov %ebp,%edi

402306: e8 f5 ea ff ff callq 400e00 <connect@plt>

40230b: 85 c0 test %eax,%eax

40230d: 79 59 jns 402368 <submitr+0x1d1>

40230f: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax

402316: 3a 20 55

402319: 48 89 03 mov %rax,(%rbx)

40231c: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax

402323: 20 74 6f

402326: 48 89 43 08 mov %rax,0x8(%rbx)

40232a: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6e6f6320,%rax

402331: 65 63 74

402334: 48 89 43 10 mov %rax,0x10(%rbx)

402338: 48 b8 20 74 6f 20 74 movabs $0x20656874206f7420,%rax

40233f: 68 65 20

402342: 48 89 43 18 mov %rax,0x18(%rbx)

402346: c7 43 20 73 65 72 76 movl $0x76726573,0x20(%rbx)

40234d: 66 c7 43 24 65 72 movw $0x7265,0x24(%rbx)

402353: c6 43 26 00 movb $0x0,0x26(%rbx)

402357: 89 ef mov %ebp,%edi

402359: e8 52 e9 ff ff callq 400cb0 <close@plt>

40235e: b8 ff ff ff ff mov $0xffffffff,%eax

402363: e9 38 05 00 00 jmpq 4028a0 <submitr+0x709>

402368: 48 c7 c2 ff ff ff ff mov $0xffffffffffffffff,%rdx

40236f: 4c 89 ef mov %r13,%rdi

402372: b8 00 00 00 00 mov $0x0,%eax

402377: 48 89 d1 mov %rdx,%rcx

40237a: f2 ae repnz scas %es:(%rdi),%al

40237c: 48 f7 d1 not %rcx

40237f: 48 89 ce mov %rcx,%rsi

402382: 4c 89 ff mov %r15,%rdi

402385: 48 89 d1 mov %rdx,%rcx

402388: f2 ae repnz scas %es:(%rdi),%al

40238a: 48 f7 d1 not %rcx

40238d: 49 89 c8 mov %rcx,%r8

402390: 4c 89 f7 mov %r14,%rdi

402393: 48 89 d1 mov %rdx,%rcx

402396: f2 ae repnz scas %es:(%rdi),%al

402398: 49 29 c8 sub %rcx,%r8

40239b: 48 8b 7c 24 08 mov 0x8(%rsp),%rdi

4023a0: 48 89 d1 mov %rdx,%rcx

4023a3: f2 ae repnz scas %es:(%rdi),%al

4023a5: 49 29 c8 sub %rcx,%r8

4023a8: 48 8d 44 76 fd lea -0x3(%rsi,%rsi,2),%rax

4023ad: 49 8d 44 00 7b lea 0x7b(%r8,%rax,1),%rax

4023b2: 48 3d 00 20 00 00 cmp $0x2000,%rax

4023b8: 76 72 jbe 40242c <submitr+0x295>

4023ba: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax

4023c1: 3a 20 52

4023c4: 48 89 03 mov %rax,(%rbx)

4023c7: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax

4023ce: 20 73 74

4023d1: 48 89 43 08 mov %rax,0x8(%rbx)

4023d5: 48 b8 72 69 6e 67 20 movabs $0x6f6f7420676e6972,%rax

4023dc: 74 6f 6f

4023df: 48 89 43 10 mov %rax,0x10(%rbx)

4023e3: 48 b8 20 6c 61 72 67 movabs $0x202e656772616c20,%rax

4023ea: 65 2e 20

4023ed: 48 89 43 18 mov %rax,0x18(%rbx)

4023f1: 48 b8 49 6e 63 72 65 movabs $0x6573616572636e49,%rax

4023f8: 61 73 65

4023fb: 48 89 43 20 mov %rax,0x20(%rbx)

4023ff: 48 b8 20 53 55 42 4d movabs $0x5254494d42555320,%rax

402406: 49 54 52

402409: 48 89 43 28 mov %rax,0x28(%rbx)

40240d: 48 b8 5f 4d 41 58 42 movabs $0x46554258414d5f,%rax

402414: 55 46 00

402417: 48 89 43 30 mov %rax,0x30(%rbx)

40241b: 89 ef mov %ebp,%edi

40241d: e8 8e e8 ff ff callq 400cb0 <close@plt>

402422: b8 ff ff ff ff mov $0xffffffff,%eax

402427: e9 74 04 00 00 jmpq 4028a0 <submitr+0x709>

40242c: 48 8d b4 24 20 40 00 lea 0x4020(%rsp),%rsi

402433: 00

402434: b9 00 04 00 00 mov $0x400,%ecx

402439: b8 00 00 00 00 mov $0x0,%eax

40243e: 48 89 f7 mov %rsi,%rdi

402441: f3 48 ab rep stos %rax,%es:(%rdi)

402444: 4c 89 ef mov %r13,%rdi

402447: e8 0f fb ff ff callq 401f5b <urlencode>

40244c: 85 c0 test %eax,%eax

40244e: 0f 89 8a 00 00 00 jns 4024de <submitr+0x347>

402454: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax

40245b: 3a 20 52

40245e: 48 89 03 mov %rax,(%rbx)

402461: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax

402468: 20 73 74

40246b: 48 89 43 08 mov %rax,0x8(%rbx)

40246f: 48 b8 72 69 6e 67 20 movabs $0x6e6f6320676e6972,%rax

402476: 63 6f 6e

402479: 48 89 43 10 mov %rax,0x10(%rbx)

40247d: 48 b8 74 61 69 6e 73 movabs $0x6e6120736e696174,%rax

402484: 20 61 6e

402487: 48 89 43 18 mov %rax,0x18(%rbx)

40248b: 48 b8 20 69 6c 6c 65 movabs $0x6c6167656c6c6920,%rax

402492: 67 61 6c

402495: 48 89 43 20 mov %rax,0x20(%rbx)

402499: 48 b8 20 6f 72 20 75 movabs $0x72706e7520726f20,%rax

4024a0: 6e 70 72

4024a3: 48 89 43 28 mov %rax,0x28(%rbx)

4024a7: 48 b8 69 6e 74 61 62 movabs $0x20656c6261746e69,%rax

4024ae: 6c 65 20

4024b1: 48 89 43 30 mov %rax,0x30(%rbx)

4024b5: 48 b8 63 68 61 72 61 movabs $0x6574636172616863,%rax

4024bc: 63 74 65

4024bf: 48 89 43 38 mov %rax,0x38(%rbx)

4024c3: 66 c7 43 40 72 2e movw $0x2e72,0x40(%rbx)

4024c9: c6 43 42 00 movb $0x0,0x42(%rbx)

4024cd: 89 ef mov %ebp,%edi

4024cf: e8 dc e7 ff ff callq 400cb0 <close@plt>

4024d4: b8 ff ff ff ff mov $0xffffffff,%eax

4024d9: e9 c2 03 00 00 jmpq 4028a0 <submitr+0x709>

4024de: 4d 89 e1 mov %r12,%r9

4024e1: 4c 8d 84 24 20 40 00 lea 0x4020(%rsp),%r8

4024e8: 00

4024e9: 4c 89 f9 mov %r15,%rcx

4024ec: 4c 89 f2 mov %r14,%rdx

4024ef: be 38 33 40 00 mov $0x403338,%esi

4024f4: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

4024fb: 00

4024fc: b8 00 00 00 00 mov $0x0,%eax

402501: e8 da e8 ff ff callq 400de0 <sprintf@plt>

402506: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

40250d: 00

40250e: b8 00 00 00 00 mov $0x0,%eax

402513: 48 c7 c1 ff ff ff ff mov $0xffffffffffffffff,%rcx

40251a: f2 ae repnz scas %es:(%rdi),%al

40251c: 48 f7 d1 not %rcx

40251f: 48 8d 51 ff lea -0x1(%rcx),%rdx

402523: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40252a: 00

40252b: 89 ef mov %ebp,%edi

40252d: e8 ff fa ff ff callq 402031 <rio\_writen>

402532: 48 85 c0 test %rax,%rax

402535: 79 6e jns 4025a5 <submitr+0x40e>

402537: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

40253e: 3a 20 43

402541: 48 89 03 mov %rax,(%rbx)

402544: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40254b: 20 75 6e

40254e: 48 89 43 08 mov %rax,0x8(%rbx)

402552: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402559: 74 6f 20

40255c: 48 89 43 10 mov %rax,0x10(%rbx)

402560: 48 b8 77 72 69 74 65 movabs $0x6f74206574697277,%rax

402567: 20 74 6f

40256a: 48 89 43 18 mov %rax,0x18(%rbx)

40256e: 48 b8 20 74 68 65 20 movabs $0x7365722065687420,%rax

402575: 72 65 73

402578: 48 89 43 20 mov %rax,0x20(%rbx)

40257c: 48 b8 75 6c 74 20 73 movabs $0x7672657320746c75,%rax

402583: 65 72 76

402586: 48 89 43 28 mov %rax,0x28(%rbx)

40258a: 66 c7 43 30 65 72 movw $0x7265,0x30(%rbx)

402590: c6 43 32 00 movb $0x0,0x32(%rbx)

402594: 89 ef mov %ebp,%edi

402596: e8 15 e7 ff ff callq 400cb0 <close@plt>

40259b: b8 ff ff ff ff mov $0xffffffff,%eax

4025a0: e9 fb 02 00 00 jmpq 4028a0 <submitr+0x709>

4025a5: 89 ee mov %ebp,%esi

4025a7: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4025ae: 00

4025af: e8 6c f9 ff ff callq 401f20 <rio\_readinitb>

4025b4: ba 00 20 00 00 mov $0x2000,%edx

4025b9: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

4025c0: 00

4025c1: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4025c8: 00

4025c9: e8 46 fb ff ff callq 402114 <rio\_readlineb>

4025ce: 48 85 c0 test %rax,%rax

4025d1: 7f 7d jg 402650 <submitr+0x4b9>

4025d3: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

4025da: 3a 20 43

4025dd: 48 89 03 mov %rax,(%rbx)

4025e0: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

4025e7: 20 75 6e

4025ea: 48 89 43 08 mov %rax,0x8(%rbx)

4025ee: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

4025f5: 74 6f 20

4025f8: 48 89 43 10 mov %rax,0x10(%rbx)

4025fc: 48 b8 72 65 61 64 20 movabs $0x7269662064616572,%rax

402603: 66 69 72

402606: 48 89 43 18 mov %rax,0x18(%rbx)

40260a: 48 b8 73 74 20 68 65 movabs $0x6564616568207473,%rax

402611: 61 64 65

402614: 48 89 43 20 mov %rax,0x20(%rbx)

402618: 48 b8 72 20 66 72 6f movabs $0x72206d6f72662072,%rax

40261f: 6d 20 72

402622: 48 89 43 28 mov %rax,0x28(%rbx)

402626: 48 b8 65 73 75 6c 74 movabs $0x657320746c757365,%rax

40262d: 20 73 65

402630: 48 89 43 30 mov %rax,0x30(%rbx)

402634: c7 43 38 72 76 65 72 movl $0x72657672,0x38(%rbx)

40263b: c6 43 3c 00 movb $0x0,0x3c(%rbx)

40263f: 89 ef mov %ebp,%edi

402641: e8 6a e6 ff ff callq 400cb0 <close@plt>

402646: b8 ff ff ff ff mov $0xffffffff,%eax

40264b: e9 50 02 00 00 jmpq 4028a0 <submitr+0x709>

402650: 4c 8d 44 24 10 lea 0x10(%rsp),%r8

402655: 48 8d 8c 24 1c 20 00 lea 0x201c(%rsp),%rcx

40265c: 00

40265d: 48 8d 94 24 20 20 00 lea 0x2020(%rsp),%rdx

402664: 00

402665: be af 33 40 00 mov $0x4033af,%esi

40266a: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

402671: 00

402672: b8 00 00 00 00 mov $0x0,%eax

402677: e8 f4 e6 ff ff callq 400d70 <\_\_isoc99\_sscanf@plt>

40267c: e9 98 00 00 00 jmpq 402719 <submitr+0x582>

402681: ba 00 20 00 00 mov $0x2000,%edx

402686: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40268d: 00

40268e: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

402695: 00

402696: e8 79 fa ff ff callq 402114 <rio\_readlineb>

40269b: 48 85 c0 test %rax,%rax

40269e: 7f 79 jg 402719 <submitr+0x582>

4026a0: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

4026a7: 3a 20 43

4026aa: 48 89 03 mov %rax,(%rbx)

4026ad: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

4026b4: 20 75 6e

4026b7: 48 89 43 08 mov %rax,0x8(%rbx)

4026bb: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

4026c2: 74 6f 20

4026c5: 48 89 43 10 mov %rax,0x10(%rbx)

4026c9: 48 b8 72 65 61 64 20 movabs $0x6165682064616572,%rax

4026d0: 68 65 61

4026d3: 48 89 43 18 mov %rax,0x18(%rbx)

4026d7: 48 b8 64 65 72 73 20 movabs $0x6f72662073726564,%rax

4026de: 66 72 6f

4026e1: 48 89 43 20 mov %rax,0x20(%rbx)

4026e5: 48 b8 6d 20 74 68 65 movabs $0x657220656874206d,%rax

4026ec: 20 72 65

4026ef: 48 89 43 28 mov %rax,0x28(%rbx)

4026f3: 48 b8 73 75 6c 74 20 movabs $0x72657320746c7573,%rax

4026fa: 73 65 72

4026fd: 48 89 43 30 mov %rax,0x30(%rbx)

402701: c7 43 38 76 65 72 00 movl $0x726576,0x38(%rbx)

402708: 89 ef mov %ebp,%edi

40270a: e8 a1 e5 ff ff callq 400cb0 <close@plt>

40270f: b8 ff ff ff ff mov $0xffffffff,%eax

402714: e9 87 01 00 00 jmpq 4028a0 <submitr+0x709>

402719: 0f b6 84 24 20 60 00 movzbl 0x6020(%rsp),%eax

402720: 00

402721: 83 e8 0d sub $0xd,%eax

402724: 75 15 jne 40273b <submitr+0x5a4>

402726: 0f b6 84 24 21 60 00 movzbl 0x6021(%rsp),%eax

40272d: 00

40272e: 83 e8 0a sub $0xa,%eax

402731: 75 08 jne 40273b <submitr+0x5a4>

402733: 0f b6 84 24 22 60 00 movzbl 0x6022(%rsp),%eax

40273a: 00

40273b: 85 c0 test %eax,%eax

40273d: 0f 85 3e ff ff ff jne 402681 <submitr+0x4ea>

402743: ba 00 20 00 00 mov $0x2000,%edx

402748: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40274f: 00

402750: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

402757: 00

402758: e8 b7 f9 ff ff callq 402114 <rio\_readlineb>

40275d: 48 85 c0 test %rax,%rax

402760: 0f 8f 83 00 00 00 jg 4027e9 <submitr+0x652>

402766: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

40276d: 3a 20 43

402770: 48 89 03 mov %rax,(%rbx)

402773: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40277a: 20 75 6e

40277d: 48 89 43 08 mov %rax,0x8(%rbx)

402781: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402788: 74 6f 20

40278b: 48 89 43 10 mov %rax,0x10(%rbx)

40278f: 48 b8 72 65 61 64 20 movabs $0x6174732064616572,%rax

402796: 73 74 61

402799: 48 89 43 18 mov %rax,0x18(%rbx)

40279d: 48 b8 74 75 73 20 6d movabs $0x7373656d20737574,%rax

4027a4: 65 73 73

4027a7: 48 89 43 20 mov %rax,0x20(%rbx)

4027ab: 48 b8 61 67 65 20 66 movabs $0x6d6f726620656761,%rax

4027b2: 72 6f 6d

4027b5: 48 89 43 28 mov %rax,0x28(%rbx)

4027b9: 48 b8 20 72 65 73 75 movabs $0x20746c7573657220,%rax

4027c0: 6c 74 20

4027c3: 48 89 43 30 mov %rax,0x30(%rbx)

4027c7: c7 43 38 73 65 72 76 movl $0x76726573,0x38(%rbx)

4027ce: 66 c7 43 3c 65 72 movw $0x7265,0x3c(%rbx)

4027d4: c6 43 3e 00 movb $0x0,0x3e(%rbx)

4027d8: 89 ef mov %ebp,%edi

4027da: e8 d1 e4 ff ff callq 400cb0 <close@plt>

4027df: b8 ff ff ff ff mov $0xffffffff,%eax

4027e4: e9 b7 00 00 00 jmpq 4028a0 <submitr+0x709>

4027e9: 8b 94 24 1c 20 00 00 mov 0x201c(%rsp),%edx

4027f0: 81 fa c8 00 00 00 cmp $0xc8,%edx

4027f6: 74 28 je 402820 <submitr+0x689>

4027f8: 48 8d 4c 24 10 lea 0x10(%rsp),%rcx

4027fd: be 78 33 40 00 mov $0x403378,%esi

402802: 48 89 df mov %rbx,%rdi

402805: b8 00 00 00 00 mov $0x0,%eax

40280a: e8 d1 e5 ff ff callq 400de0 <sprintf@plt>

40280f: 89 ef mov %ebp,%edi

402811: e8 9a e4 ff ff callq 400cb0 <close@plt>

402816: b8 ff ff ff ff mov $0xffffffff,%eax

40281b: e9 80 00 00 00 jmpq 4028a0 <submitr+0x709>

402820: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

402827: 00

402828: 48 89 df mov %rbx,%rdi

40282b: e8 10 e4 ff ff callq 400c40 <strcpy@plt>

402830: 89 ef mov %ebp,%edi

402832: e8 79 e4 ff ff callq 400cb0 <close@plt>

402837: 0f b6 03 movzbl (%rbx),%eax

40283a: 83 e8 4f sub $0x4f,%eax

40283d: 75 18 jne 402857 <submitr+0x6c0>

40283f: 0f b6 53 01 movzbl 0x1(%rbx),%edx

402843: 83 ea 4b sub $0x4b,%edx

402846: 75 11 jne 402859 <submitr+0x6c2>

402848: 0f b6 53 02 movzbl 0x2(%rbx),%edx

40284c: 83 ea 0a sub $0xa,%edx

40284f: 75 08 jne 402859 <submitr+0x6c2>

402851: 0f b6 53 03 movzbl 0x3(%rbx),%edx

402855: eb 02 jmp 402859 <submitr+0x6c2>

402857: 89 c2 mov %eax,%edx

402859: 85 d2 test %edx,%edx

40285b: 74 30 je 40288d <submitr+0x6f6>

40285d: bf c0 33 40 00 mov $0x4033c0,%edi

402862: b9 05 00 00 00 mov $0x5,%ecx

402867: 48 89 de mov %rbx,%rsi

40286a: f3 a6 repz cmpsb %es:(%rdi),%ds:(%rsi)

40286c: 0f 97 c1 seta %cl

40286f: 0f 92 c2 setb %dl

402872: 38 d1 cmp %dl,%cl

402874: 74 1e je 402894 <submitr+0x6fd>

402876: 85 c0 test %eax,%eax

402878: 75 0d jne 402887 <submitr+0x6f0>

40287a: 0f b6 43 01 movzbl 0x1(%rbx),%eax

40287e: 83 e8 4b sub $0x4b,%eax

402881: 75 04 jne 402887 <submitr+0x6f0>

402883: 0f b6 43 02 movzbl 0x2(%rbx),%eax

402887: 85 c0 test %eax,%eax

402889: 75 10 jne 40289b <submitr+0x704>

40288b: eb 13 jmp 4028a0 <submitr+0x709>

40288d: b8 00 00 00 00 mov $0x0,%eax

402892: eb 0c jmp 4028a0 <submitr+0x709>

402894: b8 00 00 00 00 mov $0x0,%eax

402899: eb 05 jmp 4028a0 <submitr+0x709>

40289b: b8 ff ff ff ff mov $0xffffffff,%eax

4028a0: 48 81 c4 48 a0 00 00 add $0xa048,%rsp

4028a7: 5b pop %rbx

4028a8: 5d pop %rbp

4028a9: 41 5c pop %r12

4028ab: 41 5d pop %r13

4028ad: 41 5e pop %r14

4028af: 41 5f pop %r15

4028b1: c3 retq

00000000004028b2 <init\_timeout>:

4028b2: 53 push %rbx

4028b3: 89 fb mov %edi,%ebx

4028b5: 85 ff test %edi,%edi

4028b7: 74 1f je 4028d8 <init\_timeout+0x26>

4028b9: 85 ff test %edi,%edi

4028bb: 79 05 jns 4028c2 <init\_timeout+0x10>

4028bd: bb 00 00 00 00 mov $0x0,%ebx

4028c2: be 32 1f 40 00 mov $0x401f32,%esi

4028c7: bf 0e 00 00 00 mov $0xe,%edi

4028cc: e8 0f e4 ff ff callq 400ce0 <signal@plt>

4028d1: 89 df mov %ebx,%edi

4028d3: e8 c8 e3 ff ff callq 400ca0 <alarm@plt>

4028d8: 5b pop %rbx

4028d9: c3 retq

00000000004028da <init\_driver>:

4028da: 55 push %rbp

4028db: 53 push %rbx

4028dc: 48 83 ec 18 sub $0x18,%rsp

4028e0: 48 89 fd mov %rdi,%rbp

4028e3: be 01 00 00 00 mov $0x1,%esi

4028e8: bf 0d 00 00 00 mov $0xd,%edi

4028ed: e8 ee e3 ff ff callq 400ce0 <signal@plt>

4028f2: be 01 00 00 00 mov $0x1,%esi

4028f7: bf 1d 00 00 00 mov $0x1d,%edi

4028fc: e8 df e3 ff ff callq 400ce0 <signal@plt>

402901: be 01 00 00 00 mov $0x1,%esi

402906: bf 1d 00 00 00 mov $0x1d,%edi

40290b: e8 d0 e3 ff ff callq 400ce0 <signal@plt>

402910: ba 00 00 00 00 mov $0x0,%edx

402915: be 01 00 00 00 mov $0x1,%esi

40291a: bf 02 00 00 00 mov $0x2,%edi

40291f: e8 ec e4 ff ff callq 400e10 <socket@plt>

402924: 89 c3 mov %eax,%ebx

402926: 85 c0 test %eax,%eax

402928: 79 4f jns 402979 <init\_driver+0x9f>

40292a: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

402931: 3a 20 43

402934: 48 89 45 00 mov %rax,0x0(%rbp)

402938: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40293f: 20 75 6e

402942: 48 89 45 08 mov %rax,0x8(%rbp)

402946: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

40294d: 74 6f 20

402950: 48 89 45 10 mov %rax,0x10(%rbp)

402954: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax

40295b: 65 20 73

40295e: 48 89 45 18 mov %rax,0x18(%rbp)

402962: c7 45 20 6f 63 6b 65 movl $0x656b636f,0x20(%rbp)

402969: 66 c7 45 24 74 00 movw $0x74,0x24(%rbp)

40296f: b8 ff ff ff ff mov $0xffffffff,%eax

402974: e9 23 01 00 00 jmpq 402a9c <init\_driver+0x1c2>

402979: bf 13 2f 40 00 mov $0x402f13,%edi

40297e: e8 6d e3 ff ff callq 400cf0 <gethostbyname@plt>

402983: 48 85 c0 test %rax,%rax

402986: 75 68 jne 4029f0 <init\_driver+0x116>

402988: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax

40298f: 3a 20 44

402992: 48 89 45 00 mov %rax,0x0(%rbp)

402996: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax

40299d: 20 75 6e

4029a0: 48 89 45 08 mov %rax,0x8(%rbp)

4029a4: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

4029ab: 74 6f 20

4029ae: 48 89 45 10 mov %rax,0x10(%rbp)

4029b2: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax

4029b9: 76 65 20

4029bc: 48 89 45 18 mov %rax,0x18(%rbp)

4029c0: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax

4029c7: 72 20 61

4029ca: 48 89 45 20 mov %rax,0x20(%rbp)

4029ce: c7 45 28 64 64 72 65 movl $0x65726464,0x28(%rbp)

4029d5: 66 c7 45 2c 73 73 movw $0x7373,0x2c(%rbp)

4029db: c6 45 2e 00 movb $0x0,0x2e(%rbp)

4029df: 89 df mov %ebx,%edi

4029e1: e8 ca e2 ff ff callq 400cb0 <close@plt>

4029e6: b8 ff ff ff ff mov $0xffffffff,%eax

4029eb: e9 ac 00 00 00 jmpq 402a9c <init\_driver+0x1c2>

4029f0: 48 c7 04 24 00 00 00 movq $0x0,(%rsp)

4029f7: 00

4029f8: 48 c7 44 24 08 00 00 movq $0x0,0x8(%rsp)

4029ff: 00 00

402a01: 66 c7 04 24 02 00 movw $0x2,(%rsp)

402a07: 48 8b 48 18 mov 0x18(%rax),%rcx

402a0b: 48 63 50 14 movslq 0x14(%rax),%rdx

402a0f: 48 8d 74 24 04 lea 0x4(%rsp),%rsi

402a14: 48 8b 39 mov (%rcx),%rdi

402a17: e8 74 e3 ff ff callq 400d90 <bcopy@plt>

402a1c: 66 c7 44 24 02 17 7d movw $0x7d17,0x2(%rsp)

402a23: ba 10 00 00 00 mov $0x10,%edx

402a28: 48 89 e6 mov %rsp,%rsi

402a2b: 89 df mov %ebx,%edi

402a2d: e8 ce e3 ff ff callq 400e00 <connect@plt>

402a32: 85 c0 test %eax,%eax

402a34: 79 50 jns 402a86 <init\_driver+0x1ac>

402a36: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax

402a3d: 3a 20 55

402a40: 48 89 45 00 mov %rax,0x0(%rbp)

402a44: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax

402a4b: 20 74 6f

402a4e: 48 89 45 08 mov %rax,0x8(%rbp)

402a52: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6e6f6320,%rax

402a59: 65 63 74

402a5c: 48 89 45 10 mov %rax,0x10(%rbp)

402a60: 48 b8 20 74 6f 20 73 movabs $0x76726573206f7420,%rax

402a67: 65 72 76

402a6a: 48 89 45 18 mov %rax,0x18(%rbp)

402a6e: 66 c7 45 20 65 72 movw $0x7265,0x20(%rbp)

402a74: c6 45 22 00 movb $0x0,0x22(%rbp)

402a78: 89 df mov %ebx,%edi

402a7a: e8 31 e2 ff ff callq 400cb0 <close@plt>

402a7f: b8 ff ff ff ff mov $0xffffffff,%eax

402a84: eb 16 jmp 402a9c <init\_driver+0x1c2>

402a86: 89 df mov %ebx,%edi

402a88: e8 23 e2 ff ff callq 400cb0 <close@plt>

402a8d: 66 c7 45 00 4f 4b movw $0x4b4f,0x0(%rbp)

402a93: c6 45 02 00 movb $0x0,0x2(%rbp)

402a97: b8 00 00 00 00 mov $0x0,%eax

402a9c: 48 83 c4 18 add $0x18,%rsp

402aa0: 5b pop %rbx

402aa1: 5d pop %rbp

402aa2: c3 retq

0000000000402aa3 <driver\_post>:

402aa3: 53 push %rbx

402aa4: 48 83 ec 10 sub $0x10,%rsp

402aa8: 4c 89 cb mov %r9,%rbx

402aab: 45 85 c0 test %r8d,%r8d

402aae: 74 22 je 402ad2 <driver\_post+0x2f>

402ab0: 48 89 ce mov %rcx,%rsi

402ab3: bf c5 33 40 00 mov $0x4033c5,%edi

402ab8: b8 00 00 00 00 mov $0x0,%eax

402abd: e8 be e1 ff ff callq 400c80 <printf@plt>

402ac2: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)

402ac7: c6 43 02 00 movb $0x0,0x2(%rbx)

402acb: b8 00 00 00 00 mov $0x0,%eax

402ad0: eb 39 jmp 402b0b <driver\_post+0x68>

402ad2: 48 85 ff test %rdi,%rdi

402ad5: 74 26 je 402afd <driver\_post+0x5a>

402ad7: 80 3f 00 cmpb $0x0,(%rdi)

402ada: 74 21 je 402afd <driver\_post+0x5a>

402adc: 4c 89 0c 24 mov %r9,(%rsp)

402ae0: 49 89 c9 mov %rcx,%r9

402ae3: 49 89 d0 mov %rdx,%r8

402ae6: 48 89 f9 mov %rdi,%rcx

402ae9: 48 89 f2 mov %rsi,%rdx

402aec: be 7d 17 00 00 mov $0x177d,%esi

402af1: bf 13 2f 40 00 mov $0x402f13,%edi

402af6: e8 9c f6 ff ff callq 402197 <submitr>

402afb: eb 0e jmp 402b0b <driver\_post+0x68>

402afd: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)

402b02: c6 43 02 00 movb $0x0,0x2(%rbx)

402b06: b8 00 00 00 00 mov $0x0,%eax

402b0b: 48 83 c4 10 add $0x10,%rsp

402b0f: 5b pop %rbx

402b10: c3 retq

0000000000402b11 <check>:

402b11: 89 f8 mov %edi,%eax

402b13: c1 e8 1c shr $0x1c,%eax

402b16: 85 c0 test %eax,%eax

402b18: 74 1d je 402b37 <check+0x26>

402b1a: b9 00 00 00 00 mov $0x0,%ecx

402b1f: eb 0b jmp 402b2c <check+0x1b>

402b21: 89 f8 mov %edi,%eax

402b23: d3 e8 shr %cl,%eax

402b25: 3c 0a cmp $0xa,%al

402b27: 74 14 je 402b3d <check+0x2c>

402b29: 83 c1 08 add $0x8,%ecx

402b2c: 83 f9 1f cmp $0x1f,%ecx

402b2f: 7e f0 jle 402b21 <check+0x10>

402b31: b8 01 00 00 00 mov $0x1,%eax

402b36: c3 retq

402b37: b8 00 00 00 00 mov $0x0,%eax

402b3c: c3 retq

402b3d: b8 00 00 00 00 mov $0x0,%eax

402b42: c3 retq

0000000000402b43 <gencookie>:

402b43: 53 push %rbx

402b44: 83 c7 01 add $0x1,%edi

402b47: e8 d4 e0 ff ff callq 400c20 <srandom@plt>

402b4c: e8 ff e1 ff ff callq 400d50 <random@plt>

402b51: 89 c3 mov %eax,%ebx

402b53: 89 c7 mov %eax,%edi

402b55: e8 b7 ff ff ff callq 402b11 <check>

402b5a: 85 c0 test %eax,%eax

402b5c: 74 ee je 402b4c <gencookie+0x9>

402b5e: 89 d8 mov %ebx,%eax

402b60: 5b pop %rbx

402b61: c3 retq

402b62: 66 2e 0f 1f 84 00 00 nopw %cs:0x0(%rax,%rax,1)

402b69: 00 00 00

402b6c: 0f 1f 40 00 nopl 0x0(%rax)

0000000000402b70 <\_\_libc\_csu\_init>:

402b70: 41 57 push %r15

402b72: 41 89 ff mov %edi,%r15d

402b75: 41 56 push %r14

402b77: 49 89 f6 mov %rsi,%r14

402b7a: 41 55 push %r13

402b7c: 49 89 d5 mov %rdx,%r13

402b7f: 41 54 push %r12

402b81: 4c 8d 25 88 12 20 00 lea 0x201288(%rip),%r12 # 603e10 <\_\_frame\_dummy\_init\_array\_entry>

402b88: 55 push %rbp

402b89: 48 8d 2d 88 12 20 00 lea 0x201288(%rip),%rbp # 603e18 <\_\_init\_array\_end>

402b90: 53 push %rbx

402b91: 4c 29 e5 sub %r12,%rbp

402b94: 31 db xor %ebx,%ebx

402b96: 48 c1 fd 03 sar $0x3,%rbp

402b9a: 48 83 ec 08 sub $0x8,%rsp

402b9e: e8 25 e0 ff ff callq 400bc8 <\_init>

402ba3: 48 85 ed test %rbp,%rbp

402ba6: 74 1e je 402bc6 <\_\_libc\_csu\_init+0x56>

402ba8: 0f 1f 84 00 00 00 00 nopl 0x0(%rax,%rax,1)

402baf: 00

402bb0: 4c 89 ea mov %r13,%rdx

402bb3: 4c 89 f6 mov %r14,%rsi

402bb6: 44 89 ff mov %r15d,%edi

402bb9: 41 ff 14 dc callq \*(%r12,%rbx,8)

402bbd: 48 83 c3 01 add $0x1,%rbx

402bc1: 48 39 eb cmp %rbp,%rbx

402bc4: 75 ea jne 402bb0 <\_\_libc\_csu\_init+0x40>

402bc6: 48 83 c4 08 add $0x8,%rsp

402bca: 5b pop %rbx

402bcb: 5d pop %rbp

402bcc: 41 5c pop %r12

402bce: 41 5d pop %r13

402bd0: 41 5e pop %r14

402bd2: 41 5f pop %r15

402bd4: c3 retq

402bd5: 90 nop

402bd6: 66 2e 0f 1f 84 00 00 nopw %cs:0x0(%rax,%rax,1)

402bdd: 00 00 00

0000000000402be0 <\_\_libc\_csu\_fini>:

402be0: f3 c3 repz retq

402be2: 66 90 xchg %ax,%ax

Disassembly of section .fini:

0000000000402be4 <\_fini>:

402be4: 48 83 ec 08 sub $0x8,%rsp

402be8: 48 83 c4 08 add $0x8,%rsp

402bec: c3 retq