rtarget: file format elf64-x86-64

Disassembly of section .init:

0000000000400bc8 <\_init>:

400bc8: 48 83 ec 08 sub $0x8,%rsp

400bcc: 48 8b 05 25 44 20 00 mov 0x204425(%rip),%rax # 604ff8 <\_DYNAMIC+0x1d0>

400bd3: 48 85 c0 test %rax,%rax

400bd6: 74 05 je 400bdd <\_init+0x15>

400bd8: e8 33 01 00 00 callq 400d10 <\_\_gmon\_start\_\_@plt>

400bdd: 48 83 c4 08 add $0x8,%rsp

400be1: c3 retq

Disassembly of section .plt:

0000000000400bf0 <strcasecmp@plt-0x10>:

400bf0: ff 35 12 44 20 00 pushq 0x204412(%rip) # 605008 <\_GLOBAL\_OFFSET\_TABLE\_+0x8>

400bf6: ff 25 14 44 20 00 jmpq \*0x204414(%rip) # 605010 <\_GLOBAL\_OFFSET\_TABLE\_+0x10>

400bfc: 0f 1f 40 00 nopl 0x0(%rax)

0000000000400c00 <strcasecmp@plt>:

400c00: ff 25 12 44 20 00 jmpq \*0x204412(%rip) # 605018 <\_GLOBAL\_OFFSET\_TABLE\_+0x18>

400c06: 68 00 00 00 00 pushq $0x0

400c0b: e9 e0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c10 <\_\_errno\_location@plt>:

400c10: ff 25 0a 44 20 00 jmpq \*0x20440a(%rip) # 605020 <\_GLOBAL\_OFFSET\_TABLE\_+0x20>

400c16: 68 01 00 00 00 pushq $0x1

400c1b: e9 d0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c20 <srandom@plt>:

400c20: ff 25 02 44 20 00 jmpq \*0x204402(%rip) # 605028 <\_GLOBAL\_OFFSET\_TABLE\_+0x28>

400c26: 68 02 00 00 00 pushq $0x2

400c2b: e9 c0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c30 <strncmp@plt>:

400c30: ff 25 fa 43 20 00 jmpq \*0x2043fa(%rip) # 605030 <\_GLOBAL\_OFFSET\_TABLE\_+0x30>

400c36: 68 03 00 00 00 pushq $0x3

400c3b: e9 b0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c40 <strcpy@plt>:

400c40: ff 25 f2 43 20 00 jmpq \*0x2043f2(%rip) # 605038 <\_GLOBAL\_OFFSET\_TABLE\_+0x38>

400c46: 68 04 00 00 00 pushq $0x4

400c4b: e9 a0 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c50 <puts@plt>:

400c50: ff 25 ea 43 20 00 jmpq \*0x2043ea(%rip) # 605040 <\_GLOBAL\_OFFSET\_TABLE\_+0x40>

400c56: 68 05 00 00 00 pushq $0x5

400c5b: e9 90 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c60 <write@plt>:

400c60: ff 25 e2 43 20 00 jmpq \*0x2043e2(%rip) # 605048 <\_GLOBAL\_OFFSET\_TABLE\_+0x48>

400c66: 68 06 00 00 00 pushq $0x6

400c6b: e9 80 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c70 <mmap@plt>:

400c70: ff 25 da 43 20 00 jmpq \*0x2043da(%rip) # 605050 <\_GLOBAL\_OFFSET\_TABLE\_+0x50>

400c76: 68 07 00 00 00 pushq $0x7

400c7b: e9 70 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c80 <printf@plt>:

400c80: ff 25 d2 43 20 00 jmpq \*0x2043d2(%rip) # 605058 <\_GLOBAL\_OFFSET\_TABLE\_+0x58>

400c86: 68 08 00 00 00 pushq $0x8

400c8b: e9 60 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400c90 <memset@plt>:

400c90: ff 25 ca 43 20 00 jmpq \*0x2043ca(%rip) # 605060 <\_GLOBAL\_OFFSET\_TABLE\_+0x60>

400c96: 68 09 00 00 00 pushq $0x9

400c9b: e9 50 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400ca0 <alarm@plt>:

400ca0: ff 25 c2 43 20 00 jmpq \*0x2043c2(%rip) # 605068 <\_GLOBAL\_OFFSET\_TABLE\_+0x68>

400ca6: 68 0a 00 00 00 pushq $0xa

400cab: e9 40 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400cb0 <close@plt>:

400cb0: ff 25 ba 43 20 00 jmpq \*0x2043ba(%rip) # 605070 <\_GLOBAL\_OFFSET\_TABLE\_+0x70>

400cb6: 68 0b 00 00 00 pushq $0xb

400cbb: e9 30 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400cc0 <read@plt>:

400cc0: ff 25 b2 43 20 00 jmpq \*0x2043b2(%rip) # 605078 <\_GLOBAL\_OFFSET\_TABLE\_+0x78>

400cc6: 68 0c 00 00 00 pushq $0xc

400ccb: e9 20 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400cd0 <\_\_libc\_start\_main@plt>:

400cd0: ff 25 aa 43 20 00 jmpq \*0x2043aa(%rip) # 605080 <\_GLOBAL\_OFFSET\_TABLE\_+0x80>

400cd6: 68 0d 00 00 00 pushq $0xd

400cdb: e9 10 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400ce0 <signal@plt>:

400ce0: ff 25 a2 43 20 00 jmpq \*0x2043a2(%rip) # 605088 <\_GLOBAL\_OFFSET\_TABLE\_+0x88>

400ce6: 68 0e 00 00 00 pushq $0xe

400ceb: e9 00 ff ff ff jmpq 400bf0 <\_init+0x28>

0000000000400cf0 <gethostbyname@plt>:

400cf0: ff 25 9a 43 20 00 jmpq \*0x20439a(%rip) # 605090 <\_GLOBAL\_OFFSET\_TABLE\_+0x90>

400cf6: 68 0f 00 00 00 pushq $0xf

400cfb: e9 f0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d00 <fprintf@plt>:

400d00: ff 25 92 43 20 00 jmpq \*0x204392(%rip) # 605098 <\_GLOBAL\_OFFSET\_TABLE\_+0x98>

400d06: 68 10 00 00 00 pushq $0x10

400d0b: e9 e0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d10 <\_\_gmon\_start\_\_@plt>:

400d10: ff 25 8a 43 20 00 jmpq \*0x20438a(%rip) # 6050a0 <\_GLOBAL\_OFFSET\_TABLE\_+0xa0>

400d16: 68 11 00 00 00 pushq $0x11

400d1b: e9 d0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d20 <strtol@plt>:

400d20: ff 25 82 43 20 00 jmpq \*0x204382(%rip) # 6050a8 <\_GLOBAL\_OFFSET\_TABLE\_+0xa8>

400d26: 68 12 00 00 00 pushq $0x12

400d2b: e9 c0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d30 <memcpy@plt>:

400d30: ff 25 7a 43 20 00 jmpq \*0x20437a(%rip) # 6050b0 <\_GLOBAL\_OFFSET\_TABLE\_+0xb0>

400d36: 68 13 00 00 00 pushq $0x13

400d3b: e9 b0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d40 <time@plt>:

400d40: ff 25 72 43 20 00 jmpq \*0x204372(%rip) # 6050b8 <\_GLOBAL\_OFFSET\_TABLE\_+0xb8>

400d46: 68 14 00 00 00 pushq $0x14

400d4b: e9 a0 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d50 <random@plt>:

400d50: ff 25 6a 43 20 00 jmpq \*0x20436a(%rip) # 6050c0 <\_GLOBAL\_OFFSET\_TABLE\_+0xc0>

400d56: 68 15 00 00 00 pushq $0x15

400d5b: e9 90 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d60 <\_IO\_getc@plt>:

400d60: ff 25 62 43 20 00 jmpq \*0x204362(%rip) # 6050c8 <\_GLOBAL\_OFFSET\_TABLE\_+0xc8>

400d66: 68 16 00 00 00 pushq $0x16

400d6b: e9 80 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d70 <\_\_isoc99\_sscanf@plt>:

400d70: ff 25 5a 43 20 00 jmpq \*0x20435a(%rip) # 6050d0 <\_GLOBAL\_OFFSET\_TABLE\_+0xd0>

400d76: 68 17 00 00 00 pushq $0x17

400d7b: e9 70 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d80 <munmap@plt>:

400d80: ff 25 52 43 20 00 jmpq \*0x204352(%rip) # 6050d8 <\_GLOBAL\_OFFSET\_TABLE\_+0xd8>

400d86: 68 18 00 00 00 pushq $0x18

400d8b: e9 60 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400d90 <bcopy@plt>:

400d90: ff 25 4a 43 20 00 jmpq \*0x20434a(%rip) # 6050e0 <\_GLOBAL\_OFFSET\_TABLE\_+0xe0>

400d96: 68 19 00 00 00 pushq $0x19

400d9b: e9 50 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400da0 <fopen@plt>:

400da0: ff 25 42 43 20 00 jmpq \*0x204342(%rip) # 6050e8 <\_GLOBAL\_OFFSET\_TABLE\_+0xe8>

400da6: 68 1a 00 00 00 pushq $0x1a

400dab: e9 40 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400db0 <getopt@plt>:

400db0: ff 25 3a 43 20 00 jmpq \*0x20433a(%rip) # 6050f0 <\_GLOBAL\_OFFSET\_TABLE\_+0xf0>

400db6: 68 1b 00 00 00 pushq $0x1b

400dbb: e9 30 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400dc0 <strtoul@plt>:

400dc0: ff 25 32 43 20 00 jmpq \*0x204332(%rip) # 6050f8 <\_GLOBAL\_OFFSET\_TABLE\_+0xf8>

400dc6: 68 1c 00 00 00 pushq $0x1c

400dcb: e9 20 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400dd0 <gethostname@plt>:

400dd0: ff 25 2a 43 20 00 jmpq \*0x20432a(%rip) # 605100 <\_GLOBAL\_OFFSET\_TABLE\_+0x100>

400dd6: 68 1d 00 00 00 pushq $0x1d

400ddb: e9 10 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400de0 <sprintf@plt>:

400de0: ff 25 22 43 20 00 jmpq \*0x204322(%rip) # 605108 <\_GLOBAL\_OFFSET\_TABLE\_+0x108>

400de6: 68 1e 00 00 00 pushq $0x1e

400deb: e9 00 fe ff ff jmpq 400bf0 <\_init+0x28>

0000000000400df0 <exit@plt>:

400df0: ff 25 1a 43 20 00 jmpq \*0x20431a(%rip) # 605110 <\_GLOBAL\_OFFSET\_TABLE\_+0x110>

400df6: 68 1f 00 00 00 pushq $0x1f

400dfb: e9 f0 fd ff ff jmpq 400bf0 <\_init+0x28>

0000000000400e00 <connect@plt>:

400e00: ff 25 12 43 20 00 jmpq \*0x204312(%rip) # 605118 <\_GLOBAL\_OFFSET\_TABLE\_+0x118>

400e06: 68 20 00 00 00 pushq $0x20

400e0b: e9 e0 fd ff ff jmpq 400bf0 <\_init+0x28>

0000000000400e10 <socket@plt>:

400e10: ff 25 0a 43 20 00 jmpq \*0x20430a(%rip) # 605120 <\_GLOBAL\_OFFSET\_TABLE\_+0x120>

400e16: 68 21 00 00 00 pushq $0x21

400e1b: e9 d0 fd ff ff jmpq 400bf0 <\_init+0x28>

Disassembly of section .text:

0000000000400e20 <\_start>:

400e20: 31 ed xor %ebp,%ebp

400e22: 49 89 d1 mov %rdx,%r9

400e25: 5e pop %rsi

400e26: 48 89 e2 mov %rsp,%rdx

400e29: 48 83 e4 f0 and $0xfffffffffffffff0,%rsp

400e2d: 50 push %rax

400e2e: 54 push %rsp

400e2f: 49 c7 c0 00 2d 40 00 mov $0x402d00,%r8

400e36: 48 c7 c1 90 2c 40 00 mov $0x402c90,%rcx

400e3d: 48 c7 c7 e0 10 40 00 mov $0x4010e0,%rdi

400e44: e8 87 fe ff ff callq 400cd0 <\_\_libc\_start\_main@plt>

400e49: f4 hlt

400e4a: 66 0f 1f 44 00 00 nopw 0x0(%rax,%rax,1)

0000000000400e50 <deregister\_tm\_clones>:

400e50: b8 b7 54 60 00 mov $0x6054b7,%eax

400e55: 55 push %rbp

400e56: 48 2d b0 54 60 00 sub $0x6054b0,%rax

400e5c: 48 83 f8 0e cmp $0xe,%rax

400e60: 48 89 e5 mov %rsp,%rbp

400e63: 77 02 ja 400e67 <deregister\_tm\_clones+0x17>

400e65: 5d pop %rbp

400e66: c3 retq

400e67: b8 00 00 00 00 mov $0x0,%eax

400e6c: 48 85 c0 test %rax,%rax

400e6f: 74 f4 je 400e65 <deregister\_tm\_clones+0x15>

400e71: 5d pop %rbp

400e72: bf b0 54 60 00 mov $0x6054b0,%edi

400e77: ff e0 jmpq \*%rax

400e79: 0f 1f 80 00 00 00 00 nopl 0x0(%rax)

0000000000400e80 <register\_tm\_clones>:

400e80: b8 b0 54 60 00 mov $0x6054b0,%eax

400e85: 55 push %rbp

400e86: 48 2d b0 54 60 00 sub $0x6054b0,%rax

400e8c: 48 c1 f8 03 sar $0x3,%rax

400e90: 48 89 e5 mov %rsp,%rbp

400e93: 48 89 c2 mov %rax,%rdx

400e96: 48 c1 ea 3f shr $0x3f,%rdx

400e9a: 48 01 d0 add %rdx,%rax

400e9d: 48 d1 f8 sar %rax

400ea0: 75 02 jne 400ea4 <register\_tm\_clones+0x24>

400ea2: 5d pop %rbp

400ea3: c3 retq

400ea4: ba 00 00 00 00 mov $0x0,%edx

400ea9: 48 85 d2 test %rdx,%rdx

400eac: 74 f4 je 400ea2 <register\_tm\_clones+0x22>

400eae: 5d pop %rbp

400eaf: 48 89 c6 mov %rax,%rsi

400eb2: bf b0 54 60 00 mov $0x6054b0,%edi

400eb7: ff e2 jmpq \*%rdx

400eb9: 0f 1f 80 00 00 00 00 nopl 0x0(%rax)

0000000000400ec0 <\_\_do\_global\_dtors\_aux>:

400ec0: 80 3d 11 46 20 00 00 cmpb $0x0,0x204611(%rip) # 6054d8 <completed.6344>

400ec7: 75 11 jne 400eda <\_\_do\_global\_dtors\_aux+0x1a>

400ec9: 55 push %rbp

400eca: 48 89 e5 mov %rsp,%rbp

400ecd: e8 7e ff ff ff callq 400e50 <deregister\_tm\_clones>

400ed2: 5d pop %rbp

400ed3: c6 05 fe 45 20 00 01 movb $0x1,0x2045fe(%rip) # 6054d8 <completed.6344>

400eda: f3 c3 repz retq

400edc: 0f 1f 40 00 nopl 0x0(%rax)

0000000000400ee0 <frame\_dummy>:

400ee0: 48 83 3d 38 3f 20 00 cmpq $0x0,0x203f38(%rip) # 604e20 <\_\_JCR\_END\_\_>

400ee7: 00

400ee8: 74 1e je 400f08 <frame\_dummy+0x28>

400eea: b8 00 00 00 00 mov $0x0,%eax

400eef: 48 85 c0 test %rax,%rax

400ef2: 74 14 je 400f08 <frame\_dummy+0x28>

400ef4: 55 push %rbp

400ef5: bf 20 4e 60 00 mov $0x604e20,%edi

400efa: 48 89 e5 mov %rsp,%rbp

400efd: ff d0 callq \*%rax

400eff: 5d pop %rbp

400f00: e9 7b ff ff ff jmpq 400e80 <register\_tm\_clones>

400f05: 0f 1f 00 nopl (%rax)

400f08: e9 73 ff ff ff jmpq 400e80 <register\_tm\_clones>

400f0d: 0f 1f 00 nopl (%rax)

0000000000400f10 <usage>:

400f10: 48 83 ec 08 sub $0x8,%rsp

400f14: 48 89 fe mov %rdi,%rsi

400f17: 83 3d ea 45 20 00 00 cmpl $0x0,0x2045ea(%rip) # 605508 <is\_checker>

400f1e: 74 39 je 400f59 <usage+0x49>

400f20: bf 20 2d 40 00 mov $0x402d20,%edi

400f25: b8 00 00 00 00 mov $0x0,%eax

400f2a: e8 51 fd ff ff callq 400c80 <printf@plt>

400f2f: bf 58 2d 40 00 mov $0x402d58,%edi

400f34: e8 17 fd ff ff callq 400c50 <puts@plt>

400f39: bf d0 2e 40 00 mov $0x402ed0,%edi

400f3e: e8 0d fd ff ff callq 400c50 <puts@plt>

400f43: bf 80 2d 40 00 mov $0x402d80,%edi

400f48: e8 03 fd ff ff callq 400c50 <puts@plt>

400f4d: bf ea 2e 40 00 mov $0x402eea,%edi

400f52: e8 f9 fc ff ff callq 400c50 <puts@plt>

400f57: eb 2d jmp 400f86 <usage+0x76>

400f59: bf 06 2f 40 00 mov $0x402f06,%edi

400f5e: b8 00 00 00 00 mov $0x0,%eax

400f63: e8 18 fd ff ff callq 400c80 <printf@plt>

400f68: bf a8 2d 40 00 mov $0x402da8,%edi

400f6d: e8 de fc ff ff callq 400c50 <puts@plt>

400f72: bf d0 2d 40 00 mov $0x402dd0,%edi

400f77: e8 d4 fc ff ff callq 400c50 <puts@plt>

400f7c: bf 24 2f 40 00 mov $0x402f24,%edi

400f81: e8 ca fc ff ff callq 400c50 <puts@plt>

400f86: bf 00 00 00 00 mov $0x0,%edi

400f8b: e8 60 fe ff ff callq 400df0 <exit@plt>

0000000000400f90 <initialize\_target>:

400f90: 55 push %rbp

400f91: 53 push %rbx

400f92: 48 81 ec 08 21 00 00 sub $0x2108,%rsp

400f99: 89 f5 mov %esi,%ebp

400f9b: 89 3d 57 45 20 00 mov %edi,0x204557(%rip) # 6054f8 <check\_level>

400fa1: 8b 3d c1 41 20 00 mov 0x2041c1(%rip),%edi # 605168 <target\_id>

400fa7: e8 b7 1c 00 00 callq 402c63 <gencookie>

400fac: 89 05 52 45 20 00 mov %eax,0x204552(%rip) # 605504 <cookie>

400fb2: 89 c7 mov %eax,%edi

400fb4: e8 aa 1c 00 00 callq 402c63 <gencookie>

400fb9: 89 05 41 45 20 00 mov %eax,0x204541(%rip) # 605500 <authkey>

400fbf: 8b 05 a3 41 20 00 mov 0x2041a3(%rip),%eax # 605168 <target\_id>

400fc5: 8d 78 01 lea 0x1(%rax),%edi

400fc8: e8 53 fc ff ff callq 400c20 <srandom@plt>

400fcd: e8 7e fd ff ff callq 400d50 <random@plt>

400fd2: 89 c7 mov %eax,%edi

400fd4: e8 ca 02 00 00 callq 4012a3 <scramble>

400fd9: 89 c3 mov %eax,%ebx

400fdb: 85 ed test %ebp,%ebp

400fdd: 74 18 je 400ff7 <initialize\_target+0x67>

400fdf: bf 00 00 00 00 mov $0x0,%edi

400fe4: e8 57 fd ff ff callq 400d40 <time@plt>

400fe9: 89 c7 mov %eax,%edi

400feb: e8 30 fc ff ff callq 400c20 <srandom@plt>

400ff0: e8 5b fd ff ff callq 400d50 <random@plt>

400ff5: eb 05 jmp 400ffc <initialize\_target+0x6c>

400ff7: b8 00 00 00 00 mov $0x0,%eax

400ffc: 01 c3 add %eax,%ebx

400ffe: 0f b7 db movzwl %bx,%ebx

401001: 8d 04 dd 00 01 00 00 lea 0x100(,%rbx,8),%eax

401008: 89 c0 mov %eax,%eax

40100a: 48 89 05 8f 44 20 00 mov %rax,0x20448f(%rip) # 6054a0 <buf\_offset>

401011: c6 05 10 51 20 00 72 movb $0x72,0x205110(%rip) # 606128 <target\_prefix>

401018: 83 3d 89 44 20 00 00 cmpl $0x0,0x204489(%rip) # 6054a8 <notify>

40101f: 0f 84 b1 00 00 00 je 4010d6 <initialize\_target+0x146>

401025: 83 3d dc 44 20 00 00 cmpl $0x0,0x2044dc(%rip) # 605508 <is\_checker>

40102c: 0f 85 a4 00 00 00 jne 4010d6 <initialize\_target+0x146>

401032: be 00 01 00 00 mov $0x100,%esi

401037: 48 89 e7 mov %rsp,%rdi

40103a: e8 91 fd ff ff callq 400dd0 <gethostname@plt>

40103f: 85 c0 test %eax,%eax

401041: 74 25 je 401068 <initialize\_target+0xd8>

401043: bf 00 2e 40 00 mov $0x402e00,%edi

401048: e8 03 fc ff ff callq 400c50 <puts@plt>

40104d: bf 08 00 00 00 mov $0x8,%edi

401052: e8 99 fd ff ff callq 400df0 <exit@plt>

401057: 48 89 e6 mov %rsp,%rsi

40105a: e8 a1 fb ff ff callq 400c00 <strcasecmp@plt>

40105f: 85 c0 test %eax,%eax

401061: 74 21 je 401084 <initialize\_target+0xf4>

401063: 83 c3 01 add $0x1,%ebx

401066: eb 05 jmp 40106d <initialize\_target+0xdd>

401068: bb 00 00 00 00 mov $0x0,%ebx

40106d: 48 63 c3 movslq %ebx,%rax

401070: 48 8b 3c c5 80 51 60 mov 0x605180(,%rax,8),%rdi

401077: 00

401078: 48 85 ff test %rdi,%rdi

40107b: 75 da jne 401057 <initialize\_target+0xc7>

40107d: b8 00 00 00 00 mov $0x0,%eax

401082: eb 05 jmp 401089 <initialize\_target+0xf9>

401084: b8 01 00 00 00 mov $0x1,%eax

401089: 85 c0 test %eax,%eax

40108b: 75 17 jne 4010a4 <initialize\_target+0x114>

40108d: 48 89 e6 mov %rsp,%rsi

401090: bf 38 2e 40 00 mov $0x402e38,%edi

401095: e8 e6 fb ff ff callq 400c80 <printf@plt>

40109a: bf 08 00 00 00 mov $0x8,%edi

40109f: e8 4c fd ff ff callq 400df0 <exit@plt>

4010a4: 48 8d bc 24 00 01 00 lea 0x100(%rsp),%rdi

4010ab: 00

4010ac: e8 49 19 00 00 callq 4029fa <init\_driver>

4010b1: 85 c0 test %eax,%eax

4010b3: 79 21 jns 4010d6 <initialize\_target+0x146>

4010b5: 48 8d b4 24 00 01 00 lea 0x100(%rsp),%rsi

4010bc: 00

4010bd: bf 78 2e 40 00 mov $0x402e78,%edi

4010c2: b8 00 00 00 00 mov $0x0,%eax

4010c7: e8 b4 fb ff ff callq 400c80 <printf@plt>

4010cc: bf 08 00 00 00 mov $0x8,%edi

4010d1: e8 1a fd ff ff callq 400df0 <exit@plt>

4010d6: 48 81 c4 08 21 00 00 add $0x2108,%rsp

4010dd: 5b pop %rbx

4010de: 5d pop %rbp

4010df: c3 retq

00000000004010e0 <main>:

4010e0: 41 56 push %r14

4010e2: 41 55 push %r13

4010e4: 41 54 push %r12

4010e6: 55 push %rbp

4010e7: 53 push %rbx

4010e8: 41 89 fc mov %edi,%r12d

4010eb: 48 89 f3 mov %rsi,%rbx

4010ee: be 21 1e 40 00 mov $0x401e21,%esi

4010f3: bf 0b 00 00 00 mov $0xb,%edi

4010f8: e8 e3 fb ff ff callq 400ce0 <signal@plt>

4010fd: be d3 1d 40 00 mov $0x401dd3,%esi

401102: bf 07 00 00 00 mov $0x7,%edi

401107: e8 d4 fb ff ff callq 400ce0 <signal@plt>

40110c: be 6f 1e 40 00 mov $0x401e6f,%esi

401111: bf 04 00 00 00 mov $0x4,%edi

401116: e8 c5 fb ff ff callq 400ce0 <signal@plt>

40111b: 83 3d e6 43 20 00 00 cmpl $0x0,0x2043e6(%rip) # 605508 <is\_checker>

401122: 74 20 je 401144 <main+0x64>

401124: be bd 1e 40 00 mov $0x401ebd,%esi

401129: bf 0e 00 00 00 mov $0xe,%edi

40112e: e8 ad fb ff ff callq 400ce0 <signal@plt>

401133: bf 05 00 00 00 mov $0x5,%edi

401138: e8 63 fb ff ff callq 400ca0 <alarm@plt>

40113d: bd 42 2f 40 00 mov $0x402f42,%ebp

401142: eb 05 jmp 401149 <main+0x69>

401144: bd 3d 2f 40 00 mov $0x402f3d,%ebp

401149: 48 8b 05 70 43 20 00 mov 0x204370(%rip),%rax # 6054c0 <stdin@@GLIBC\_2.2.5>

401150: 48 89 05 99 43 20 00 mov %rax,0x204399(%rip) # 6054f0 <infile>

401157: 41 bd 00 00 00 00 mov $0x0,%r13d

40115d: 41 be 00 00 00 00 mov $0x0,%r14d

401163: e9 b9 00 00 00 jmpq 401221 <main+0x141>

401168: 83 e8 61 sub $0x61,%eax

40116b: 3c 10 cmp $0x10,%al

40116d: 0f 87 93 00 00 00 ja 401206 <main+0x126>

401173: 0f b6 c0 movzbl %al,%eax

401176: ff 24 c5 88 2f 40 00 jmpq \*0x402f88(,%rax,8)

40117d: 48 8b 3b mov (%rbx),%rdi

401180: e8 8b fd ff ff callq 400f10 <usage>

401185: be e5 31 40 00 mov $0x4031e5,%esi

40118a: 48 8b 3d 37 43 20 00 mov 0x204337(%rip),%rdi # 6054c8 <optarg@@GLIBC\_2.2.5>

401191: e8 0a fc ff ff callq 400da0 <fopen@plt>

401196: 48 89 05 53 43 20 00 mov %rax,0x204353(%rip) # 6054f0 <infile>

40119d: 48 85 c0 test %rax,%rax

4011a0: 75 7f jne 401221 <main+0x141>

4011a2: 48 8b 15 1f 43 20 00 mov 0x20431f(%rip),%rdx # 6054c8 <optarg@@GLIBC\_2.2.5>

4011a9: be 4a 2f 40 00 mov $0x402f4a,%esi

4011ae: 48 8b 3d 1b 43 20 00 mov 0x20431b(%rip),%rdi # 6054d0 <stderr@@GLIBC\_2.2.5>

4011b5: e8 46 fb ff ff callq 400d00 <fprintf@plt>

4011ba: b8 01 00 00 00 mov $0x1,%eax

4011bf: e9 d6 00 00 00 jmpq 40129a <main+0x1ba>

4011c4: ba 10 00 00 00 mov $0x10,%edx

4011c9: be 00 00 00 00 mov $0x0,%esi

4011ce: 48 8b 3d f3 42 20 00 mov 0x2042f3(%rip),%rdi # 6054c8 <optarg@@GLIBC\_2.2.5>

4011d5: e8 e6 fb ff ff callq 400dc0 <strtoul@plt>

4011da: 41 89 c6 mov %eax,%r14d

4011dd: eb 42 jmp 401221 <main+0x141>

4011df: ba 0a 00 00 00 mov $0xa,%edx

4011e4: be 00 00 00 00 mov $0x0,%esi

4011e9: 48 8b 3d d8 42 20 00 mov 0x2042d8(%rip),%rdi # 6054c8 <optarg@@GLIBC\_2.2.5>

4011f0: e8 2b fb ff ff callq 400d20 <strtol@plt>

4011f5: 41 89 c5 mov %eax,%r13d

4011f8: eb 27 jmp 401221 <main+0x141>

4011fa: c7 05 a4 42 20 00 00 movl $0x0,0x2042a4(%rip) # 6054a8 <notify>

401201: 00 00 00

401204: eb 1b jmp 401221 <main+0x141>

401206: 40 0f be f6 movsbl %sil,%esi

40120a: bf 67 2f 40 00 mov $0x402f67,%edi

40120f: b8 00 00 00 00 mov $0x0,%eax

401214: e8 67 fa ff ff callq 400c80 <printf@plt>

401219: 48 8b 3b mov (%rbx),%rdi

40121c: e8 ef fc ff ff callq 400f10 <usage>

401221: 48 89 ea mov %rbp,%rdx

401224: 48 89 de mov %rbx,%rsi

401227: 44 89 e7 mov %r12d,%edi

40122a: e8 81 fb ff ff callq 400db0 <getopt@plt>

40122f: 89 c6 mov %eax,%esi

401231: 3c ff cmp $0xff,%al

401233: 0f 85 2f ff ff ff jne 401168 <main+0x88>

401239: be 01 00 00 00 mov $0x1,%esi

40123e: 44 89 ef mov %r13d,%edi

401241: e8 4a fd ff ff callq 400f90 <initialize\_target>

401246: 83 3d bb 42 20 00 00 cmpl $0x0,0x2042bb(%rip) # 605508 <is\_checker>

40124d: 74 25 je 401274 <main+0x194>

40124f: 44 3b 35 aa 42 20 00 cmp 0x2042aa(%rip),%r14d # 605500 <authkey>

401256: 74 1c je 401274 <main+0x194>

401258: 44 89 f6 mov %r14d,%esi

40125b: bf a0 2e 40 00 mov $0x402ea0,%edi

401260: b8 00 00 00 00 mov $0x0,%eax

401265: e8 16 fa ff ff callq 400c80 <printf@plt>

40126a: b8 00 00 00 00 mov $0x0,%eax

40126f: e8 67 08 00 00 callq 401adb <check\_fail>

401274: 8b 35 8a 42 20 00 mov 0x20428a(%rip),%esi # 605504 <cookie>

40127a: bf 7a 2f 40 00 mov $0x402f7a,%edi

40127f: b8 00 00 00 00 mov $0x0,%eax

401284: e8 f7 f9 ff ff callq 400c80 <printf@plt>

401289: 48 8b 3d 10 42 20 00 mov 0x204210(%rip),%rdi # 6054a0 <buf\_offset>

401290: e8 76 0c 00 00 callq 401f0b <launch>

401295: b8 00 00 00 00 mov $0x0,%eax

40129a: 5b pop %rbx

40129b: 5d pop %rbp

40129c: 41 5c pop %r12

40129e: 41 5d pop %r13

4012a0: 41 5e pop %r14

4012a2: c3 retq

00000000004012a3 <scramble>:

4012a3: b8 00 00 00 00 mov $0x0,%eax

4012a8: eb 11 jmp 4012bb <scramble+0x18>

4012aa: 69 c8 73 30 00 00 imul $0x3073,%eax,%ecx

4012b0: 01 f9 add %edi,%ecx

4012b2: 89 c2 mov %eax,%edx

4012b4: 89 4c 94 d0 mov %ecx,-0x30(%rsp,%rdx,4)

4012b8: 83 c0 01 add $0x1,%eax

4012bb: 83 f8 09 cmp $0x9,%eax

4012be: 76 ea jbe 4012aa <scramble+0x7>

4012c0: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4012c4: 69 c0 9a 75 00 00 imul $0x759a,%eax,%eax

4012ca: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4012ce: 8b 44 24 f4 mov -0xc(%rsp),%eax

4012d2: 69 c0 1c 73 00 00 imul $0x731c,%eax,%eax

4012d8: 89 44 24 f4 mov %eax,-0xc(%rsp)

4012dc: 8b 44 24 d0 mov -0x30(%rsp),%eax

4012e0: 69 c0 1f af 00 00 imul $0xaf1f,%eax,%eax

4012e6: 89 44 24 d0 mov %eax,-0x30(%rsp)

4012ea: 8b 44 24 e0 mov -0x20(%rsp),%eax

4012ee: 69 c0 0b 5a 00 00 imul $0x5a0b,%eax,%eax

4012f4: 89 44 24 e0 mov %eax,-0x20(%rsp)

4012f8: 8b 44 24 f0 mov -0x10(%rsp),%eax

4012fc: 69 c0 3e 17 00 00 imul $0x173e,%eax,%eax

401302: 89 44 24 f0 mov %eax,-0x10(%rsp)

401306: 8b 44 24 f0 mov -0x10(%rsp),%eax

40130a: 69 c0 c1 fd 00 00 imul $0xfdc1,%eax,%eax

401310: 89 44 24 f0 mov %eax,-0x10(%rsp)

401314: 8b 44 24 d8 mov -0x28(%rsp),%eax

401318: 69 c0 6e 05 00 00 imul $0x56e,%eax,%eax

40131e: 89 44 24 d8 mov %eax,-0x28(%rsp)

401322: 8b 44 24 d8 mov -0x28(%rsp),%eax

401326: 69 c0 a5 90 00 00 imul $0x90a5,%eax,%eax

40132c: 89 44 24 d8 mov %eax,-0x28(%rsp)

401330: 8b 44 24 d0 mov -0x30(%rsp),%eax

401334: 69 c0 a3 0c 00 00 imul $0xca3,%eax,%eax

40133a: 89 44 24 d0 mov %eax,-0x30(%rsp)

40133e: 8b 44 24 d0 mov -0x30(%rsp),%eax

401342: 69 c0 33 cd 00 00 imul $0xcd33,%eax,%eax

401348: 89 44 24 d0 mov %eax,-0x30(%rsp)

40134c: 8b 44 24 e8 mov -0x18(%rsp),%eax

401350: 69 c0 82 3b 00 00 imul $0x3b82,%eax,%eax

401356: 89 44 24 e8 mov %eax,-0x18(%rsp)

40135a: 8b 44 24 e8 mov -0x18(%rsp),%eax

40135e: 69 c0 d3 6c 00 00 imul $0x6cd3,%eax,%eax

401364: 89 44 24 e8 mov %eax,-0x18(%rsp)

401368: 8b 44 24 dc mov -0x24(%rsp),%eax

40136c: 69 c0 2c 08 00 00 imul $0x82c,%eax,%eax

401372: 89 44 24 dc mov %eax,-0x24(%rsp)

401376: 8b 44 24 f0 mov -0x10(%rsp),%eax

40137a: 69 c0 2b 41 00 00 imul $0x412b,%eax,%eax

401380: 89 44 24 f0 mov %eax,-0x10(%rsp)

401384: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401388: 69 c0 05 30 00 00 imul $0x3005,%eax,%eax

40138e: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401392: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401396: 69 c0 ef 6a 00 00 imul $0x6aef,%eax,%eax

40139c: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4013a0: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4013a4: 69 c0 2e 16 00 00 imul $0x162e,%eax,%eax

4013aa: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4013ae: 8b 44 24 ec mov -0x14(%rsp),%eax

4013b2: 69 c0 4d 7b 00 00 imul $0x7b4d,%eax,%eax

4013b8: 89 44 24 ec mov %eax,-0x14(%rsp)

4013bc: 8b 44 24 d0 mov -0x30(%rsp),%eax

4013c0: 69 c0 b6 fe 00 00 imul $0xfeb6,%eax,%eax

4013c6: 89 44 24 d0 mov %eax,-0x30(%rsp)

4013ca: 8b 44 24 d8 mov -0x28(%rsp),%eax

4013ce: 69 c0 e6 5f 00 00 imul $0x5fe6,%eax,%eax

4013d4: 89 44 24 d8 mov %eax,-0x28(%rsp)

4013d8: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4013dc: 69 c0 72 e1 00 00 imul $0xe172,%eax,%eax

4013e2: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4013e6: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4013ea: 69 c0 58 cd 00 00 imul $0xcd58,%eax,%eax

4013f0: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4013f4: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4013f8: 69 c0 28 77 00 00 imul $0x7728,%eax,%eax

4013fe: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401402: 8b 44 24 f4 mov -0xc(%rsp),%eax

401406: 69 c0 67 c3 00 00 imul $0xc367,%eax,%eax

40140c: 89 44 24 f4 mov %eax,-0xc(%rsp)

401410: 8b 44 24 d8 mov -0x28(%rsp),%eax

401414: 69 c0 0c 9a 00 00 imul $0x9a0c,%eax,%eax

40141a: 89 44 24 d8 mov %eax,-0x28(%rsp)

40141e: 8b 44 24 e0 mov -0x20(%rsp),%eax

401422: 69 c0 e8 0a 00 00 imul $0xae8,%eax,%eax

401428: 89 44 24 e0 mov %eax,-0x20(%rsp)

40142c: 8b 44 24 dc mov -0x24(%rsp),%eax

401430: 69 c0 da 9a 00 00 imul $0x9ada,%eax,%eax

401436: 89 44 24 dc mov %eax,-0x24(%rsp)

40143a: 8b 44 24 d0 mov -0x30(%rsp),%eax

40143e: 69 c0 c4 86 00 00 imul $0x86c4,%eax,%eax

401444: 89 44 24 d0 mov %eax,-0x30(%rsp)

401448: 8b 44 24 e0 mov -0x20(%rsp),%eax

40144c: 69 c0 02 59 00 00 imul $0x5902,%eax,%eax

401452: 89 44 24 e0 mov %eax,-0x20(%rsp)

401456: 8b 44 24 d4 mov -0x2c(%rsp),%eax

40145a: 69 c0 82 dd 00 00 imul $0xdd82,%eax,%eax

401460: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401464: 8b 44 24 ec mov -0x14(%rsp),%eax

401468: 69 c0 55 31 00 00 imul $0x3155,%eax,%eax

40146e: 89 44 24 ec mov %eax,-0x14(%rsp)

401472: 8b 44 24 f4 mov -0xc(%rsp),%eax

401476: 69 c0 a7 2e 00 00 imul $0x2ea7,%eax,%eax

40147c: 89 44 24 f4 mov %eax,-0xc(%rsp)

401480: 8b 44 24 f0 mov -0x10(%rsp),%eax

401484: 69 c0 7d 26 00 00 imul $0x267d,%eax,%eax

40148a: 89 44 24 f0 mov %eax,-0x10(%rsp)

40148e: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401492: 69 c0 d0 0c 00 00 imul $0xcd0,%eax,%eax

401498: 89 44 24 d4 mov %eax,-0x2c(%rsp)

40149c: 8b 44 24 d8 mov -0x28(%rsp),%eax

4014a0: 69 c0 4c 7f 00 00 imul $0x7f4c,%eax,%eax

4014a6: 89 44 24 d8 mov %eax,-0x28(%rsp)

4014aa: 8b 44 24 d4 mov -0x2c(%rsp),%eax

4014ae: 69 c0 27 3b 00 00 imul $0x3b27,%eax,%eax

4014b4: 89 44 24 d4 mov %eax,-0x2c(%rsp)

4014b8: 8b 44 24 dc mov -0x24(%rsp),%eax

4014bc: 69 c0 5e ca 00 00 imul $0xca5e,%eax,%eax

4014c2: 89 44 24 dc mov %eax,-0x24(%rsp)

4014c6: 8b 44 24 d8 mov -0x28(%rsp),%eax

4014ca: 69 c0 a0 47 00 00 imul $0x47a0,%eax,%eax

4014d0: 89 44 24 d8 mov %eax,-0x28(%rsp)

4014d4: 8b 44 24 ec mov -0x14(%rsp),%eax

4014d8: 69 c0 7c 66 00 00 imul $0x667c,%eax,%eax

4014de: 89 44 24 ec mov %eax,-0x14(%rsp)

4014e2: 8b 44 24 d0 mov -0x30(%rsp),%eax

4014e6: 69 c0 51 c5 00 00 imul $0xc551,%eax,%eax

4014ec: 89 44 24 d0 mov %eax,-0x30(%rsp)

4014f0: 8b 44 24 dc mov -0x24(%rsp),%eax

4014f4: 69 c0 a1 9b 00 00 imul $0x9ba1,%eax,%eax

4014fa: 89 44 24 dc mov %eax,-0x24(%rsp)

4014fe: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401502: 69 c0 17 d6 00 00 imul $0xd617,%eax,%eax

401508: 89 44 24 d4 mov %eax,-0x2c(%rsp)

40150c: 8b 44 24 ec mov -0x14(%rsp),%eax

401510: 69 c0 f4 47 00 00 imul $0x47f4,%eax,%eax

401516: 89 44 24 ec mov %eax,-0x14(%rsp)

40151a: 8b 44 24 d8 mov -0x28(%rsp),%eax

40151e: 69 c0 81 d9 00 00 imul $0xd981,%eax,%eax

401524: 89 44 24 d8 mov %eax,-0x28(%rsp)

401528: 8b 44 24 dc mov -0x24(%rsp),%eax

40152c: 69 c0 cb 24 00 00 imul $0x24cb,%eax,%eax

401532: 89 44 24 dc mov %eax,-0x24(%rsp)

401536: 8b 44 24 d0 mov -0x30(%rsp),%eax

40153a: 69 c0 fe 29 00 00 imul $0x29fe,%eax,%eax

401540: 89 44 24 d0 mov %eax,-0x30(%rsp)

401544: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401548: 69 c0 74 ff 00 00 imul $0xff74,%eax,%eax

40154e: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401552: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401556: 69 c0 a3 9d 00 00 imul $0x9da3,%eax,%eax

40155c: 89 44 24 e4 mov %eax,-0x1c(%rsp)

401560: 8b 44 24 d8 mov -0x28(%rsp),%eax

401564: 69 c0 99 02 00 00 imul $0x299,%eax,%eax

40156a: 89 44 24 d8 mov %eax,-0x28(%rsp)

40156e: 8b 44 24 f4 mov -0xc(%rsp),%eax

401572: 69 c0 24 4b 00 00 imul $0x4b24,%eax,%eax

401578: 89 44 24 f4 mov %eax,-0xc(%rsp)

40157c: 8b 44 24 dc mov -0x24(%rsp),%eax

401580: 69 c0 2a 17 00 00 imul $0x172a,%eax,%eax

401586: 89 44 24 dc mov %eax,-0x24(%rsp)

40158a: 8b 44 24 d0 mov -0x30(%rsp),%eax

40158e: 69 c0 a4 e2 00 00 imul $0xe2a4,%eax,%eax

401594: 89 44 24 d0 mov %eax,-0x30(%rsp)

401598: 8b 44 24 d0 mov -0x30(%rsp),%eax

40159c: 69 c0 ca 8e 00 00 imul $0x8eca,%eax,%eax

4015a2: 89 44 24 d0 mov %eax,-0x30(%rsp)

4015a6: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4015aa: 69 c0 0e 23 00 00 imul $0x230e,%eax,%eax

4015b0: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4015b4: 8b 44 24 e8 mov -0x18(%rsp),%eax

4015b8: 69 c0 4b 35 00 00 imul $0x354b,%eax,%eax

4015be: 89 44 24 e8 mov %eax,-0x18(%rsp)

4015c2: 8b 44 24 f0 mov -0x10(%rsp),%eax

4015c6: 69 c0 cf f5 00 00 imul $0xf5cf,%eax,%eax

4015cc: 89 44 24 f0 mov %eax,-0x10(%rsp)

4015d0: 8b 44 24 d8 mov -0x28(%rsp),%eax

4015d4: 69 c0 99 b8 00 00 imul $0xb899,%eax,%eax

4015da: 89 44 24 d8 mov %eax,-0x28(%rsp)

4015de: 8b 44 24 e8 mov -0x18(%rsp),%eax

4015e2: 69 c0 67 07 00 00 imul $0x767,%eax,%eax

4015e8: 89 44 24 e8 mov %eax,-0x18(%rsp)

4015ec: 8b 44 24 d8 mov -0x28(%rsp),%eax

4015f0: 69 c0 c2 28 00 00 imul $0x28c2,%eax,%eax

4015f6: 89 44 24 d8 mov %eax,-0x28(%rsp)

4015fa: 8b 44 24 ec mov -0x14(%rsp),%eax

4015fe: 69 c0 84 a1 00 00 imul $0xa184,%eax,%eax

401604: 89 44 24 ec mov %eax,-0x14(%rsp)

401608: 8b 44 24 f0 mov -0x10(%rsp),%eax

40160c: 69 c0 eb 97 00 00 imul $0x97eb,%eax,%eax

401612: 89 44 24 f0 mov %eax,-0x10(%rsp)

401616: 8b 44 24 f0 mov -0x10(%rsp),%eax

40161a: 69 c0 ae a3 00 00 imul $0xa3ae,%eax,%eax

401620: 89 44 24 f0 mov %eax,-0x10(%rsp)

401624: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401628: 69 c0 27 24 00 00 imul $0x2427,%eax,%eax

40162e: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401632: 8b 44 24 d8 mov -0x28(%rsp),%eax

401636: 69 c0 51 bd 00 00 imul $0xbd51,%eax,%eax

40163c: 89 44 24 d8 mov %eax,-0x28(%rsp)

401640: 8b 44 24 e0 mov -0x20(%rsp),%eax

401644: 69 c0 c5 eb 00 00 imul $0xebc5,%eax,%eax

40164a: 89 44 24 e0 mov %eax,-0x20(%rsp)

40164e: 8b 44 24 d0 mov -0x30(%rsp),%eax

401652: 69 c0 a6 a8 00 00 imul $0xa8a6,%eax,%eax

401658: 89 44 24 d0 mov %eax,-0x30(%rsp)

40165c: 8b 44 24 e8 mov -0x18(%rsp),%eax

401660: 69 c0 c1 8e 00 00 imul $0x8ec1,%eax,%eax

401666: 89 44 24 e8 mov %eax,-0x18(%rsp)

40166a: 8b 44 24 f0 mov -0x10(%rsp),%eax

40166e: 69 c0 78 e4 00 00 imul $0xe478,%eax,%eax

401674: 89 44 24 f0 mov %eax,-0x10(%rsp)

401678: 8b 44 24 ec mov -0x14(%rsp),%eax

40167c: 69 c0 8f b5 00 00 imul $0xb58f,%eax,%eax

401682: 89 44 24 ec mov %eax,-0x14(%rsp)

401686: 8b 44 24 e8 mov -0x18(%rsp),%eax

40168a: 69 c0 d3 12 00 00 imul $0x12d3,%eax,%eax

401690: 89 44 24 e8 mov %eax,-0x18(%rsp)

401694: 8b 44 24 d0 mov -0x30(%rsp),%eax

401698: 69 c0 e7 ef 00 00 imul $0xefe7,%eax,%eax

40169e: 89 44 24 d0 mov %eax,-0x30(%rsp)

4016a2: 8b 44 24 e0 mov -0x20(%rsp),%eax

4016a6: 69 c0 c7 bb 00 00 imul $0xbbc7,%eax,%eax

4016ac: 89 44 24 e0 mov %eax,-0x20(%rsp)

4016b0: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016b4: 69 c0 89 d4 00 00 imul $0xd489,%eax,%eax

4016ba: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016be: 8b 44 24 ec mov -0x14(%rsp),%eax

4016c2: 69 c0 0e a9 00 00 imul $0xa90e,%eax,%eax

4016c8: 89 44 24 ec mov %eax,-0x14(%rsp)

4016cc: 8b 44 24 e4 mov -0x1c(%rsp),%eax

4016d0: 69 c0 17 a4 00 00 imul $0xa417,%eax,%eax

4016d6: 89 44 24 e4 mov %eax,-0x1c(%rsp)

4016da: 8b 44 24 e8 mov -0x18(%rsp),%eax

4016de: 69 c0 d8 4a 00 00 imul $0x4ad8,%eax,%eax

4016e4: 89 44 24 e8 mov %eax,-0x18(%rsp)

4016e8: 8b 44 24 e0 mov -0x20(%rsp),%eax

4016ec: 69 c0 15 16 00 00 imul $0x1615,%eax,%eax

4016f2: 89 44 24 e0 mov %eax,-0x20(%rsp)

4016f6: 8b 44 24 ec mov -0x14(%rsp),%eax

4016fa: 69 c0 62 d3 00 00 imul $0xd362,%eax,%eax

401700: 89 44 24 ec mov %eax,-0x14(%rsp)

401704: 8b 44 24 d8 mov -0x28(%rsp),%eax

401708: 69 c0 d8 2f 00 00 imul $0x2fd8,%eax,%eax

40170e: 89 44 24 d8 mov %eax,-0x28(%rsp)

401712: 8b 44 24 d4 mov -0x2c(%rsp),%eax

401716: 69 c0 fd 60 00 00 imul $0x60fd,%eax,%eax

40171c: 89 44 24 d4 mov %eax,-0x2c(%rsp)

401720: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401724: 69 c0 2a 99 00 00 imul $0x992a,%eax,%eax

40172a: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40172e: 8b 44 24 e4 mov -0x1c(%rsp),%eax

401732: 69 c0 8d 1e 00 00 imul $0x1e8d,%eax,%eax

401738: 89 44 24 e4 mov %eax,-0x1c(%rsp)

40173c: 8b 44 24 f0 mov -0x10(%rsp),%eax

401740: 69 c0 ee 94 00 00 imul $0x94ee,%eax,%eax

401746: 89 44 24 f0 mov %eax,-0x10(%rsp)

40174a: 8b 44 24 dc mov -0x24(%rsp),%eax

40174e: 69 c0 77 45 00 00 imul $0x4577,%eax,%eax

401754: 89 44 24 dc mov %eax,-0x24(%rsp)

401758: 8b 44 24 f0 mov -0x10(%rsp),%eax

40175c: 69 c0 8d 0a 00 00 imul $0xa8d,%eax,%eax

401762: 89 44 24 f0 mov %eax,-0x10(%rsp)

401766: 8b 44 24 d0 mov -0x30(%rsp),%eax

40176a: 69 c0 57 35 00 00 imul $0x3557,%eax,%eax

401770: 89 44 24 d0 mov %eax,-0x30(%rsp)

401774: 8b 44 24 f0 mov -0x10(%rsp),%eax

401778: 69 c0 ed 60 00 00 imul $0x60ed,%eax,%eax

40177e: 89 44 24 f0 mov %eax,-0x10(%rsp)

401782: 8b 44 24 e8 mov -0x18(%rsp),%eax

401786: 69 c0 f4 a2 00 00 imul $0xa2f4,%eax,%eax

40178c: 89 44 24 e8 mov %eax,-0x18(%rsp)

401790: ba 00 00 00 00 mov $0x0,%edx

401795: b8 00 00 00 00 mov $0x0,%eax

40179a: eb 0b jmp 4017a7 <scramble+0x504>

40179c: 89 d1 mov %edx,%ecx

40179e: 8b 4c 8c d0 mov -0x30(%rsp,%rcx,4),%ecx

4017a2: 01 c8 add %ecx,%eax

4017a4: 83 c2 01 add $0x1,%edx

4017a7: 83 fa 09 cmp $0x9,%edx

4017aa: 76 f0 jbe 40179c <scramble+0x4f9>

4017ac: f3 c3 repz retq

00000000004017ae <getbuf>:

4017ae: 48 83 ec 18 sub $0x18,%rsp

4017b2: 48 89 e7 mov %rsp,%rdi

4017b5: e8 50 03 00 00 callq 401b0a <Gets>

4017ba: b8 01 00 00 00 mov $0x1,%eax

4017bf: 48 83 c4 18 add $0x18,%rsp

4017c3: c3 retq

00000000004017c4 <touch1>:

4017c4: 48 83 ec 08 sub $0x8,%rsp

4017c8: c7 05 2a 3d 20 00 01 movl $0x1,0x203d2a(%rip) # 6054fc <vlevel>

4017cf: 00 00 00

4017d2: bf 3e 30 40 00 mov $0x40303e,%edi

4017d7: e8 74 f4 ff ff callq 400c50 <puts@plt>

4017dc: bf 01 00 00 00 mov $0x1,%edi

4017e1: e8 13 05 00 00 callq 401cf9 <validate>

4017e6: bf 00 00 00 00 mov $0x0,%edi

4017eb: e8 00 f6 ff ff callq 400df0 <exit@plt>

00000000004017f0 <touch2>:

4017f0: 48 83 ec 08 sub $0x8,%rsp

4017f4: 89 fe mov %edi,%esi

4017f6: c7 05 fc 3c 20 00 02 movl $0x2,0x203cfc(%rip) # 6054fc <vlevel>

4017fd: 00 00 00

401800: 3b 3d fe 3c 20 00 cmp 0x203cfe(%rip),%edi # 605504 <cookie>

401806: 75 1b jne 401823 <touch2+0x33>

401808: bf 60 30 40 00 mov $0x403060,%edi

40180d: b8 00 00 00 00 mov $0x0,%eax

401812: e8 69 f4 ff ff callq 400c80 <printf@plt>

401817: bf 02 00 00 00 mov $0x2,%edi

40181c: e8 d8 04 00 00 callq 401cf9 <validate>

401821: eb 19 jmp 40183c <touch2+0x4c>

401823: bf 88 30 40 00 mov $0x403088,%edi

401828: b8 00 00 00 00 mov $0x0,%eax

40182d: e8 4e f4 ff ff callq 400c80 <printf@plt>

401832: bf 02 00 00 00 mov $0x2,%edi

401837: e8 6f 05 00 00 callq 401dab <fail>

40183c: bf 00 00 00 00 mov $0x0,%edi

401841: e8 aa f5 ff ff callq 400df0 <exit@plt>

0000000000401846 <hexmatch>:

401846: 41 54 push %r12

401848: 55 push %rbp

401849: 53 push %rbx

40184a: 48 83 ec 70 sub $0x70,%rsp

40184e: 41 89 fc mov %edi,%r12d

401851: 48 89 f5 mov %rsi,%rbp

401854: e8 f7 f4 ff ff callq 400d50 <random@plt>

401859: 48 89 c1 mov %rax,%rcx

40185c: 48 ba 0b d7 a3 70 3d movabs $0xa3d70a3d70a3d70b,%rdx

401863: 0a d7 a3

401866: 48 f7 ea imul %rdx

401869: 48 8d 04 0a lea (%rdx,%rcx,1),%rax

40186d: 48 c1 f8 06 sar $0x6,%rax

401871: 48 89 ce mov %rcx,%rsi

401874: 48 c1 fe 3f sar $0x3f,%rsi

401878: 48 29 f0 sub %rsi,%rax

40187b: 48 8d 04 80 lea (%rax,%rax,4),%rax

40187f: 48 8d 04 80 lea (%rax,%rax,4),%rax

401883: 48 c1 e0 02 shl $0x2,%rax

401887: 48 29 c1 sub %rax,%rcx

40188a: 48 8d 1c 0c lea (%rsp,%rcx,1),%rbx

40188e: 44 89 e2 mov %r12d,%edx

401891: be 5b 30 40 00 mov $0x40305b,%esi

401896: 48 89 df mov %rbx,%rdi

401899: b8 00 00 00 00 mov $0x0,%eax

40189e: e8 3d f5 ff ff callq 400de0 <sprintf@plt>

4018a3: ba 09 00 00 00 mov $0x9,%edx

4018a8: 48 89 de mov %rbx,%rsi

4018ab: 48 89 ef mov %rbp,%rdi

4018ae: e8 7d f3 ff ff callq 400c30 <strncmp@plt>

4018b3: 85 c0 test %eax,%eax

4018b5: 0f 94 c0 sete %al

4018b8: 0f b6 c0 movzbl %al,%eax

4018bb: 48 83 c4 70 add $0x70,%rsp

4018bf: 5b pop %rbx

4018c0: 5d pop %rbp

4018c1: 41 5c pop %r12

4018c3: c3 retq

00000000004018c4 <touch3>:

4018c4: 53 push %rbx

4018c5: 48 89 fb mov %rdi,%rbx

4018c8: c7 05 2a 3c 20 00 03 movl $0x3,0x203c2a(%rip) # 6054fc <vlevel>

4018cf: 00 00 00

4018d2: 48 89 fe mov %rdi,%rsi

4018d5: 8b 3d 29 3c 20 00 mov 0x203c29(%rip),%edi # 605504 <cookie>

4018db: e8 66 ff ff ff callq 401846 <hexmatch>

4018e0: 85 c0 test %eax,%eax

4018e2: 74 1e je 401902 <touch3+0x3e>

4018e4: 48 89 de mov %rbx,%rsi

4018e7: bf b0 30 40 00 mov $0x4030b0,%edi

4018ec: b8 00 00 00 00 mov $0x0,%eax

4018f1: e8 8a f3 ff ff callq 400c80 <printf@plt>

4018f6: bf 03 00 00 00 mov $0x3,%edi

4018fb: e8 f9 03 00 00 callq 401cf9 <validate>

401900: eb 1c jmp 40191e <touch3+0x5a>

401902: 48 89 de mov %rbx,%rsi

401905: bf d8 30 40 00 mov $0x4030d8,%edi

40190a: b8 00 00 00 00 mov $0x0,%eax

40190f: e8 6c f3 ff ff callq 400c80 <printf@plt>

401914: bf 03 00 00 00 mov $0x3,%edi

401919: e8 8d 04 00 00 callq 401dab <fail>

40191e: bf 00 00 00 00 mov $0x0,%edi

401923: e8 c8 f4 ff ff callq 400df0 <exit@plt>

0000000000401928 <test>:

401928: 48 83 ec 08 sub $0x8,%rsp

40192c: b8 00 00 00 00 mov $0x0,%eax

401931: e8 78 fe ff ff callq 4017ae <getbuf>

401936: 89 c6 mov %eax,%esi

401938: bf 00 31 40 00 mov $0x403100,%edi

40193d: b8 00 00 00 00 mov $0x0,%eax

401942: e8 39 f3 ff ff callq 400c80 <printf@plt>

401947: 48 83 c4 08 add $0x8,%rsp

40194b: c3 retq

000000000040194c <start\_farm>:

40194c: b8 01 00 00 00 mov $0x1,%eax

401951: c3 retq

0000000000401952 <getval\_326>:

401952: b8 58 90 91 c3 mov $0xc3919058,%eax

401957: c3 retq

0000000000401958 <addval\_492>:

401958: 8d 87 48 89 c7 92 lea -0x6d3876b8(%rdi),%eax

40195e: c3 retq

000000000040195f <addval\_454>:

40195f: 8d 87 48 89 c7 c3 lea -0x3c3876b8(%rdi),%eax

401965: c3 retq

0000000000401966 <addval\_199>:

401966: 8d 87 51 58 90 90 lea -0x6f6fa7af(%rdi),%eax

40196c: c3 retq

000000000040196d <getval\_422>:

40196d: b8 3d 58 92 90 mov $0x9092583d,%eax

401972: c3 retq

0000000000401973 <setval\_446>:

401973: c7 07 a6 85 27 58 movl $0x582785a6,(%rdi)

401979: c3 retq

000000000040197a <addval\_323>:

40197a: 8d 87 48 89 c7 c3 lea -0x3c3876b8(%rdi),%eax

401980: c3 retq

0000000000401981 <getval\_279>:

401981: b8 49 89 c7 c3 mov $0xc3c78949,%eax

401986: c3 retq

0000000000401987 <mid\_farm>:

401987: b8 01 00 00 00 mov $0x1,%eax

40198c: c3 retq

000000000040198d <add\_xy>:

40198d: 48 8d 04 37 lea (%rdi,%rsi,1),%rax

401991: c3 retq

0000000000401992 <getval\_164>:

401992: b8 8b d6 20 d2 mov $0xd220d68b,%eax

401997: c3 retq

0000000000401998 <addval\_145>:

401998: 8d 87 99 ca 08 c0 lea -0x3ff73567(%rdi),%eax

40199e: c3 retq

000000000040199f <addval\_241>:

40199f: 8d 87 89 ca 84 c0 lea -0x3f7b3577(%rdi),%eax

4019a5: c3 retq

00000000004019a6 <addval\_433>:

4019a6: 8d 87 89 d6 28 c0 lea -0x3fd72977(%rdi),%eax

4019ac: c3 retq

00000000004019ad <setval\_249>:

4019ad: c7 07 14 89 ca 91 movl $0x91ca8914,(%rdi)

4019b3: c3 retq

00000000004019b4 <setval\_205>:

4019b4: c7 07 81 c1 20 c0 movl $0xc020c181,(%rdi)

4019ba: c3 retq

00000000004019bb <setval\_436>:

4019bb: c7 07 48 81 e0 90 movl $0x90e08148,(%rdi)

4019c1: c3 retq

00000000004019c2 <getval\_300>:

4019c2: b8 48 89 e0 94 mov $0x94e08948,%eax

4019c7: c3 retq

00000000004019c8 <setval\_100>:

4019c8: c7 07 8b ca 20 c9 movl $0xc920ca8b,(%rdi)

4019ce: c3 retq

00000000004019cf <addval\_282>:

4019cf: 8d 87 c9 c1 38 c9 lea -0x36c73e37(%rdi),%eax

4019d5: c3 retq

00000000004019d6 <getval\_342>:

4019d6: b8 48 89 e0 c3 mov $0xc3e08948,%eax

4019db: c3 retq

00000000004019dc <setval\_333>:

4019dc: c7 07 89 d6 08 db movl $0xdb08d689,(%rdi)

4019e2: c3 retq

00000000004019e3 <getval\_434>:

4019e3: b8 09 c1 08 c9 mov $0xc908c109,%eax

4019e8: c3 retq

00000000004019e9 <getval\_190>:

4019e9: b8 89 c1 90 90 mov $0x9090c189,%eax

4019ee: c3 retq

00000000004019ef <addval\_110>:

4019ef: 8d 87 8d d6 90 90 lea -0x6f6f2973(%rdi),%eax

4019f5: c3 retq

00000000004019f6 <setval\_155>:

4019f6: c7 07 89 d6 28 c9 movl $0xc928d689,(%rdi)

4019fc: c3 retq

00000000004019fd <addval\_426>:

4019fd: 8d 87 48 89 e0 94 lea -0x6b1f76b8(%rdi),%eax

401a03: c3 retq

0000000000401a04 <addval\_257>:

401a04: 8d 87 19 89 c1 92 lea -0x6d3e76e7(%rdi),%eax

401a0a: c3 retq

0000000000401a0b <addval\_352>:

401a0b: 8d 87 c9 d6 90 c3 lea -0x3c6f2937(%rdi),%eax

401a11: c3 retq

0000000000401a12 <addval\_457>:

401a12: 8d 87 89 ca 90 c3 lea -0x3c6f3577(%rdi),%eax

401a18: c3 retq

0000000000401a19 <setval\_390>:

401a19: c7 07 89 d6 20 c0 movl $0xc020d689,(%rdi)

401a1f: c3 retq

0000000000401a20 <getval\_493>:

401a20: b8 89 c1 20 db mov $0xdb20c189,%eax

401a25: c3 retq

0000000000401a26 <getval\_224>:

401a26: b8 48 89 e0 c2 mov $0xc2e08948,%eax

401a2b: c3 retq

0000000000401a2c <setval\_193>:

401a2c: c7 07 88 c1 08 d2 movl $0xd208c188,(%rdi)

401a32: c3 retq

0000000000401a33 <setval\_292>:

401a33: c7 07 48 89 e0 c3 movl $0xc3e08948,(%rdi)

401a39: c3 retq

0000000000401a3a <getval\_328>:

401a3a: b8 89 ca 60 c9 mov $0xc960ca89,%eax

401a3f: c3 retq

0000000000401a40 <addval\_388>:

401a40: 8d 87 ea 36 8d c1 lea -0x3e72c916(%rdi),%eax

401a46: c3 retq

0000000000401a47 <addval\_272>:

401a47: 8d 87 89 ca 94 c0 lea -0x3f6b3577(%rdi),%eax

401a4d: c3 retq

0000000000401a4e <addval\_391>:

401a4e: 8d 87 89 d6 c4 c9 lea -0x363b2977(%rdi),%eax

401a54: c3 retq

0000000000401a55 <addval\_185>:

401a55: 8d 87 c9 ca c3 49 lea 0x49c3cac9(%rdi),%eax

401a5b: c3 retq

0000000000401a5c <setval\_338>:

401a5c: c7 07 48 89 e0 94 movl $0x94e08948,(%rdi)

401a62: c3 retq

0000000000401a63 <setval\_103>:

401a63: c7 07 48 88 e0 c3 movl $0xc3e08848,(%rdi)

401a69: c3 retq

0000000000401a6a <end\_farm>:

401a6a: b8 01 00 00 00 mov $0x1,%eax

401a6f: c3 retq

0000000000401a70 <save\_char>:

401a70: 8b 05 ae 46 20 00 mov 0x2046ae(%rip),%eax # 606124 <gets\_cnt>

401a76: 3d ff 03 00 00 cmp $0x3ff,%eax

401a7b: 7f 49 jg 401ac6 <save\_char+0x56>

401a7d: 8d 14 40 lea (%rax,%rax,2),%edx

401a80: 89 f9 mov %edi,%ecx

401a82: c0 e9 04 shr $0x4,%cl

401a85: 83 e1 0f and $0xf,%ecx

401a88: 0f b6 b1 20 34 40 00 movzbl 0x403420(%rcx),%esi

401a8f: 48 63 ca movslq %edx,%rcx

401a92: 40 88 b1 20 55 60 00 mov %sil,0x605520(%rcx)

401a99: 8d 4a 01 lea 0x1(%rdx),%ecx

401a9c: 83 e7 0f and $0xf,%edi

401a9f: 0f b6 b7 20 34 40 00 movzbl 0x403420(%rdi),%esi

401aa6: 48 63 c9 movslq %ecx,%rcx

401aa9: 40 88 b1 20 55 60 00 mov %sil,0x605520(%rcx)

401ab0: 83 c2 02 add $0x2,%edx

401ab3: 48 63 d2 movslq %edx,%rdx

401ab6: c6 82 20 55 60 00 20 movb $0x20,0x605520(%rdx)

401abd: 83 c0 01 add $0x1,%eax

401ac0: 89 05 5e 46 20 00 mov %eax,0x20465e(%rip) # 606124 <gets\_cnt>

401ac6: f3 c3 repz retq

0000000000401ac8 <save\_term>:

401ac8: 8b 05 56 46 20 00 mov 0x204656(%rip),%eax # 606124 <gets\_cnt>

401ace: 8d 04 40 lea (%rax,%rax,2),%eax

401ad1: 48 98 cltq

401ad3: c6 80 20 55 60 00 00 movb $0x0,0x605520(%rax)

401ada: c3 retq

0000000000401adb <check\_fail>:

401adb: 48 83 ec 08 sub $0x8,%rsp

401adf: 0f be 35 42 46 20 00 movsbl 0x204642(%rip),%esi # 606128 <target\_prefix>

401ae6: b9 20 55 60 00 mov $0x605520,%ecx

401aeb: 8b 15 07 3a 20 00 mov 0x203a07(%rip),%edx # 6054f8 <check\_level>

401af1: bf 23 31 40 00 mov $0x403123,%edi

401af6: b8 00 00 00 00 mov $0x0,%eax

401afb: e8 80 f1 ff ff callq 400c80 <printf@plt>

401b00: bf 01 00 00 00 mov $0x1,%edi

401b05: e8 e6 f2 ff ff callq 400df0 <exit@plt>

0000000000401b0a <Gets>:

401b0a: 41 54 push %r12

401b0c: 55 push %rbp

401b0d: 53 push %rbx

401b0e: 49 89 fc mov %rdi,%r12

401b11: c7 05 09 46 20 00 00 movl $0x0,0x204609(%rip) # 606124 <gets\_cnt>

401b18: 00 00 00

401b1b: 48 89 fb mov %rdi,%rbx

401b1e: eb 11 jmp 401b31 <Gets+0x27>

401b20: 48 8d 6b 01 lea 0x1(%rbx),%rbp

401b24: 88 03 mov %al,(%rbx)

401b26: 0f b6 f8 movzbl %al,%edi

401b29: e8 42 ff ff ff callq 401a70 <save\_char>

401b2e: 48 89 eb mov %rbp,%rbx

401b31: 48 8b 3d b8 39 20 00 mov 0x2039b8(%rip),%rdi # 6054f0 <infile>

401b38: e8 23 f2 ff ff callq 400d60 <\_IO\_getc@plt>

401b3d: 83 f8 ff cmp $0xffffffff,%eax

401b40: 74 05 je 401b47 <Gets+0x3d>

401b42: 83 f8 0a cmp $0xa,%eax

401b45: 75 d9 jne 401b20 <Gets+0x16>

401b47: c6 03 00 movb $0x0,(%rbx)

401b4a: b8 00 00 00 00 mov $0x0,%eax

401b4f: e8 74 ff ff ff callq 401ac8 <save\_term>

401b54: 4c 89 e0 mov %r12,%rax

401b57: 5b pop %rbx

401b58: 5d pop %rbp

401b59: 41 5c pop %r12

401b5b: c3 retq

0000000000401b5c <notify\_server>:

401b5c: 83 3d a5 39 20 00 00 cmpl $0x0,0x2039a5(%rip) # 605508 <is\_checker>

401b63: 0f 85 8e 01 00 00 jne 401cf7 <notify\_server+0x19b>

401b69: 53 push %rbx

401b6a: 48 81 ec 10 40 00 00 sub $0x4010,%rsp

401b71: 89 fb mov %edi,%ebx

401b73: 8b 05 ab 45 20 00 mov 0x2045ab(%rip),%eax # 606124 <gets\_cnt>

401b79: 83 c0 64 add $0x64,%eax

401b7c: 3d 00 20 00 00 cmp $0x2000,%eax

401b81: 7e 19 jle 401b9c <notify\_server+0x40>

401b83: bf 58 32 40 00 mov $0x403258,%edi

401b88: b8 00 00 00 00 mov $0x0,%eax

401b8d: e8 ee f0 ff ff callq 400c80 <printf@plt>

401b92: bf 01 00 00 00 mov $0x1,%edi

401b97: e8 54 f2 ff ff callq 400df0 <exit@plt>

401b9c: 44 0f be 0d 84 45 20 movsbl 0x204584(%rip),%r9d # 606128 <target\_prefix>

401ba3: 00

401ba4: 83 3d fd 38 20 00 00 cmpl $0x0,0x2038fd(%rip) # 6054a8 <notify>

401bab: 74 09 je 401bb6 <notify\_server+0x5a>

401bad: 44 8b 05 4c 39 20 00 mov 0x20394c(%rip),%r8d # 605500 <authkey>

401bb4: eb 06 jmp 401bbc <notify\_server+0x60>

401bb6: 41 b8 ff ff ff ff mov $0xffffffff,%r8d

401bbc: 85 db test %ebx,%ebx

401bbe: 74 07 je 401bc7 <notify\_server+0x6b>

401bc0: b9 39 31 40 00 mov $0x403139,%ecx

401bc5: eb 05 jmp 401bcc <notify\_server+0x70>

401bc7: b9 3e 31 40 00 mov $0x40313e,%ecx

401bcc: 48 c7 44 24 08 20 55 movq $0x605520,0x8(%rsp)

401bd3: 60 00

401bd5: 89 34 24 mov %esi,(%rsp)

401bd8: 8b 15 8a 35 20 00 mov 0x20358a(%rip),%edx # 605168 <target\_id>

401bde: be 43 31 40 00 mov $0x403143,%esi

401be3: 48 8d bc 24 10 20 00 lea 0x2010(%rsp),%rdi

401bea: 00

401beb: b8 00 00 00 00 mov $0x0,%eax

401bf0: e8 eb f1 ff ff callq 400de0 <sprintf@plt>

401bf5: 83 3d ac 38 20 00 00 cmpl $0x0,0x2038ac(%rip) # 6054a8 <notify>

401bfc: 74 78 je 401c76 <notify\_server+0x11a>

401bfe: 85 db test %ebx,%ebx

401c00: 74 68 je 401c6a <notify\_server+0x10e>

401c02: 4c 8d 4c 24 10 lea 0x10(%rsp),%r9

401c07: 41 b8 00 00 00 00 mov $0x0,%r8d

401c0d: 48 8d 8c 24 10 20 00 lea 0x2010(%rsp),%rcx

401c14: 00

401c15: 48 8b 15 54 35 20 00 mov 0x203554(%rip),%rdx # 605170 <lab>

401c1c: 48 8b 35 55 35 20 00 mov 0x203555(%rip),%rsi # 605178 <course>

401c23: 48 8b 3d 36 35 20 00 mov 0x203536(%rip),%rdi # 605160 <user\_id>

401c2a: e8 94 0f 00 00 callq 402bc3 <driver\_post>

401c2f: 85 c0 test %eax,%eax

401c31: 79 1e jns 401c51 <notify\_server+0xf5>

401c33: 48 8d 74 24 10 lea 0x10(%rsp),%rsi

401c38: bf 5f 31 40 00 mov $0x40315f,%edi

401c3d: b8 00 00 00 00 mov $0x0,%eax

401c42: e8 39 f0 ff ff callq 400c80 <printf@plt>

401c47: bf 01 00 00 00 mov $0x1,%edi

401c4c: e8 9f f1 ff ff callq 400df0 <exit@plt>

401c51: bf 88 32 40 00 mov $0x403288,%edi

401c56: e8 f5 ef ff ff callq 400c50 <puts@plt>

401c5b: bf 6b 31 40 00 mov $0x40316b,%edi

401c60: e8 eb ef ff ff callq 400c50 <puts@plt>

401c65: e9 85 00 00 00 jmpq 401cef <notify\_server+0x193>

401c6a: bf 75 31 40 00 mov $0x403175,%edi

401c6f: e8 dc ef ff ff callq 400c50 <puts@plt>

401c74: eb 79 jmp 401cef <notify\_server+0x193>

401c76: 85 db test %ebx,%ebx

401c78: 74 08 je 401c82 <notify\_server+0x126>

401c7a: be 39 31 40 00 mov $0x403139,%esi

401c7f: 90 nop

401c80: eb 05 jmp 401c87 <notify\_server+0x12b>

401c82: be 3e 31 40 00 mov $0x40313e,%esi

401c87: bf c0 32 40 00 mov $0x4032c0,%edi

401c8c: b8 00 00 00 00 mov $0x0,%eax

401c91: e8 ea ef ff ff callq 400c80 <printf@plt>

401c96: 48 8b 35 c3 34 20 00 mov 0x2034c3(%rip),%rsi # 605160 <user\_id>

401c9d: bf 7c 31 40 00 mov $0x40317c,%edi

401ca2: b8 00 00 00 00 mov $0x0,%eax

401ca7: e8 d4 ef ff ff callq 400c80 <printf@plt>

401cac: 48 8b 35 c5 34 20 00 mov 0x2034c5(%rip),%rsi # 605178 <course>

401cb3: bf 89 31 40 00 mov $0x403189,%edi

401cb8: b8 00 00 00 00 mov $0x0,%eax

401cbd: e8 be ef ff ff callq 400c80 <printf@plt>

401cc2: 48 8b 35 a7 34 20 00 mov 0x2034a7(%rip),%rsi # 605170 <lab>

401cc9: bf 95 31 40 00 mov $0x403195,%edi

401cce: b8 00 00 00 00 mov $0x0,%eax

401cd3: e8 a8 ef ff ff callq 400c80 <printf@plt>

401cd8: 48 8d b4 24 10 20 00 lea 0x2010(%rsp),%rsi

401cdf: 00

401ce0: bf 9e 31 40 00 mov $0x40319e,%edi

401ce5: b8 00 00 00 00 mov $0x0,%eax

401cea: e8 91 ef ff ff callq 400c80 <printf@plt>

401cef: 48 81 c4 10 40 00 00 add $0x4010,%rsp

401cf6: 5b pop %rbx

401cf7: f3 c3 repz retq

0000000000401cf9 <validate>:

401cf9: 53 push %rbx

401cfa: 89 fb mov %edi,%ebx

401cfc: 83 3d 05 38 20 00 00 cmpl $0x0,0x203805(%rip) # 605508 <is\_checker>

401d03: 74 60 je 401d65 <validate+0x6c>

401d05: 39 3d f1 37 20 00 cmp %edi,0x2037f1(%rip) # 6054fc <vlevel>

401d0b: 74 14 je 401d21 <validate+0x28>

401d0d: bf aa 31 40 00 mov $0x4031aa,%edi

401d12: e8 39 ef ff ff callq 400c50 <puts@plt>

401d17: b8 00 00 00 00 mov $0x0,%eax

401d1c: e8 ba fd ff ff callq 401adb <check\_fail>

401d21: 8b 35 d1 37 20 00 mov 0x2037d1(%rip),%esi # 6054f8 <check\_level>

401d27: 39 fe cmp %edi,%esi

401d29: 74 1b je 401d46 <validate+0x4d>

401d2b: 89 fa mov %edi,%edx

401d2d: bf e8 32 40 00 mov $0x4032e8,%edi

401d32: b8 00 00 00 00 mov $0x0,%eax

401d37: e8 44 ef ff ff callq 400c80 <printf@plt>

401d3c: b8 00 00 00 00 mov $0x0,%eax

401d41: e8 95 fd ff ff callq 401adb <check\_fail>

401d46: 0f be 35 db 43 20 00 movsbl 0x2043db(%rip),%esi # 606128 <target\_prefix>

401d4d: b9 20 55 60 00 mov $0x605520,%ecx

401d52: 89 fa mov %edi,%edx

401d54: bf c8 31 40 00 mov $0x4031c8,%edi

401d59: b8 00 00 00 00 mov $0x0,%eax

401d5e: e8 1d ef ff ff callq 400c80 <printf@plt>

401d63: eb 44 jmp 401da9 <validate+0xb0>

401d65: 39 3d 91 37 20 00 cmp %edi,0x203791(%rip) # 6054fc <vlevel>

401d6b: 74 18 je 401d85 <validate+0x8c>

401d6d: bf aa 31 40 00 mov $0x4031aa,%edi

401d72: e8 d9 ee ff ff callq 400c50 <puts@plt>

401d77: 89 de mov %ebx,%esi

401d79: bf 00 00 00 00 mov $0x0,%edi

401d7e: e8 d9 fd ff ff callq 401b5c <notify\_server>

401d83: eb 24 jmp 401da9 <validate+0xb0>

401d85: 0f be 15 9c 43 20 00 movsbl 0x20439c(%rip),%edx # 606128 <target\_prefix>

401d8c: 89 fe mov %edi,%esi

401d8e: bf 10 33 40 00 mov $0x403310,%edi

401d93: b8 00 00 00 00 mov $0x0,%eax

401d98: e8 e3 ee ff ff callq 400c80 <printf@plt>

401d9d: 89 de mov %ebx,%esi

401d9f: bf 01 00 00 00 mov $0x1,%edi

401da4: e8 b3 fd ff ff callq 401b5c <notify\_server>

401da9: 5b pop %rbx

401daa: c3 retq

0000000000401dab <fail>:

401dab: 48 83 ec 08 sub $0x8,%rsp

401daf: 83 3d 52 37 20 00 00 cmpl $0x0,0x203752(%rip) # 605508 <is\_checker>

401db6: 74 0a je 401dc2 <fail+0x17>

401db8: b8 00 00 00 00 mov $0x0,%eax

401dbd: e8 19 fd ff ff callq 401adb <check\_fail>

401dc2: 89 fe mov %edi,%esi

401dc4: bf 00 00 00 00 mov $0x0,%edi

401dc9: e8 8e fd ff ff callq 401b5c <notify\_server>

401dce: 48 83 c4 08 add $0x8,%rsp

401dd2: c3 retq

0000000000401dd3 <bushandler>:

401dd3: 48 83 ec 08 sub $0x8,%rsp

401dd7: 83 3d 2a 37 20 00 00 cmpl $0x0,0x20372a(%rip) # 605508 <is\_checker>

401dde: 74 14 je 401df4 <bushandler+0x21>

401de0: bf dd 31 40 00 mov $0x4031dd,%edi

401de5: e8 66 ee ff ff callq 400c50 <puts@plt>

401dea: b8 00 00 00 00 mov $0x0,%eax

401def: e8 e7 fc ff ff callq 401adb <check\_fail>

401df4: bf 48 33 40 00 mov $0x403348,%edi

401df9: e8 52 ee ff ff callq 400c50 <puts@plt>

401dfe: bf e7 31 40 00 mov $0x4031e7,%edi

401e03: e8 48 ee ff ff callq 400c50 <puts@plt>

401e08: be 00 00 00 00 mov $0x0,%esi

401e0d: bf 00 00 00 00 mov $0x0,%edi

401e12: e8 45 fd ff ff callq 401b5c <notify\_server>

401e17: bf 01 00 00 00 mov $0x1,%edi

401e1c: e8 cf ef ff ff callq 400df0 <exit@plt>

0000000000401e21 <seghandler>:

401e21: 48 83 ec 08 sub $0x8,%rsp

401e25: 83 3d dc 36 20 00 00 cmpl $0x0,0x2036dc(%rip) # 605508 <is\_checker>

401e2c: 74 14 je 401e42 <seghandler+0x21>

401e2e: bf fd 31 40 00 mov $0x4031fd,%edi

401e33: e8 18 ee ff ff callq 400c50 <puts@plt>

401e38: b8 00 00 00 00 mov $0x0,%eax

401e3d: e8 99 fc ff ff callq 401adb <check\_fail>

401e42: bf 68 33 40 00 mov $0x403368,%edi

401e47: e8 04 ee ff ff callq 400c50 <puts@plt>

401e4c: bf e7 31 40 00 mov $0x4031e7,%edi

401e51: e8 fa ed ff ff callq 400c50 <puts@plt>

401e56: be 00 00 00 00 mov $0x0,%esi

401e5b: bf 00 00 00 00 mov $0x0,%edi

401e60: e8 f7 fc ff ff callq 401b5c <notify\_server>

401e65: bf 01 00 00 00 mov $0x1,%edi

401e6a: e8 81 ef ff ff callq 400df0 <exit@plt>

0000000000401e6f <illegalhandler>:

401e6f: 48 83 ec 08 sub $0x8,%rsp

401e73: 83 3d 8e 36 20 00 00 cmpl $0x0,0x20368e(%rip) # 605508 <is\_checker>

401e7a: 74 14 je 401e90 <illegalhandler+0x21>

401e7c: bf 10 32 40 00 mov $0x403210,%edi

401e81: e8 ca ed ff ff callq 400c50 <puts@plt>

401e86: b8 00 00 00 00 mov $0x0,%eax

401e8b: e8 4b fc ff ff callq 401adb <check\_fail>

401e90: bf 90 33 40 00 mov $0x403390,%edi

401e95: e8 b6 ed ff ff callq 400c50 <puts@plt>

401e9a: bf e7 31 40 00 mov $0x4031e7,%edi

401e9f: e8 ac ed ff ff callq 400c50 <puts@plt>

401ea4: be 00 00 00 00 mov $0x0,%esi

401ea9: bf 00 00 00 00 mov $0x0,%edi

401eae: e8 a9 fc ff ff callq 401b5c <notify\_server>

401eb3: bf 01 00 00 00 mov $0x1,%edi

401eb8: e8 33 ef ff ff callq 400df0 <exit@plt>

0000000000401ebd <sigalrmhandler>:

401ebd: 48 83 ec 08 sub $0x8,%rsp

401ec1: 83 3d 40 36 20 00 00 cmpl $0x0,0x203640(%rip) # 605508 <is\_checker>

401ec8: 74 14 je 401ede <sigalrmhandler+0x21>

401eca: bf 24 32 40 00 mov $0x403224,%edi

401ecf: e8 7c ed ff ff callq 400c50 <puts@plt>

401ed4: b8 00 00 00 00 mov $0x0,%eax

401ed9: e8 fd fb ff ff callq 401adb <check\_fail>

401ede: be 05 00 00 00 mov $0x5,%esi

401ee3: bf c0 33 40 00 mov $0x4033c0,%edi

401ee8: b8 00 00 00 00 mov $0x0,%eax

401eed: e8 8e ed ff ff callq 400c80 <printf@plt>

401ef2: be 00 00 00 00 mov $0x0,%esi

401ef7: bf 00 00 00 00 mov $0x0,%edi

401efc: e8 5b fc ff ff callq 401b5c <notify\_server>

401f01: bf 01 00 00 00 mov $0x1,%edi

401f06: e8 e5 ee ff ff callq 400df0 <exit@plt>

0000000000401f0b <launch>:

401f0b: 55 push %rbp

401f0c: 48 89 e5 mov %rsp,%rbp

401f0f: 48 89 fa mov %rdi,%rdx

401f12: 48 8d 47 1e lea 0x1e(%rdi),%rax

401f16: 48 83 e0 f0 and $0xfffffffffffffff0,%rax

401f1a: 48 29 c4 sub %rax,%rsp

401f1d: 48 8d 7c 24 0f lea 0xf(%rsp),%rdi

401f22: 48 83 e7 f0 and $0xfffffffffffffff0,%rdi

401f26: be f4 00 00 00 mov $0xf4,%esi

401f2b: e8 60 ed ff ff callq 400c90 <memset@plt>

401f30: 48 8b 05 89 35 20 00 mov 0x203589(%rip),%rax # 6054c0 <stdin@@GLIBC\_2.2.5>

401f37: 48 39 05 b2 35 20 00 cmp %rax,0x2035b2(%rip) # 6054f0 <infile>

401f3e: 75 0f jne 401f4f <launch+0x44>

401f40: bf 2c 32 40 00 mov $0x40322c,%edi

401f45: b8 00 00 00 00 mov $0x0,%eax

401f4a: e8 31 ed ff ff callq 400c80 <printf@plt>

401f4f: c7 05 a3 35 20 00 00 movl $0x0,0x2035a3(%rip) # 6054fc <vlevel>

401f56: 00 00 00

401f59: b8 00 00 00 00 mov $0x0,%eax

401f5e: e8 c5 f9 ff ff callq 401928 <test>

401f63: 83 3d 9e 35 20 00 00 cmpl $0x0,0x20359e(%rip) # 605508 <is\_checker>

401f6a: 74 14 je 401f80 <launch+0x75>

401f6c: bf 39 32 40 00 mov $0x403239,%edi

401f71: e8 da ec ff ff callq 400c50 <puts@plt>

401f76: b8 00 00 00 00 mov $0x0,%eax

401f7b: e8 5b fb ff ff callq 401adb <check\_fail>

401f80: bf 44 32 40 00 mov $0x403244,%edi

401f85: e8 c6 ec ff ff callq 400c50 <puts@plt>

401f8a: c9 leaveq

401f8b: c3 retq

0000000000401f8c <stable\_launch>:

401f8c: 53 push %rbx

401f8d: 48 89 3d 54 35 20 00 mov %rdi,0x203554(%rip) # 6054e8 <global\_offset>

401f94: 41 b9 00 00 00 00 mov $0x0,%r9d

401f9a: 41 b8 00 00 00 00 mov $0x0,%r8d

401fa0: b9 32 01 00 00 mov $0x132,%ecx

401fa5: ba 07 00 00 00 mov $0x7,%edx

401faa: be 00 00 10 00 mov $0x100000,%esi

401faf: bf 00 60 58 55 mov $0x55586000,%edi

401fb4: e8 b7 ec ff ff callq 400c70 <mmap@plt>

401fb9: 48 89 c3 mov %rax,%rbx

401fbc: 48 3d 00 60 58 55 cmp $0x55586000,%rax

401fc2: 74 32 je 401ff6 <stable\_launch+0x6a>

401fc4: be 00 00 10 00 mov $0x100000,%esi

401fc9: 48 89 c7 mov %rax,%rdi

401fcc: e8 af ed ff ff callq 400d80 <munmap@plt>

401fd1: ba 00 60 58 55 mov $0x55586000,%edx

401fd6: be f8 33 40 00 mov $0x4033f8,%esi

401fdb: 48 8b 3d ee 34 20 00 mov 0x2034ee(%rip),%rdi # 6054d0 <stderr@@GLIBC\_2.2.5>

401fe2: b8 00 00 00 00 mov $0x0,%eax

401fe7: e8 14 ed ff ff callq 400d00 <fprintf@plt>

401fec: bf 01 00 00 00 mov $0x1,%edi

401ff1: e8 fa ed ff ff callq 400df0 <exit@plt>

401ff6: 48 8d 90 f8 ff 0f 00 lea 0xffff8(%rax),%rdx

401ffd: 48 89 15 2c 41 20 00 mov %rdx,0x20412c(%rip) # 606130 <stack\_top>

402004: 48 89 e0 mov %rsp,%rax

402007: 48 89 d4 mov %rdx,%rsp

40200a: 48 89 c2 mov %rax,%rdx

40200d: 48 89 15 cc 34 20 00 mov %rdx,0x2034cc(%rip) # 6054e0 <global\_save\_stack>

402014: 48 8b 3d cd 34 20 00 mov 0x2034cd(%rip),%rdi # 6054e8 <global\_offset>

40201b: e8 eb fe ff ff callq 401f0b <launch>

402020: 48 8b 05 b9 34 20 00 mov 0x2034b9(%rip),%rax # 6054e0 <global\_save\_stack>

402027: 48 89 c4 mov %rax,%rsp

40202a: be 00 00 10 00 mov $0x100000,%esi

40202f: 48 89 df mov %rbx,%rdi

402032: e8 49 ed ff ff callq 400d80 <munmap@plt>

402037: 5b pop %rbx

402038: c3 retq

402039: 0f 1f 80 00 00 00 00 nopl 0x0(%rax)

0000000000402040 <rio\_readinitb>:

402040: 89 37 mov %esi,(%rdi)

402042: c7 47 04 00 00 00 00 movl $0x0,0x4(%rdi)

402049: 48 8d 47 10 lea 0x10(%rdi),%rax

40204d: 48 89 47 08 mov %rax,0x8(%rdi)

402051: c3 retq

0000000000402052 <sigalrm\_handler>:

402052: 48 83 ec 08 sub $0x8,%rsp

402056: ba 00 00 00 00 mov $0x0,%edx

40205b: be 30 34 40 00 mov $0x403430,%esi

402060: 48 8b 3d 69 34 20 00 mov 0x203469(%rip),%rdi # 6054d0 <stderr@@GLIBC\_2.2.5>

402067: b8 00 00 00 00 mov $0x0,%eax

40206c: e8 8f ec ff ff callq 400d00 <fprintf@plt>

402071: bf 01 00 00 00 mov $0x1,%edi

402076: e8 75 ed ff ff callq 400df0 <exit@plt>

000000000040207b <urlencode>:

40207b: 41 54 push %r12

40207d: 55 push %rbp

40207e: 53 push %rbx

40207f: 48 83 ec 10 sub $0x10,%rsp

402083: 48 89 fb mov %rdi,%rbx

402086: 48 89 f5 mov %rsi,%rbp

402089: b8 00 00 00 00 mov $0x0,%eax

40208e: 48 c7 c1 ff ff ff ff mov $0xffffffffffffffff,%rcx

402095: f2 ae repnz scas %es:(%rdi),%al

402097: 48 f7 d1 not %rcx

40209a: 8d 41 ff lea -0x1(%rcx),%eax

40209d: e9 93 00 00 00 jmpq 402135 <urlencode+0xba>

4020a2: 0f b6 13 movzbl (%rbx),%edx

4020a5: 80 fa 2a cmp $0x2a,%dl

4020a8: 0f 94 c1 sete %cl

4020ab: 80 fa 2d cmp $0x2d,%dl

4020ae: 0f 94 c0 sete %al

4020b1: 08 c1 or %al,%cl

4020b3: 75 1f jne 4020d4 <urlencode+0x59>

4020b5: 80 fa 2e cmp $0x2e,%dl

4020b8: 74 1a je 4020d4 <urlencode+0x59>

4020ba: 80 fa 5f cmp $0x5f,%dl

4020bd: 74 15 je 4020d4 <urlencode+0x59>

4020bf: 8d 42 d0 lea -0x30(%rdx),%eax

4020c2: 3c 09 cmp $0x9,%al

4020c4: 76 0e jbe 4020d4 <urlencode+0x59>

4020c6: 8d 42 bf lea -0x41(%rdx),%eax

4020c9: 3c 19 cmp $0x19,%al

4020cb: 76 07 jbe 4020d4 <urlencode+0x59>

4020cd: 8d 42 9f lea -0x61(%rdx),%eax

4020d0: 3c 19 cmp $0x19,%al

4020d2: 77 09 ja 4020dd <urlencode+0x62>

4020d4: 88 55 00 mov %dl,0x0(%rbp)

4020d7: 48 8d 6d 01 lea 0x1(%rbp),%rbp

4020db: eb 51 jmp 40212e <urlencode+0xb3>

4020dd: 80 fa 20 cmp $0x20,%dl

4020e0: 75 0a jne 4020ec <urlencode+0x71>

4020e2: c6 45 00 2b movb $0x2b,0x0(%rbp)

4020e6: 48 8d 6d 01 lea 0x1(%rbp),%rbp

4020ea: eb 42 jmp 40212e <urlencode+0xb3>

4020ec: 8d 42 e0 lea -0x20(%rdx),%eax

4020ef: 3c 5f cmp $0x5f,%al

4020f1: 0f 96 c1 setbe %cl

4020f4: 80 fa 09 cmp $0x9,%dl

4020f7: 0f 94 c0 sete %al

4020fa: 08 c1 or %al,%cl

4020fc: 74 45 je 402143 <urlencode+0xc8>

4020fe: 0f b6 d2 movzbl %dl,%edx

402101: be c8 34 40 00 mov $0x4034c8,%esi

402106: 48 89 e7 mov %rsp,%rdi

402109: b8 00 00 00 00 mov $0x0,%eax

40210e: e8 cd ec ff ff callq 400de0 <sprintf@plt>

402113: 0f b6 04 24 movzbl (%rsp),%eax

402117: 88 45 00 mov %al,0x0(%rbp)

40211a: 0f b6 44 24 01 movzbl 0x1(%rsp),%eax

40211f: 88 45 01 mov %al,0x1(%rbp)

402122: 0f b6 44 24 02 movzbl 0x2(%rsp),%eax

402127: 88 45 02 mov %al,0x2(%rbp)

40212a: 48 8d 6d 03 lea 0x3(%rbp),%rbp

40212e: 48 83 c3 01 add $0x1,%rbx

402132: 44 89 e0 mov %r12d,%eax

402135: 44 8d 60 ff lea -0x1(%rax),%r12d

402139: 85 c0 test %eax,%eax

40213b: 0f 85 61 ff ff ff jne 4020a2 <urlencode+0x27>

402141: eb 05 jmp 402148 <urlencode+0xcd>

402143: b8 ff ff ff ff mov $0xffffffff,%eax

402148: 48 83 c4 10 add $0x10,%rsp

40214c: 5b pop %rbx

40214d: 5d pop %rbp

40214e: 41 5c pop %r12

402150: c3 retq

0000000000402151 <rio\_writen>:

402151: 41 55 push %r13

402153: 41 54 push %r12

402155: 55 push %rbp

402156: 53 push %rbx

402157: 48 83 ec 08 sub $0x8,%rsp

40215b: 41 89 fc mov %edi,%r12d

40215e: 48 89 f5 mov %rsi,%rbp

402161: 49 89 d5 mov %rdx,%r13

402164: 48 89 d3 mov %rdx,%rbx

402167: eb 28 jmp 402191 <rio\_writen+0x40>

402169: 48 89 da mov %rbx,%rdx

40216c: 48 89 ee mov %rbp,%rsi

40216f: 44 89 e7 mov %r12d,%edi

402172: e8 e9 ea ff ff callq 400c60 <write@plt>

402177: 48 85 c0 test %rax,%rax

40217a: 7f 0f jg 40218b <rio\_writen+0x3a>

40217c: e8 8f ea ff ff callq 400c10 <\_\_errno\_location@plt>

402181: 83 38 04 cmpl $0x4,(%rax)

402184: 75 15 jne 40219b <rio\_writen+0x4a>

402186: b8 00 00 00 00 mov $0x0,%eax

40218b: 48 29 c3 sub %rax,%rbx

40218e: 48 01 c5 add %rax,%rbp

402191: 48 85 db test %rbx,%rbx

402194: 75 d3 jne 402169 <rio\_writen+0x18>

402196: 4c 89 e8 mov %r13,%rax

402199: eb 07 jmp 4021a2 <rio\_writen+0x51>

40219b: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

4021a2: 48 83 c4 08 add $0x8,%rsp

4021a6: 5b pop %rbx

4021a7: 5d pop %rbp

4021a8: 41 5c pop %r12

4021aa: 41 5d pop %r13

4021ac: c3 retq

00000000004021ad <rio\_read>:

4021ad: 41 56 push %r14

4021af: 41 55 push %r13

4021b1: 41 54 push %r12

4021b3: 55 push %rbp

4021b4: 53 push %rbx

4021b5: 48 89 fb mov %rdi,%rbx

4021b8: 49 89 f6 mov %rsi,%r14

4021bb: 49 89 d5 mov %rdx,%r13

4021be: 4c 8d 67 10 lea 0x10(%rdi),%r12

4021c2: eb 2a jmp 4021ee <rio\_read+0x41>

4021c4: ba 00 20 00 00 mov $0x2000,%edx

4021c9: 4c 89 e6 mov %r12,%rsi

4021cc: 8b 3b mov (%rbx),%edi

4021ce: e8 ed ea ff ff callq 400cc0 <read@plt>

4021d3: 89 43 04 mov %eax,0x4(%rbx)

4021d6: 85 c0 test %eax,%eax

4021d8: 79 0c jns 4021e6 <rio\_read+0x39>

4021da: e8 31 ea ff ff callq 400c10 <\_\_errno\_location@plt>

4021df: 83 38 04 cmpl $0x4,(%rax)

4021e2: 74 0a je 4021ee <rio\_read+0x41>

4021e4: eb 37 jmp 40221d <rio\_read+0x70>

4021e6: 85 c0 test %eax,%eax

4021e8: 74 3c je 402226 <rio\_read+0x79>

4021ea: 4c 89 63 08 mov %r12,0x8(%rbx)

4021ee: 8b 6b 04 mov 0x4(%rbx),%ebp

4021f1: 85 ed test %ebp,%ebp

4021f3: 7e cf jle 4021c4 <rio\_read+0x17>

4021f5: 89 e8 mov %ebp,%eax

4021f7: 4c 39 e8 cmp %r13,%rax

4021fa: 72 03 jb 4021ff <rio\_read+0x52>

4021fc: 44 89 ed mov %r13d,%ebp

4021ff: 4c 63 e5 movslq %ebp,%r12

402202: 48 8b 73 08 mov 0x8(%rbx),%rsi

402206: 4c 89 e2 mov %r12,%rdx

402209: 4c 89 f7 mov %r14,%rdi

40220c: e8 1f eb ff ff callq 400d30 <memcpy@plt>

402211: 4c 01 63 08 add %r12,0x8(%rbx)

402215: 29 6b 04 sub %ebp,0x4(%rbx)

402218: 4c 89 e0 mov %r12,%rax

40221b: eb 0e jmp 40222b <rio\_read+0x7e>

40221d: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

402224: eb 05 jmp 40222b <rio\_read+0x7e>

402226: b8 00 00 00 00 mov $0x0,%eax

40222b: 5b pop %rbx

40222c: 5d pop %rbp

40222d: 41 5c pop %r12

40222f: 41 5d pop %r13

402231: 41 5e pop %r14

402233: c3 retq

0000000000402234 <rio\_readlineb>:

402234: 41 55 push %r13

402236: 41 54 push %r12

402238: 55 push %rbp

402239: 53 push %rbx

40223a: 48 83 ec 18 sub $0x18,%rsp

40223e: 49 89 fd mov %rdi,%r13

402241: 48 89 f5 mov %rsi,%rbp

402244: 49 89 d4 mov %rdx,%r12

402247: bb 01 00 00 00 mov $0x1,%ebx

40224c: eb 3d jmp 40228b <rio\_readlineb+0x57>

40224e: ba 01 00 00 00 mov $0x1,%edx

402253: 48 8d 74 24 0f lea 0xf(%rsp),%rsi

402258: 4c 89 ef mov %r13,%rdi

40225b: e8 4d ff ff ff callq 4021ad <rio\_read>

402260: 83 f8 01 cmp $0x1,%eax

402263: 75 12 jne 402277 <rio\_readlineb+0x43>

402265: 48 8d 55 01 lea 0x1(%rbp),%rdx

402269: 0f b6 44 24 0f movzbl 0xf(%rsp),%eax

40226e: 88 45 00 mov %al,0x0(%rbp)

402271: 3c 0a cmp $0xa,%al

402273: 75 0f jne 402284 <rio\_readlineb+0x50>

402275: eb 1b jmp 402292 <rio\_readlineb+0x5e>

402277: 85 c0 test %eax,%eax

402279: 75 23 jne 40229e <rio\_readlineb+0x6a>

40227b: 48 83 fb 01 cmp $0x1,%rbx

40227f: 90 nop

402280: 75 13 jne 402295 <rio\_readlineb+0x61>

402282: eb 23 jmp 4022a7 <rio\_readlineb+0x73>

402284: 48 83 c3 01 add $0x1,%rbx

402288: 48 89 d5 mov %rdx,%rbp

40228b: 4c 39 e3 cmp %r12,%rbx

40228e: 72 be jb 40224e <rio\_readlineb+0x1a>

402290: eb 03 jmp 402295 <rio\_readlineb+0x61>

402292: 48 89 d5 mov %rdx,%rbp

402295: c6 45 00 00 movb $0x0,0x0(%rbp)

402299: 48 89 d8 mov %rbx,%rax

40229c: eb 0e jmp 4022ac <rio\_readlineb+0x78>

40229e: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax

4022a5: eb 05 jmp 4022ac <rio\_readlineb+0x78>

4022a7: b8 00 00 00 00 mov $0x0,%eax

4022ac: 48 83 c4 18 add $0x18,%rsp

4022b0: 5b pop %rbx

4022b1: 5d pop %rbp

4022b2: 41 5c pop %r12

4022b4: 41 5d pop %r13

4022b6: c3 retq

00000000004022b7 <submitr>:

4022b7: 41 57 push %r15

4022b9: 41 56 push %r14

4022bb: 41 55 push %r13

4022bd: 41 54 push %r12

4022bf: 55 push %rbp

4022c0: 53 push %rbx

4022c1: 48 81 ec 48 a0 00 00 sub $0xa048,%rsp

4022c8: 49 89 fc mov %rdi,%r12

4022cb: 89 74 24 04 mov %esi,0x4(%rsp)

4022cf: 49 89 d7 mov %rdx,%r15

4022d2: 49 89 ce mov %rcx,%r14

4022d5: 4c 89 44 24 08 mov %r8,0x8(%rsp)

4022da: 4d 89 cd mov %r9,%r13

4022dd: 48 8b 9c 24 80 a0 00 mov 0xa080(%rsp),%rbx

4022e4: 00

4022e5: c7 84 24 1c 20 00 00 movl $0x0,0x201c(%rsp)

4022ec: 00 00 00 00

4022f0: ba 00 00 00 00 mov $0x0,%edx

4022f5: be 01 00 00 00 mov $0x1,%esi

4022fa: bf 02 00 00 00 mov $0x2,%edi

4022ff: e8 0c eb ff ff callq 400e10 <socket@plt>

402304: 89 c5 mov %eax,%ebp

402306: 85 c0 test %eax,%eax

402308: 79 4e jns 402358 <submitr+0xa1>

40230a: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

402311: 3a 20 43

402314: 48 89 03 mov %rax,(%rbx)

402317: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40231e: 20 75 6e

402321: 48 89 43 08 mov %rax,0x8(%rbx)

402325: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

40232c: 74 6f 20

40232f: 48 89 43 10 mov %rax,0x10(%rbx)

402333: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax

40233a: 65 20 73

40233d: 48 89 43 18 mov %rax,0x18(%rbx)

402341: c7 43 20 6f 63 6b 65 movl $0x656b636f,0x20(%rbx)

402348: 66 c7 43 24 74 00 movw $0x74,0x24(%rbx)

40234e: b8 ff ff ff ff mov $0xffffffff,%eax

402353: e9 68 06 00 00 jmpq 4029c0 <submitr+0x709>

402358: 4c 89 e7 mov %r12,%rdi

40235b: e8 90 e9 ff ff callq 400cf0 <gethostbyname@plt>

402360: 48 85 c0 test %rax,%rax

402363: 75 67 jne 4023cc <submitr+0x115>

402365: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax

40236c: 3a 20 44

40236f: 48 89 03 mov %rax,(%rbx)

402372: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax

402379: 20 75 6e

40237c: 48 89 43 08 mov %rax,0x8(%rbx)

402380: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402387: 74 6f 20

40238a: 48 89 43 10 mov %rax,0x10(%rbx)

40238e: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax

402395: 76 65 20

402398: 48 89 43 18 mov %rax,0x18(%rbx)

40239c: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax

4023a3: 72 20 61

4023a6: 48 89 43 20 mov %rax,0x20(%rbx)

4023aa: c7 43 28 64 64 72 65 movl $0x65726464,0x28(%rbx)

4023b1: 66 c7 43 2c 73 73 movw $0x7373,0x2c(%rbx)

4023b7: c6 43 2e 00 movb $0x0,0x2e(%rbx)

4023bb: 89 ef mov %ebp,%edi

4023bd: e8 ee e8 ff ff callq 400cb0 <close@plt>

4023c2: b8 ff ff ff ff mov $0xffffffff,%eax

4023c7: e9 f4 05 00 00 jmpq 4029c0 <submitr+0x709>

4023cc: 48 c7 84 24 30 a0 00 movq $0x0,0xa030(%rsp)

4023d3: 00 00 00 00 00

4023d8: 48 c7 84 24 38 a0 00 movq $0x0,0xa038(%rsp)

4023df: 00 00 00 00 00

4023e4: 66 c7 84 24 30 a0 00 movw $0x2,0xa030(%rsp)

4023eb: 00 02 00

4023ee: 48 8b 48 18 mov 0x18(%rax),%rcx

4023f2: 48 63 50 14 movslq 0x14(%rax),%rdx

4023f6: 48 8d b4 24 34 a0 00 lea 0xa034(%rsp),%rsi

4023fd: 00

4023fe: 48 8b 39 mov (%rcx),%rdi

402401: e8 8a e9 ff ff callq 400d90 <bcopy@plt>

402406: 0f b7 44 24 04 movzwl 0x4(%rsp),%eax

40240b: 66 c1 c8 08 ror $0x8,%ax

40240f: 66 89 84 24 32 a0 00 mov %ax,0xa032(%rsp)

402416: 00

402417: ba 10 00 00 00 mov $0x10,%edx

40241c: 48 8d b4 24 30 a0 00 lea 0xa030(%rsp),%rsi

402423: 00

402424: 89 ef mov %ebp,%edi

402426: e8 d5 e9 ff ff callq 400e00 <connect@plt>

40242b: 85 c0 test %eax,%eax

40242d: 79 59 jns 402488 <submitr+0x1d1>

40242f: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax

402436: 3a 20 55

402439: 48 89 03 mov %rax,(%rbx)

40243c: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax

402443: 20 74 6f

402446: 48 89 43 08 mov %rax,0x8(%rbx)

40244a: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6e6f6320,%rax

402451: 65 63 74

402454: 48 89 43 10 mov %rax,0x10(%rbx)

402458: 48 b8 20 74 6f 20 74 movabs $0x20656874206f7420,%rax

40245f: 68 65 20

402462: 48 89 43 18 mov %rax,0x18(%rbx)

402466: c7 43 20 73 65 72 76 movl $0x76726573,0x20(%rbx)

40246d: 66 c7 43 24 65 72 movw $0x7265,0x24(%rbx)

402473: c6 43 26 00 movb $0x0,0x26(%rbx)

402477: 89 ef mov %ebp,%edi

402479: e8 32 e8 ff ff callq 400cb0 <close@plt>

40247e: b8 ff ff ff ff mov $0xffffffff,%eax

402483: e9 38 05 00 00 jmpq 4029c0 <submitr+0x709>

402488: 48 c7 c2 ff ff ff ff mov $0xffffffffffffffff,%rdx

40248f: 4c 89 ef mov %r13,%rdi

402492: b8 00 00 00 00 mov $0x0,%eax

402497: 48 89 d1 mov %rdx,%rcx

40249a: f2 ae repnz scas %es:(%rdi),%al

40249c: 48 f7 d1 not %rcx

40249f: 48 89 ce mov %rcx,%rsi

4024a2: 4c 89 ff mov %r15,%rdi

4024a5: 48 89 d1 mov %rdx,%rcx

4024a8: f2 ae repnz scas %es:(%rdi),%al

4024aa: 48 f7 d1 not %rcx

4024ad: 49 89 c8 mov %rcx,%r8

4024b0: 4c 89 f7 mov %r14,%rdi

4024b3: 48 89 d1 mov %rdx,%rcx

4024b6: f2 ae repnz scas %es:(%rdi),%al

4024b8: 49 29 c8 sub %rcx,%r8

4024bb: 48 8b 7c 24 08 mov 0x8(%rsp),%rdi

4024c0: 48 89 d1 mov %rdx,%rcx

4024c3: f2 ae repnz scas %es:(%rdi),%al

4024c5: 49 29 c8 sub %rcx,%r8

4024c8: 48 8d 44 76 fd lea -0x3(%rsi,%rsi,2),%rax

4024cd: 49 8d 44 00 7b lea 0x7b(%r8,%rax,1),%rax

4024d2: 48 3d 00 20 00 00 cmp $0x2000,%rax

4024d8: 76 72 jbe 40254c <submitr+0x295>

4024da: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax

4024e1: 3a 20 52

4024e4: 48 89 03 mov %rax,(%rbx)

4024e7: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax

4024ee: 20 73 74

4024f1: 48 89 43 08 mov %rax,0x8(%rbx)

4024f5: 48 b8 72 69 6e 67 20 movabs $0x6f6f7420676e6972,%rax

4024fc: 74 6f 6f

4024ff: 48 89 43 10 mov %rax,0x10(%rbx)

402503: 48 b8 20 6c 61 72 67 movabs $0x202e656772616c20,%rax

40250a: 65 2e 20

40250d: 48 89 43 18 mov %rax,0x18(%rbx)

402511: 48 b8 49 6e 63 72 65 movabs $0x6573616572636e49,%rax

402518: 61 73 65

40251b: 48 89 43 20 mov %rax,0x20(%rbx)

40251f: 48 b8 20 53 55 42 4d movabs $0x5254494d42555320,%rax

402526: 49 54 52

402529: 48 89 43 28 mov %rax,0x28(%rbx)

40252d: 48 b8 5f 4d 41 58 42 movabs $0x46554258414d5f,%rax

402534: 55 46 00

402537: 48 89 43 30 mov %rax,0x30(%rbx)

40253b: 89 ef mov %ebp,%edi

40253d: e8 6e e7 ff ff callq 400cb0 <close@plt>

402542: b8 ff ff ff ff mov $0xffffffff,%eax

402547: e9 74 04 00 00 jmpq 4029c0 <submitr+0x709>

40254c: 48 8d b4 24 20 40 00 lea 0x4020(%rsp),%rsi

402553: 00

402554: b9 00 04 00 00 mov $0x400,%ecx

402559: b8 00 00 00 00 mov $0x0,%eax

40255e: 48 89 f7 mov %rsi,%rdi

402561: f3 48 ab rep stos %rax,%es:(%rdi)

402564: 4c 89 ef mov %r13,%rdi

402567: e8 0f fb ff ff callq 40207b <urlencode>

40256c: 85 c0 test %eax,%eax

40256e: 0f 89 8a 00 00 00 jns 4025fe <submitr+0x347>

402574: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax

40257b: 3a 20 52

40257e: 48 89 03 mov %rax,(%rbx)

402581: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax

402588: 20 73 74

40258b: 48 89 43 08 mov %rax,0x8(%rbx)

40258f: 48 b8 72 69 6e 67 20 movabs $0x6e6f6320676e6972,%rax

402596: 63 6f 6e

402599: 48 89 43 10 mov %rax,0x10(%rbx)

40259d: 48 b8 74 61 69 6e 73 movabs $0x6e6120736e696174,%rax

4025a4: 20 61 6e

4025a7: 48 89 43 18 mov %rax,0x18(%rbx)

4025ab: 48 b8 20 69 6c 6c 65 movabs $0x6c6167656c6c6920,%rax

4025b2: 67 61 6c

4025b5: 48 89 43 20 mov %rax,0x20(%rbx)

4025b9: 48 b8 20 6f 72 20 75 movabs $0x72706e7520726f20,%rax

4025c0: 6e 70 72

4025c3: 48 89 43 28 mov %rax,0x28(%rbx)

4025c7: 48 b8 69 6e 74 61 62 movabs $0x20656c6261746e69,%rax

4025ce: 6c 65 20

4025d1: 48 89 43 30 mov %rax,0x30(%rbx)

4025d5: 48 b8 63 68 61 72 61 movabs $0x6574636172616863,%rax

4025dc: 63 74 65

4025df: 48 89 43 38 mov %rax,0x38(%rbx)

4025e3: 66 c7 43 40 72 2e movw $0x2e72,0x40(%rbx)

4025e9: c6 43 42 00 movb $0x0,0x42(%rbx)

4025ed: 89 ef mov %ebp,%edi

4025ef: e8 bc e6 ff ff callq 400cb0 <close@plt>

4025f4: b8 ff ff ff ff mov $0xffffffff,%eax

4025f9: e9 c2 03 00 00 jmpq 4029c0 <submitr+0x709>

4025fe: 4d 89 e1 mov %r12,%r9

402601: 4c 8d 84 24 20 40 00 lea 0x4020(%rsp),%r8

402608: 00

402609: 4c 89 f9 mov %r15,%rcx

40260c: 4c 89 f2 mov %r14,%rdx

40260f: be 58 34 40 00 mov $0x403458,%esi

402614: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

40261b: 00

40261c: b8 00 00 00 00 mov $0x0,%eax

402621: e8 ba e7 ff ff callq 400de0 <sprintf@plt>

402626: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

40262d: 00

40262e: b8 00 00 00 00 mov $0x0,%eax

402633: 48 c7 c1 ff ff ff ff mov $0xffffffffffffffff,%rcx

40263a: f2 ae repnz scas %es:(%rdi),%al

40263c: 48 f7 d1 not %rcx

40263f: 48 8d 51 ff lea -0x1(%rcx),%rdx

402643: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40264a: 00

40264b: 89 ef mov %ebp,%edi

40264d: e8 ff fa ff ff callq 402151 <rio\_writen>

402652: 48 85 c0 test %rax,%rax

402655: 79 6e jns 4026c5 <submitr+0x40e>

402657: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

40265e: 3a 20 43

402661: 48 89 03 mov %rax,(%rbx)

402664: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40266b: 20 75 6e

40266e: 48 89 43 08 mov %rax,0x8(%rbx)

402672: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402679: 74 6f 20

40267c: 48 89 43 10 mov %rax,0x10(%rbx)

402680: 48 b8 77 72 69 74 65 movabs $0x6f74206574697277,%rax

402687: 20 74 6f

40268a: 48 89 43 18 mov %rax,0x18(%rbx)

40268e: 48 b8 20 74 68 65 20 movabs $0x7365722065687420,%rax

402695: 72 65 73

402698: 48 89 43 20 mov %rax,0x20(%rbx)

40269c: 48 b8 75 6c 74 20 73 movabs $0x7672657320746c75,%rax

4026a3: 65 72 76

4026a6: 48 89 43 28 mov %rax,0x28(%rbx)

4026aa: 66 c7 43 30 65 72 movw $0x7265,0x30(%rbx)

4026b0: c6 43 32 00 movb $0x0,0x32(%rbx)

4026b4: 89 ef mov %ebp,%edi

4026b6: e8 f5 e5 ff ff callq 400cb0 <close@plt>

4026bb: b8 ff ff ff ff mov $0xffffffff,%eax

4026c0: e9 fb 02 00 00 jmpq 4029c0 <submitr+0x709>

4026c5: 89 ee mov %ebp,%esi

4026c7: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4026ce: 00

4026cf: e8 6c f9 ff ff callq 402040 <rio\_readinitb>

4026d4: ba 00 20 00 00 mov $0x2000,%edx

4026d9: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

4026e0: 00

4026e1: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4026e8: 00

4026e9: e8 46 fb ff ff callq 402234 <rio\_readlineb>

4026ee: 48 85 c0 test %rax,%rax

4026f1: 7f 7d jg 402770 <submitr+0x4b9>

4026f3: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

4026fa: 3a 20 43

4026fd: 48 89 03 mov %rax,(%rbx)

402700: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

402707: 20 75 6e

40270a: 48 89 43 08 mov %rax,0x8(%rbx)

40270e: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402715: 74 6f 20

402718: 48 89 43 10 mov %rax,0x10(%rbx)

40271c: 48 b8 72 65 61 64 20 movabs $0x7269662064616572,%rax

402723: 66 69 72

402726: 48 89 43 18 mov %rax,0x18(%rbx)

40272a: 48 b8 73 74 20 68 65 movabs $0x6564616568207473,%rax

402731: 61 64 65

402734: 48 89 43 20 mov %rax,0x20(%rbx)

402738: 48 b8 72 20 66 72 6f movabs $0x72206d6f72662072,%rax

40273f: 6d 20 72

402742: 48 89 43 28 mov %rax,0x28(%rbx)

402746: 48 b8 65 73 75 6c 74 movabs $0x657320746c757365,%rax

40274d: 20 73 65

402750: 48 89 43 30 mov %rax,0x30(%rbx)

402754: c7 43 38 72 76 65 72 movl $0x72657672,0x38(%rbx)

40275b: c6 43 3c 00 movb $0x0,0x3c(%rbx)

40275f: 89 ef mov %ebp,%edi

402761: e8 4a e5 ff ff callq 400cb0 <close@plt>

402766: b8 ff ff ff ff mov $0xffffffff,%eax

40276b: e9 50 02 00 00 jmpq 4029c0 <submitr+0x709>

402770: 4c 8d 44 24 10 lea 0x10(%rsp),%r8

402775: 48 8d 8c 24 1c 20 00 lea 0x201c(%rsp),%rcx

40277c: 00

40277d: 48 8d 94 24 20 20 00 lea 0x2020(%rsp),%rdx

402784: 00

402785: be cf 34 40 00 mov $0x4034cf,%esi

40278a: 48 8d bc 24 20 60 00 lea 0x6020(%rsp),%rdi

402791: 00

402792: b8 00 00 00 00 mov $0x0,%eax

402797: e8 d4 e5 ff ff callq 400d70 <\_\_isoc99\_sscanf@plt>

40279c: e9 98 00 00 00 jmpq 402839 <submitr+0x582>

4027a1: ba 00 20 00 00 mov $0x2000,%edx

4027a6: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

4027ad: 00

4027ae: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

4027b5: 00

4027b6: e8 79 fa ff ff callq 402234 <rio\_readlineb>

4027bb: 48 85 c0 test %rax,%rax

4027be: 7f 79 jg 402839 <submitr+0x582>

4027c0: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

4027c7: 3a 20 43

4027ca: 48 89 03 mov %rax,(%rbx)

4027cd: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

4027d4: 20 75 6e

4027d7: 48 89 43 08 mov %rax,0x8(%rbx)

4027db: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

4027e2: 74 6f 20

4027e5: 48 89 43 10 mov %rax,0x10(%rbx)

4027e9: 48 b8 72 65 61 64 20 movabs $0x6165682064616572,%rax

4027f0: 68 65 61

4027f3: 48 89 43 18 mov %rax,0x18(%rbx)

4027f7: 48 b8 64 65 72 73 20 movabs $0x6f72662073726564,%rax

4027fe: 66 72 6f

402801: 48 89 43 20 mov %rax,0x20(%rbx)

402805: 48 b8 6d 20 74 68 65 movabs $0x657220656874206d,%rax

40280c: 20 72 65

40280f: 48 89 43 28 mov %rax,0x28(%rbx)

402813: 48 b8 73 75 6c 74 20 movabs $0x72657320746c7573,%rax

40281a: 73 65 72

40281d: 48 89 43 30 mov %rax,0x30(%rbx)

402821: c7 43 38 76 65 72 00 movl $0x726576,0x38(%rbx)

402828: 89 ef mov %ebp,%edi

40282a: e8 81 e4 ff ff callq 400cb0 <close@plt>

40282f: b8 ff ff ff ff mov $0xffffffff,%eax

402834: e9 87 01 00 00 jmpq 4029c0 <submitr+0x709>

402839: 0f b6 84 24 20 60 00 movzbl 0x6020(%rsp),%eax

402840: 00

402841: 83 e8 0d sub $0xd,%eax

402844: 75 15 jne 40285b <submitr+0x5a4>

402846: 0f b6 84 24 21 60 00 movzbl 0x6021(%rsp),%eax

40284d: 00

40284e: 83 e8 0a sub $0xa,%eax

402851: 75 08 jne 40285b <submitr+0x5a4>

402853: 0f b6 84 24 22 60 00 movzbl 0x6022(%rsp),%eax

40285a: 00

40285b: 85 c0 test %eax,%eax

40285d: 0f 85 3e ff ff ff jne 4027a1 <submitr+0x4ea>

402863: ba 00 20 00 00 mov $0x2000,%edx

402868: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

40286f: 00

402870: 48 8d bc 24 20 80 00 lea 0x8020(%rsp),%rdi

402877: 00

402878: e8 b7 f9 ff ff callq 402234 <rio\_readlineb>

40287d: 48 85 c0 test %rax,%rax

402880: 0f 8f 83 00 00 00 jg 402909 <submitr+0x652>

402886: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

40288d: 3a 20 43

402890: 48 89 03 mov %rax,(%rbx)

402893: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

40289a: 20 75 6e

40289d: 48 89 43 08 mov %rax,0x8(%rbx)

4028a1: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

4028a8: 74 6f 20

4028ab: 48 89 43 10 mov %rax,0x10(%rbx)

4028af: 48 b8 72 65 61 64 20 movabs $0x6174732064616572,%rax

4028b6: 73 74 61

4028b9: 48 89 43 18 mov %rax,0x18(%rbx)

4028bd: 48 b8 74 75 73 20 6d movabs $0x7373656d20737574,%rax

4028c4: 65 73 73

4028c7: 48 89 43 20 mov %rax,0x20(%rbx)

4028cb: 48 b8 61 67 65 20 66 movabs $0x6d6f726620656761,%rax

4028d2: 72 6f 6d

4028d5: 48 89 43 28 mov %rax,0x28(%rbx)

4028d9: 48 b8 20 72 65 73 75 movabs $0x20746c7573657220,%rax

4028e0: 6c 74 20

4028e3: 48 89 43 30 mov %rax,0x30(%rbx)

4028e7: c7 43 38 73 65 72 76 movl $0x76726573,0x38(%rbx)

4028ee: 66 c7 43 3c 65 72 movw $0x7265,0x3c(%rbx)

4028f4: c6 43 3e 00 movb $0x0,0x3e(%rbx)

4028f8: 89 ef mov %ebp,%edi

4028fa: e8 b1 e3 ff ff callq 400cb0 <close@plt>

4028ff: b8 ff ff ff ff mov $0xffffffff,%eax

402904: e9 b7 00 00 00 jmpq 4029c0 <submitr+0x709>

402909: 8b 94 24 1c 20 00 00 mov 0x201c(%rsp),%edx

402910: 81 fa c8 00 00 00 cmp $0xc8,%edx

402916: 74 28 je 402940 <submitr+0x689>

402918: 48 8d 4c 24 10 lea 0x10(%rsp),%rcx

40291d: be 98 34 40 00 mov $0x403498,%esi

402922: 48 89 df mov %rbx,%rdi

402925: b8 00 00 00 00 mov $0x0,%eax

40292a: e8 b1 e4 ff ff callq 400de0 <sprintf@plt>

40292f: 89 ef mov %ebp,%edi

402931: e8 7a e3 ff ff callq 400cb0 <close@plt>

402936: b8 ff ff ff ff mov $0xffffffff,%eax

40293b: e9 80 00 00 00 jmpq 4029c0 <submitr+0x709>

402940: 48 8d b4 24 20 60 00 lea 0x6020(%rsp),%rsi

402947: 00

402948: 48 89 df mov %rbx,%rdi

40294b: e8 f0 e2 ff ff callq 400c40 <strcpy@plt>

402950: 89 ef mov %ebp,%edi

402952: e8 59 e3 ff ff callq 400cb0 <close@plt>

402957: 0f b6 03 movzbl (%rbx),%eax

40295a: 83 e8 4f sub $0x4f,%eax

40295d: 75 18 jne 402977 <submitr+0x6c0>

40295f: 0f b6 53 01 movzbl 0x1(%rbx),%edx

402963: 83 ea 4b sub $0x4b,%edx

402966: 75 11 jne 402979 <submitr+0x6c2>

402968: 0f b6 53 02 movzbl 0x2(%rbx),%edx

40296c: 83 ea 0a sub $0xa,%edx

40296f: 75 08 jne 402979 <submitr+0x6c2>

402971: 0f b6 53 03 movzbl 0x3(%rbx),%edx

402975: eb 02 jmp 402979 <submitr+0x6c2>

402977: 89 c2 mov %eax,%edx

402979: 85 d2 test %edx,%edx

40297b: 74 30 je 4029ad <submitr+0x6f6>

40297d: bf e0 34 40 00 mov $0x4034e0,%edi

402982: b9 05 00 00 00 mov $0x5,%ecx

402987: 48 89 de mov %rbx,%rsi

40298a: f3 a6 repz cmpsb %es:(%rdi),%ds:(%rsi)

40298c: 0f 97 c1 seta %cl

40298f: 0f 92 c2 setb %dl

402992: 38 d1 cmp %dl,%cl

402994: 74 1e je 4029b4 <submitr+0x6fd>

402996: 85 c0 test %eax,%eax

402998: 75 0d jne 4029a7 <submitr+0x6f0>

40299a: 0f b6 43 01 movzbl 0x1(%rbx),%eax

40299e: 83 e8 4b sub $0x4b,%eax

4029a1: 75 04 jne 4029a7 <submitr+0x6f0>

4029a3: 0f b6 43 02 movzbl 0x2(%rbx),%eax

4029a7: 85 c0 test %eax,%eax

4029a9: 75 10 jne 4029bb <submitr+0x704>

4029ab: eb 13 jmp 4029c0 <submitr+0x709>

4029ad: b8 00 00 00 00 mov $0x0,%eax

4029b2: eb 0c jmp 4029c0 <submitr+0x709>

4029b4: b8 00 00 00 00 mov $0x0,%eax

4029b9: eb 05 jmp 4029c0 <submitr+0x709>

4029bb: b8 ff ff ff ff mov $0xffffffff,%eax

4029c0: 48 81 c4 48 a0 00 00 add $0xa048,%rsp

4029c7: 5b pop %rbx

4029c8: 5d pop %rbp

4029c9: 41 5c pop %r12

4029cb: 41 5d pop %r13

4029cd: 41 5e pop %r14

4029cf: 41 5f pop %r15

4029d1: c3 retq

00000000004029d2 <init\_timeout>:

4029d2: 53 push %rbx

4029d3: 89 fb mov %edi,%ebx

4029d5: 85 ff test %edi,%edi

4029d7: 74 1f je 4029f8 <init\_timeout+0x26>

4029d9: 85 ff test %edi,%edi

4029db: 79 05 jns 4029e2 <init\_timeout+0x10>

4029dd: bb 00 00 00 00 mov $0x0,%ebx

4029e2: be 52 20 40 00 mov $0x402052,%esi

4029e7: bf 0e 00 00 00 mov $0xe,%edi

4029ec: e8 ef e2 ff ff callq 400ce0 <signal@plt>

4029f1: 89 df mov %ebx,%edi

4029f3: e8 a8 e2 ff ff callq 400ca0 <alarm@plt>

4029f8: 5b pop %rbx

4029f9: c3 retq

00000000004029fa <init\_driver>:

4029fa: 55 push %rbp

4029fb: 53 push %rbx

4029fc: 48 83 ec 18 sub $0x18,%rsp

402a00: 48 89 fd mov %rdi,%rbp

402a03: be 01 00 00 00 mov $0x1,%esi

402a08: bf 0d 00 00 00 mov $0xd,%edi

402a0d: e8 ce e2 ff ff callq 400ce0 <signal@plt>

402a12: be 01 00 00 00 mov $0x1,%esi

402a17: bf 1d 00 00 00 mov $0x1d,%edi

402a1c: e8 bf e2 ff ff callq 400ce0 <signal@plt>

402a21: be 01 00 00 00 mov $0x1,%esi

402a26: bf 1d 00 00 00 mov $0x1d,%edi

402a2b: e8 b0 e2 ff ff callq 400ce0 <signal@plt>

402a30: ba 00 00 00 00 mov $0x0,%edx

402a35: be 01 00 00 00 mov $0x1,%esi

402a3a: bf 02 00 00 00 mov $0x2,%edi

402a3f: e8 cc e3 ff ff callq 400e10 <socket@plt>

402a44: 89 c3 mov %eax,%ebx

402a46: 85 c0 test %eax,%eax

402a48: 79 4f jns 402a99 <init\_driver+0x9f>

402a4a: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax

402a51: 3a 20 43

402a54: 48 89 45 00 mov %rax,0x0(%rbp)

402a58: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax

402a5f: 20 75 6e

402a62: 48 89 45 08 mov %rax,0x8(%rbp)

402a66: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402a6d: 74 6f 20

402a70: 48 89 45 10 mov %rax,0x10(%rbp)

402a74: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax

402a7b: 65 20 73

402a7e: 48 89 45 18 mov %rax,0x18(%rbp)

402a82: c7 45 20 6f 63 6b 65 movl $0x656b636f,0x20(%rbp)

402a89: 66 c7 45 24 74 00 movw $0x74,0x24(%rbp)

402a8f: b8 ff ff ff ff mov $0xffffffff,%eax

402a94: e9 23 01 00 00 jmpq 402bbc <init\_driver+0x1c2>

402a99: bf 33 30 40 00 mov $0x403033,%edi

402a9e: e8 4d e2 ff ff callq 400cf0 <gethostbyname@plt>

402aa3: 48 85 c0 test %rax,%rax

402aa6: 75 68 jne 402b10 <init\_driver+0x116>

402aa8: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax

402aaf: 3a 20 44

402ab2: 48 89 45 00 mov %rax,0x0(%rbp)

402ab6: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax

402abd: 20 75 6e

402ac0: 48 89 45 08 mov %rax,0x8(%rbp)

402ac4: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax

402acb: 74 6f 20

402ace: 48 89 45 10 mov %rax,0x10(%rbp)

402ad2: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax

402ad9: 76 65 20

402adc: 48 89 45 18 mov %rax,0x18(%rbp)

402ae0: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax

402ae7: 72 20 61

402aea: 48 89 45 20 mov %rax,0x20(%rbp)

402aee: c7 45 28 64 64 72 65 movl $0x65726464,0x28(%rbp)

402af5: 66 c7 45 2c 73 73 movw $0x7373,0x2c(%rbp)

402afb: c6 45 2e 00 movb $0x0,0x2e(%rbp)

402aff: 89 df mov %ebx,%edi

402b01: e8 aa e1 ff ff callq 400cb0 <close@plt>

402b06: b8 ff ff ff ff mov $0xffffffff,%eax

402b0b: e9 ac 00 00 00 jmpq 402bbc <init\_driver+0x1c2>

402b10: 48 c7 04 24 00 00 00 movq $0x0,(%rsp)

402b17: 00

402b18: 48 c7 44 24 08 00 00 movq $0x0,0x8(%rsp)

402b1f: 00 00

402b21: 66 c7 04 24 02 00 movw $0x2,(%rsp)

402b27: 48 8b 48 18 mov 0x18(%rax),%rcx

402b2b: 48 63 50 14 movslq 0x14(%rax),%rdx

402b2f: 48 8d 74 24 04 lea 0x4(%rsp),%rsi

402b34: 48 8b 39 mov (%rcx),%rdi

402b37: e8 54 e2 ff ff callq 400d90 <bcopy@plt>

402b3c: 66 c7 44 24 02 17 7d movw $0x7d17,0x2(%rsp)

402b43: ba 10 00 00 00 mov $0x10,%edx

402b48: 48 89 e6 mov %rsp,%rsi

402b4b: 89 df mov %ebx,%edi

402b4d: e8 ae e2 ff ff callq 400e00 <connect@plt>

402b52: 85 c0 test %eax,%eax

402b54: 79 50 jns 402ba6 <init\_driver+0x1ac>

402b56: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax

402b5d: 3a 20 55

402b60: 48 89 45 00 mov %rax,0x0(%rbp)

402b64: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax

402b6b: 20 74 6f

402b6e: 48 89 45 08 mov %rax,0x8(%rbp)

402b72: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6e6f6320,%rax

402b79: 65 63 74

402b7c: 48 89 45 10 mov %rax,0x10(%rbp)

402b80: 48 b8 20 74 6f 20 73 movabs $0x76726573206f7420,%rax

402b87: 65 72 76

402b8a: 48 89 45 18 mov %rax,0x18(%rbp)

402b8e: 66 c7 45 20 65 72 movw $0x7265,0x20(%rbp)

402b94: c6 45 22 00 movb $0x0,0x22(%rbp)

402b98: 89 df mov %ebx,%edi

402b9a: e8 11 e1 ff ff callq 400cb0 <close@plt>

402b9f: b8 ff ff ff ff mov $0xffffffff,%eax

402ba4: eb 16 jmp 402bbc <init\_driver+0x1c2>

402ba6: 89 df mov %ebx,%edi

402ba8: e8 03 e1 ff ff callq 400cb0 <close@plt>

402bad: 66 c7 45 00 4f 4b movw $0x4b4f,0x0(%rbp)

402bb3: c6 45 02 00 movb $0x0,0x2(%rbp)

402bb7: b8 00 00 00 00 mov $0x0,%eax

402bbc: 48 83 c4 18 add $0x18,%rsp

402bc0: 5b pop %rbx

402bc1: 5d pop %rbp

402bc2: c3 retq

0000000000402bc3 <driver\_post>:

402bc3: 53 push %rbx

402bc4: 48 83 ec 10 sub $0x10,%rsp

402bc8: 4c 89 cb mov %r9,%rbx

402bcb: 45 85 c0 test %r8d,%r8d

402bce: 74 22 je 402bf2 <driver\_post+0x2f>

402bd0: 48 89 ce mov %rcx,%rsi

402bd3: bf e5 34 40 00 mov $0x4034e5,%edi

402bd8: b8 00 00 00 00 mov $0x0,%eax

402bdd: e8 9e e0 ff ff callq 400c80 <printf@plt>

402be2: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)

402be7: c6 43 02 00 movb $0x0,0x2(%rbx)

402beb: b8 00 00 00 00 mov $0x0,%eax

402bf0: eb 39 jmp 402c2b <driver\_post+0x68>

402bf2: 48 85 ff test %rdi,%rdi

402bf5: 74 26 je 402c1d <driver\_post+0x5a>

402bf7: 80 3f 00 cmpb $0x0,(%rdi)

402bfa: 74 21 je 402c1d <driver\_post+0x5a>

402bfc: 4c 89 0c 24 mov %r9,(%rsp)

402c00: 49 89 c9 mov %rcx,%r9

402c03: 49 89 d0 mov %rdx,%r8

402c06: 48 89 f9 mov %rdi,%rcx

402c09: 48 89 f2 mov %rsi,%rdx

402c0c: be 7d 17 00 00 mov $0x177d,%esi

402c11: bf 33 30 40 00 mov $0x403033,%edi

402c16: e8 9c f6 ff ff callq 4022b7 <submitr>

402c1b: eb 0e jmp 402c2b <driver\_post+0x68>

402c1d: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)

402c22: c6 43 02 00 movb $0x0,0x2(%rbx)

402c26: b8 00 00 00 00 mov $0x0,%eax

402c2b: 48 83 c4 10 add $0x10,%rsp

402c2f: 5b pop %rbx

402c30: c3 retq

0000000000402c31 <check>:

402c31: 89 f8 mov %edi,%eax

402c33: c1 e8 1c shr $0x1c,%eax

402c36: 85 c0 test %eax,%eax

402c38: 74 1d je 402c57 <check+0x26>

402c3a: b9 00 00 00 00 mov $0x0,%ecx

402c3f: eb 0b jmp 402c4c <check+0x1b>

402c41: 89 f8 mov %edi,%eax

402c43: d3 e8 shr %cl,%eax

402c45: 3c 0a cmp $0xa,%al

402c47: 74 14 je 402c5d <check+0x2c>

402c49: 83 c1 08 add $0x8,%ecx

402c4c: 83 f9 1f cmp $0x1f,%ecx

402c4f: 7e f0 jle 402c41 <check+0x10>

402c51: b8 01 00 00 00 mov $0x1,%eax

402c56: c3 retq

402c57: b8 00 00 00 00 mov $0x0,%eax

402c5c: c3 retq

402c5d: b8 00 00 00 00 mov $0x0,%eax

402c62: c3 retq

0000000000402c63 <gencookie>:

402c63: 53 push %rbx

402c64: 83 c7 01 add $0x1,%edi

402c67: e8 b4 df ff ff callq 400c20 <srandom@plt>

402c6c: e8 df e0 ff ff callq 400d50 <random@plt>

402c71: 89 c3 mov %eax,%ebx

402c73: 89 c7 mov %eax,%edi

402c75: e8 b7 ff ff ff callq 402c31 <check>

402c7a: 85 c0 test %eax,%eax

402c7c: 74 ee je 402c6c <gencookie+0x9>

402c7e: 89 d8 mov %ebx,%eax

402c80: 5b pop %rbx

402c81: c3 retq

402c82: 66 2e 0f 1f 84 00 00 nopw %cs:0x0(%rax,%rax,1)

402c89: 00 00 00

402c8c: 0f 1f 40 00 nopl 0x0(%rax)

0000000000402c90 <\_\_libc\_csu\_init>:

402c90: 41 57 push %r15

402c92: 41 89 ff mov %edi,%r15d

402c95: 41 56 push %r14

402c97: 49 89 f6 mov %rsi,%r14

402c9a: 41 55 push %r13

402c9c: 49 89 d5 mov %rdx,%r13

402c9f: 41 54 push %r12

402ca1: 4c 8d 25 68 21 20 00 lea 0x202168(%rip),%r12 # 604e10 <\_\_frame\_dummy\_init\_array\_entry>

402ca8: 55 push %rbp

402ca9: 48 8d 2d 68 21 20 00 lea 0x202168(%rip),%rbp # 604e18 <\_\_init\_array\_end>

402cb0: 53 push %rbx

402cb1: 4c 29 e5 sub %r12,%rbp

402cb4: 31 db xor %ebx,%ebx

402cb6: 48 c1 fd 03 sar $0x3,%rbp

402cba: 48 83 ec 08 sub $0x8,%rsp

402cbe: e8 05 df ff ff callq 400bc8 <\_init>

402cc3: 48 85 ed test %rbp,%rbp

402cc6: 74 1e je 402ce6 <\_\_libc\_csu\_init+0x56>

402cc8: 0f 1f 84 00 00 00 00 nopl 0x0(%rax,%rax,1)

402ccf: 00

402cd0: 4c 89 ea mov %r13,%rdx

402cd3: 4c 89 f6 mov %r14,%rsi

402cd6: 44 89 ff mov %r15d,%edi

402cd9: 41 ff 14 dc callq \*(%r12,%rbx,8)

402cdd: 48 83 c3 01 add $0x1,%rbx

402ce1: 48 39 eb cmp %rbp,%rbx

402ce4: 75 ea jne 402cd0 <\_\_libc\_csu\_init+0x40>

402ce6: 48 83 c4 08 add $0x8,%rsp

402cea: 5b pop %rbx

402ceb: 5d pop %rbp

402cec: 41 5c pop %r12

402cee: 41 5d pop %r13

402cf0: 41 5e pop %r14

402cf2: 41 5f pop %r15

402cf4: c3 retq

402cf5: 90 nop

402cf6: 66 2e 0f 1f 84 00 00 nopw %cs:0x0(%rax,%rax,1)

402cfd: 00 00 00

0000000000402d00 <\_\_libc\_csu\_fini>:

402d00: f3 c3 repz retq

402d02: 66 90 xchg %ax,%ax

Disassembly of section .fini:

0000000000402d04 <\_fini>:

402d04: 48 83 ec 08 sub $0x8,%rsp

402d08: 48 83 c4 08 add $0x8,%rsp

402d0c: c3 retq