Lecture 16 Algorithms & Data Structures

Goldsmiths Computing

February 25, 2019

Introduction

Fixed point

Multiplication

Floating point

Outline

Introduction

Lecture

- 1. Random number generation
- 2. Comparison sorts
 - · insertion sort
 - · merge sort
 - heap sort
 - quick sort
 - $\Theta(N \log N)$ complexity bound
- 3. Shuffling
 - · several broken shuffle versions
 - one correct $\Theta(N \log N)$ shuffle
 - Θ(N) Fisher-Yates shuffle

Lab

Catchup and future plans

VLE activities

Graphs quiz

VLE activities (cont'd)

Graphs quiz

Graphs quiz

VLE activities (cont'd)

Graphs submission

Outline

Introduction

Fixed point

Multiplication

Floating point

Motivation

Representing:

- · integers within a range
- · continuous sequence of place-value digits

Useful and practical:

- · simple to implement in hardware
- reasonable properties
- · ... but some unexpected behaviours too

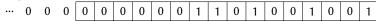
- data type for a number
- · fixed number of digits
- · implicit scaling factor
 - number represented is integer value multiplied by scaling factor

Unsigned integers

Fixed-width of binary digits

- · assumed infinite leftward zeros
- radix (decimal) point at the right-hand end of the fixed-width field
- · scaling factor of 1

Representation



(16-bit fixed point integer)

What happens when a calculation would put a 1 in the infinite sea of zeros?

hardware wraparound, carry flag

What happens when a calculation would put a 1 in the infinite sea of zeros?

hardware wraparound, carry flag

C++ wraparound

What happens when a calculation would put a 1 in the infinite sea of zeros?

hardware wraparound, carry flag

C++ wraparound

Java bad luck, no unsigned integers

Many possible representations:

sign-magnitude

```
sign high bit (0: positive; 1: negative) magnitude remaining bits
```

ones complement

twos-complement

$$-x \neg x + 1$$

What's so good about twos-complement?

- · only one zero
- · addition, subtraction and multiplication all the same as unsigned

Representation



In practice, all current systems use twos-complement.

$\begin{aligned} & \textbf{function} \ \text{NEG}(x) \\ & r \leftarrow \neg x \\ & \textbf{return} \ r + 1 \end{aligned}$



Overflow

What happens when a calculation would put a non-sign bit in the infinite sea of sign bits?

hardware wraparound, carry flag

What happens when a calculation would put a non-sign bit in the infinite sea of sign bits?

hardware wraparound, carry flag

Java wraparound

What happens when a calculation would put a non-sign bit in the infinite sea of sign bits?

hardware wraparound, carry flag

Java wraparound

C++ bad luck, undefined behaviour

Absolute value

```
abs (C++ cstdlib) or Math.abs (Java):
    return (as an int) the absolute (non-negative) value of its argument
function ABS(X)
    if x < 0 then
        return NEG(X)
    else
        return x
    end if
end function</pre>
```

· does this always return a non-negative answer?

```
popcnt (x86 instruction)
```

return how many one bits are set in the (unsigned) integer argument.

Divide-and-conquer implementation:

```
function POPCNT(x)
    return POPCNTW(x,W)
                                                        ▶ W is the integer width
end function
function POPCNTW(x,w)
    if w = 1 then
        return x
    else
        nw \leftarrow \frac{w}{2}
                                                by wassumed to be a power of 2
        lo \leftarrow POPCNTW(x \& 2^{nw} - 1, nw)
        hi \leftarrow POPCNTW(\left|\frac{x}{2^{nw}}\right|, nw)
        return lo + hi
    end if
end function
```

Parallel divide-and-conquer implementation:

1. Reading

- Henry S. Warren, Jr. Hacker's Delight, Addison-Wesley (2003)
 - sections 5-1, 7-1

Introduction

Fixed point

Multiplication

Floating point

- · working with numbers as a data structure
- · everyone knows how to multiply
- · almost no-one knows how to multiply efficiently

Previously

Numbers as array of digits (binary: bits)

• numbers have a width w, at least $1 + \log_h(n)$

Logical operations

and(x,y) return the bitwise logical and of x and y xor(x,y) return the bitwise exclusive-or of x and y

Previously

Numbers as array of digits (binary: bits)

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Logical operations

```
and(x,y) return the bitwise logical and of x and y xor(x,y) return the bitwise exclusive-or of x and y
```

Arithmetic operations

```
add(x,y) return the sum of x and y
sub(x,y) return the difference between x and y
shift(x,n) return x multiplied by the base n times
```

Previously

Multiplication

000000000000

Numbers as array of digits (binary: bits)

• numbers have a width w, at least $1 + \log_b(n)$

Logical operations

```
and(x,y) return the bitwise logical and of x and y
xor(x,y) return the bitwise exclusive-or of x and y
```

Arithmetic operations

```
add(x,y) return the sum of x and y
 sub(x,y) return the difference between x and y
shift(x,n) return x multiplied by the base n times
```

Complexity

- until now, Θ(1)
- in fact, $\Theta(w) \sim \Theta(\log(n))$

(logarithmic factor is usually irrelevant, or width is taken as constant)



Given these basic operations:

- · how do we implement multiplication?
- · how efficient is it?

Example

123×135:



Example

123×135:

Primary (old-)school multiplication

			1	2	3
×			1	2	5
				1	5
			1	0	
			5		
				9	
			6		
		3			
			3		
		2			
	1				
	1	6	6	Λ	5

Example

123×135:

Primary school multiplication

×	100	20	3
100	10000	2000	300
30	3000	600	90
5	500	100	15

Complexity analysis

time

For each digit in *x*

· multiply with each digit in y.

Assume x and y are each of width w

$$\Rightarrow \Theta(w^2)$$

Write

•
$$x = x_{hi} \times b^{w/2} + x_{lo}$$

•
$$y = y_{hi} \times b^{w/2} + y_{lo}$$

Then
$$x \times y = x_{hi}y_{hi}b^w + (x_{hi}y_{lo} + x_{lo}y_{hi})b^{w/2} + x_{lo}y_{lo}$$

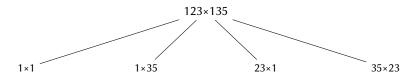
$$x \times y = x_{hi}y_{hi} b^{w} + (x_{hi}y_{lo} + x_{lo}y_{hi})b^{w/2} + x_{lo}y_{lo}$$

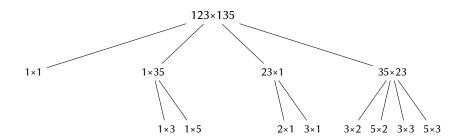
$$123 \times 135 = (1 \times 100 + 23) \times (1 \times 100 + 35)$$

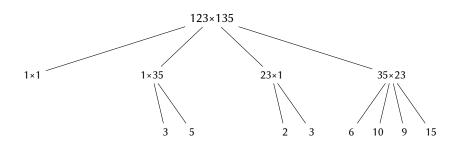
$$= 1 \times 1 \times 10000 + (23 \times 1 + 1 \times 35) \times 100 + 23 \times 35$$

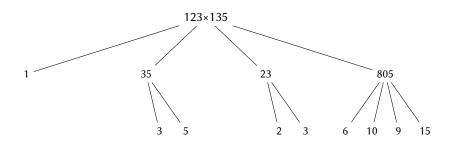
$$= 10000 + 5800 + 805$$

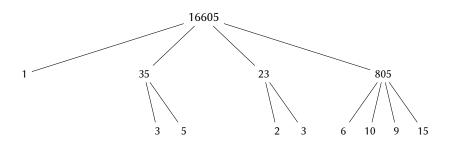
123×135











Complexity analysis

time

$$\begin{split} x \times y &= x_{hi} y_{hi} \ b^w + (x_{hi} y_{lo} + x_{lo} y_{hi}) b^{w/2} + x_{lo} y_{lo} \\ & \cdot \ T_w = 4 T_{w/2} + O(w) \end{split}$$

Complexity analysis

time

$$x \times y = x_{hi}y_{hi} b^{w} + (x_{hi}y_{lo} + x_{lo}y_{hi})b^{w/2} + x_{lo}y_{lo}$$

• $T_{w} = 4T_{w/2} + O(w)$

solution

Use the master theorem:

•
$$a = 4$$
, $b = 2$, $c = 1 \Rightarrow case 1$

•
$$\log_b a = \log_2(4) = 2$$

 $\Rightarrow T(w) \in \Theta(w^2)$

Divide and conquer, Karatsuba

Still with

•
$$x = x_{hi} \times b^{w/2} + x_{lo}$$

•
$$y = y_{hi} \times b^{w/2} + y_{lo}$$

Calculate

•
$$z_{hi} = x_{hi} \times y_{hi}$$

•
$$z_{lo} = x_{lo} \times y_{lo}$$

•
$$z_c = (x_{hi} + x_{lo}) \times (y_{hi} + y_{lo})$$

Then
$$x \times y = \frac{z_{hi}}{b^w} + (\frac{z_c}{c} - z_{hi} - z_{lo})b^{w/2} + \frac{z_{lo}}{c}$$

Divide and conquer, Karatsuba

Still with

•
$$x = x_{hi} \times b^{w/2} + x_{lo}$$

• $y = y_{hi} \times b^{w/2} + y_{lo}$

Calculate

•
$$z_{hi} = x_{hi} \times y_{hi}$$

• $z_{lo} = x_{lo} \times y_{lo}$
• $z_{c} = (x_{hi} + x_{lo}) \times (y_{hi} + y_{lo})$
Then $x \times y = z_{hi} b^{w} + (z_{c} - z_{hi} - z_{lo})b^{w/2} + z_{lo}$

Example

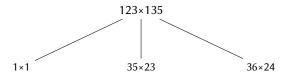
123×135:

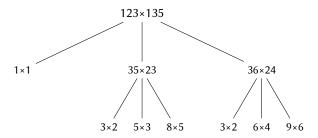
•
$$z_{hi} = 1$$
; $z_{lo} = 35 \times 23 = 805$; $z_c = 36 \times 24 = 864$

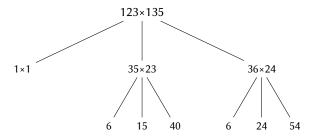
so:

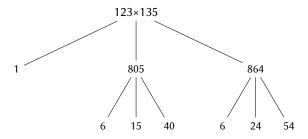
$$123 \times 135 = 1 \times 10000 + (864 - 805 - 1) \times 100 + 805$$
$$= 10000 + 5800 + 805$$
$$= 16605$$

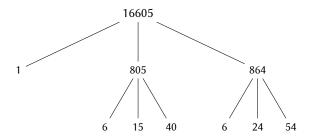
123×135











Complexity analysis

time

$$x \times y = z_{hi} b^w + (z_c - z_{hi} - z_{lo})b^{w/2} + z_{lo}$$

• $T_w = 3T_{w/2} + O(w)$

time

$$x \times y = z_{hi} b^w + (z_c - z_{hi} - z_{lo})b^{w/2} + z_{lo}$$

• $T_w = 3T_{w/2} + O(w)$

solution

Use the master theorem:

•
$$a = 3, b = 2, c = 1 \Rightarrow case 1$$

• $\log_b a = \log_2(3) = 1.58$

$$\Rightarrow T(w) \in \Theta(w^{1.58})$$

Work

- 1. Reading
 - · CLRS, section 4.2: matrix multiplication

Introduction

Fixed point

Multiplication

Floating point

Motivation

Represent a wider range of numbers than with fixed point

- fixed point: constant absolute precision
- floating point: constant relative precision

Convenient approximation to Real numbers

- but only an approximation
 - ... some unexpected behaviours too

Definition

Floating point behaviour is defined by an engineering standard:

• IEEE 754 (1985, revised 2008)

Implemented by most hardware platforms:

- · floating-point units in CPUs
 - (software support for FPU features varies)
- graphics cards (CUDA, OpenGL)
 - and other coprocessors

Operations

Operations on floats:

add return the sum of two floating point numbers sub return the difference of two floating point numbers mul return the product of two floating point numbers div return the quotient of two floating point numbers sqrt return the square root of one floating point number as on floating point units:

Operations on floating point units:

rounding mode should rounding go towards +∞, 0, -∞ or even?

trapping should the FPU generate an exception for conditions such as overflow or divide by zero?



Generaliae

Represent a number *n* as:

$$n = \text{sign} \times \text{significand} \times 2^{\text{exponent}}$$
 sign 1 or -1
 $\text{significand number in } [1,2)$
 $\text{exponent } \left\lfloor \log_2 n \right\rfloor$

Represent this in a fixed-size field using:

```
sign bit 0 (positive) or 1 (negative) mantissa fractional part of significand exponent exponent + bias
```

$$n = (-1)^s \times (1+m) \times 2^{e-B}$$

Single-precision

- 32-bit quantity:
 - 1 sign bit
 - · 8 exponent bits
 - bias is 127, range is $\pm 2^{-126}$ to 2^{127}
 - · 23 mantissa bits
 - plus "hidden bit" gives 24 binary (~7 decimal) digits of precision

Single-precision

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Representation



Single-precision

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Representation

Example

$$0.5 = 1 \times (1 + 0) \times 2^{-1}$$

sign 0

mantissa 0

exponent 126 (0x7e)

overall 0x3f000000



No representation for zero in this scheme

$$\left[\log_2(x)\right] = -\infty$$

Special representation of zero:

sign 0 or 1

exponent field 0 mantissa 0

- 64-bit quantity:
 - 1 sign bit
 - 11 exponent bits
 - bias is 1023, range is $\pm 2^{-1022}$ to 2^{1023}
 - 52 mantissa bits
 - plus "hidden bit" gives 53 binary (~16 decimal) digits of precision

- 64-bit quantity:
 - 1 sign bit
 - 11 exponent bits
 - bias is 1023, range is $\pm 2^{-1022}$ to 2^{1023}
 - 52 mantissa bits
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Representation

Double-precision

- 64-bit quantity:
 - 1 sign bit
 - 11 exponent bits
 - bias is 1023, range is $\pm 2^{-1022}$ to 2^{1023}
 - 52 mantissa bits
 - plus "hidden bit" gives 53 binary (~16 decimal) digits of precision

Representation

Example

$$0.75 = 1 \times (1 + 0.5) \times 2^{-1}$$

Floating point has a larger range than precision

- calculations with floating points will usually not give an exactly representable answer
 - (even if the input numbers were exact)

Epsilon

 ϵ is the smallest float which you can add to 1.0 and get an answer that isn't 1.0:

single-precision
$$2^{-24} + 2^{-47}$$
 double-precision $2^{-53} + 2^{-105}$

For all
$$0 < x < \varepsilon$$

$$1 + x \rightarrow 1$$

Inverse square root

Game programming history

• need to take $f(x) = \frac{1}{\sqrt{x}}$ often and quickly

Use
$$\log_2((1 + m) \times 2^{e-B}) \approx e - B + m$$
:

This was good in 1999 (Quake III Arena)

- nowadays we have hardware to do this
- SSE rsqrtss

1. Reading

 David Goldberg, What every computer scientist should know about floating point arithmetic, Computing (1991)