### "Chrome V8 Source Code" 31. What exactly does Ignition do? (two)



#### 1 abstract

This article is the sixth of the Builtin topic, explaining the Builtin::kInterpreterEntryTrampoline source code in Ignition. include InterpreterEntryTrampoline, Runtime\_InterpreterTraceBytecodeEntry and Runtime\_InterpreterTraceBytecodeExit

### 2 InterpreterEntryTrampoline

Tip: The V8 version used in this article is 7.9.10, CPU: x64, Builtins-x64.cc, see the previous article for sample code.

The source code of InterpreterEntryTrampoline is as follows:

```
1. void Builtins::Generate_InterpreterEntryTrampoline(MacroAssembler* masm) {
2. Register closure = rdi;
Register feedback_vector = rbx;
     __LoadTaggedPointerField (
5.
          rax, FieldOperand(closure, JSFunction::kSharedFunctionInfoOffset));
6.
     __LoadTaggedPointerField (
7.
          kInterpreterBytecodeArrayRegister,
          FieldOperand(rax, SharedFunctionInfo::kFunctionDataOffset));
9. GetSharedFunctionInfoBytecode(masm, kInterpreterBytecodeArrayRegister,
10.kScratchRegister);
        Label compile_lazy;
12.
         __CmpObjectType (kInterpreterBytecodeArrayRegister, BYTECODE_ARRAY_TYPE,
rax);
```

```
13.
          i(not equal, &compile lazy);
14.
          __ bind(&push_stack_frame);
15.
         FrameScope frame_scope(masm, StackFrame::MANUAL);
16.
         ___ pushq(rbp); // Caller's frame pointer.
17.
         movg(rbp, rsp);
18.
          __ Push(rsi); // Callee's context.
19.
         ___ Push(rdi); // Callee's JS function.
20.
         movw(FieldOperand(kInterpreterBytecodeArrayRegister,
                                     BytecodeArray::kOsrNestingLevelOffset),
                    Immediate(0));
         __ movq(kInterpreterBytecodeOffsetRegister,
                    Immediate(BytecodeArray::kHeaderSize - kHeapObjectTag));
25.
          _Push (kInterpreterBytecodeArrayRegister);
26.
           _SmiTag (rcx, kInterpreterBytecodeOffsetRegister);
27.
         Push (rcx):
28.
29.
          //Allocate the local and temporary register file on the stack.
30.
          //Omit.....
31.
32.
         LoadRoot(kInterpreterAccumulatorRegister, RootIndex::kUndefinedValue);
33.
         Label do dispatch;
34.
         bind(&do dispatch);
35.
         Move (
36.
              kInterpreterDispatchTableRegister,
37.
              ExternalReference::interpreter_dispatch_table_address(masm-
>isolate()));
38.
         movzxbg(r11, Operand(kInterpreterBytecodeArrayRegister,
39.
                                        kInterpreterBytecodeOffsetRegister, times_1, 0));
40.
         __ movq(kJavaScriptCallCodeStartRegister,
41.
                   Operand(kInterpreterDispatchTableRegister, r11,
42.
                              times system pointer size, 0));
43.
          call(kJavaScriptCallCodeStartRegister);
44.
         masm->isolate()->heap()->SetInterpreterEntryReturnPCOffset(masm-
>pc_offset());
45.
         __ movq(kInterpreterBytecodeArrayRegister,
46.
                   Operand(rbp, InterpreterFrameConstants::kBytecodeArrayFromFp));
47.
         __ movq(kInterpreterBytecodeOffsetRegister,
48.
                   Operand(rbp, InterpreterFrameConstants::kBytecodeOffsetFromFp));
49.
         SmiUntag (kInterpreterBytecodeOffsetRegister,
50.
                         kInterpreterBytecodeOffsetRegister);
51.
         Label do_return;
52.
         movzxbq(rbx, Operand(kInterpreterBytecodeArrayRegister,
53.
                                        kInterpreterBytecodeOffsetRegister, times_1, 0));
54.
         AdvanceBytecodeOffsetOrReturn(masm, kInterpreterBytecodeArrayRegister,
55.
                                                 kInterpreterBytecodeOffsetRegister, rbx, rcx,
56.
                                                &do_return);
57.
         __imp (&do_dispatch);
58.
          bind(&do return);
59.
         LeaveInterpreterFrame(masm, rbx, rcx);
60.
         __ret (0);
61.
          _ bind(&compile_lazy);
62.
         GenerateTailCallToReturnedCode(masm, Runtime::kCompileLazy);
63.
          ___ int3();
64.
```

In the above code, the 4th line of code: remove the SharedFunction from JSFunction and store it in rax; the 6th line of code: get the kFunctionDataOffset data from SharedFunctionInfo and store it in kInterpreterBytecodeArrayRegister; the 9th line of code: load the Bytecodearray to kInterpreterBytecodeArrayRegister. **Detailed description: (1)** In the FieldOperand(x,y) method, x is

the base address and y is the offset. This method is used to return the data at the position of x+y; (2) Because

SharedFunction::kFunctionDataOffset may store Bytecodearray or Builtin, so after executing the 6th line of code, you need to use the 9th line of code to determine whether the data in kInterpreterBytecodeArrayRegister is a Bytecodearray. Lines 10-13 of the above code: Determine whether the value of kInterpreterBytecodeArrayRegister is Bytecodarray or Builtins::kCompileLazy, and jump to the corresponding Label based on the judgment result; Lines 15-19 of the code store the caller's stack frame and push the callee's information onto the stack. Lines 20-27 of code obtain the offset of the first Bytecode in the Bytecodearray and push it onto the stack. The BytecodeArray class inherits from FixedArrayBase, and FixedArrayBase inherits from HeapObject, so when obtaining the first Bytecode, you need to use the offset just obtained. Line 32 above initializes kInterpreterAccumulatorRegister; line 35 of code loads dispatch to kInterpreterDispatchTableRegister; lines 38-40 of code loads the first Bytecode to kJavaScriptCallCodeStartRegister; line 43 of code starts executing Bytecode. After all Bytecode is executed, it will jump to line 44 of code to set the return address. In two cases, the above lines 45-63 of the code will be executed. (1) When all Bytecode is executed, Dispatch() will be called at the end of Bytecode, so it will only return when all Bytecode is executed; (2) During the execution of Bytecode Other Builtins are called in. Because calling other Builtins requires rebuilding the stack, InterpreterEntryTrampoline is also used. At this point, the analysis of InterpreterEntryTrampoline is completed.

## 3 Register

There are many Registers used in InterpreterEntryTrampoline, the list is as follows:

constexpr Register kReturnRegister0 = rax; constexpr Register kReturnRegister1 = rdx; constexpr Register kReturnRegister2 = r8; constexpr Register kJSFunctionRegister = rdi; constexpr Register kContextRegister = rsi; constexpr Register kAllocateSizeRegister = rdx; constexpr Register kSpeculationPoisonRegister = r12; constexpr Register kInterpreterAccumulatorRegister = rax; constexpr Register kInterpreterBytecodeOffsetRegister = r9; constexpr Register kInterpreterBytecodeArrayRegister = r14; constexpr Register kInterpreterBytecodeArrayRegister = r15; //Omitted......

The registers commonly used in InterpreterEntryTrampoline are rax, rdi, rdx and r15, among which r15, which has been mentioned many times, is responsible for Bytecode scheduling. When I debug Bytecode in assembly, the r15 register is often used as an "entry mark", that is, seeing r15 means something Bytecode has started, and seeing r15 again means that this Bytecode has ended.

# 4 InterpreterTraceBytecodeEntry and InterpreterTraceBytecodeExit

These two methods are used to track the interpretation process of Bytecode. InterpreterTraceBytecodeEntry can view the register status;

InterpreterTraceBytecodeExit is called after Bytecode is executed. The source code is as follows:

```
    RUNTIME_FUNCTION(Runtime_InterpreterTraceBytecodeEntry) {

        if (!FLAG trace ignition) {
3.
          return ReadOnlyRoots(isolate).undefined value();
4.
5.
        SealHandleScope shs(isolate);
6.
        DCHECK EQ(3, args.length());
7.
        CONVERT_ARG_HANDLE_CHECKED(BytecodeArray, bytecode_array, 0);
8.
        CONVERT SMI ARG CHECKED(bytecode offset, 1);
9.
        CONVERT_ARG_HANDLE_CHECKED(Object, accumulator, 2);
10.
         int offset = bytecode_offset - BytecodeArray::kHeaderSize + kHeapObjectTag;
11.
         interpreter::BytecodeArrayIterator bytecode_iterator(bytecode_array);
12.
         AdvanceToOffsetForTracing(bytecode_iterator, offset);
13.
         if (offset == bytecode_iterator.current_offset()) {
14.
           StdoutStream os;
15.
           // Print bytecode.
16.
           const uint8 t* base address = reinterpret cast<const uint8 t*>(
17.
                 bytecode_array->GetFirstBytecodeAddress());
18.
           const uint8 t* bytecode address = base address + offset;
19.
                                                                                             "@"
                             << static_cast<const void*;>(bytecode_address) << : ";
20.
                << std::setw(4) << offset <<
           interpreter::BytecodeDecoder::Decode(os, bytecode_address,
                                                            bytecode_array->parameter_count());
           os << std::endl;
           // Print all input registers and accumulator.
25.
           PrintRegisters(isolate, os, true, bytecode_iterator, accumulator);
26.
           os << std::flush;
27.
         }
28.
         return ReadOnlyRoots(isolate).undefined_value();
29. }
30. //Separator line.....
31. RUNTIME_FUNCTION(Runtime_InterpreterTraceBytecodeExit) {
32.
         if (!FLAG_trace_ignition) {
33.
           return ReadOnlyRoots(isolate).undefined_value();
34.
         }
35.
         SealHandleScope shs(isolate);
36.
         DCHECK_EQ(3, args.length());
37.
         CONVERT_ARG_HANDLE_CHECKED(BytecodeArray, bytecode_array, 0);
38.
         CONVERT_SMI_ARG_CHECKED(bytecode_offset, 1);
39.
         CONVERT_ARG_HANDLE_CHECKED(Object, accumulator, 2);
40.
         int offset = bytecode_offset - BytecodeArray::kHeaderSize + kHeapObjectTag;
41.
         interpreter::BytecodeArrayIterator bytecode_iterator(bytecode_array);
42.
         AdvanceToOffsetForTracing(bytecode_iterator, offset);
43.
         if (bytecode iterator.current operand scale() ==
44.
                    interpreter::OperandScale::kSingle ||
45.
              offset > bytecode_iterator.current_offset()) {
46.
           StdoutStream os:
47.
           // Print all output registers and accumulator.
48.
           PrintRegisters(isolate, os, false, bytecode_iterator, accumulator);
49.
           os << std::flush:
```

```
50. }
51. return ReadOnlyRoots(isolate).undefined_value();
52. }
```

The above two methods will be called before and after Bytecode is executed, but the value of FLAG\_trace\_ignition (line 2 of code) needs to be set to True. It is clearly in flags-definitions.h, the specific location is DEFINE\_BOOL(trace\_ignition, false, "trace the bytecodes executed by the ignition interpreter"). Line 21 of the code outputs Bytecode to the terminal, read

BytecodeDecoder::Decode() source code can understand the encoding methods of Bytecode and operand, which is helpful for understanding dispatch and JS debugging.

Use stack.

The PrintRegisters source code is given below:

```
1. void PrintRegisters(Isolate* isolate, std::ostream& os, bool is_input,
2.
                                interpreter::BytecodeArrayIterator&
3.
                                      bytecode iterator, // NOLINT(runtime/references)
4.
                                Handle<Object> accumulator) {
5.
        interpreter::Bytecode bytecode = bytecode_iterator.current_bytecode();
6.
        // Print accumulator.
7.
        if ((is_input && interpreter::Bytecodes::ReadsAccumulator(bytecode)) ||
8.
              (lis_input && interpreter::Bytecodes::WritesAccumulator(bytecode))) {
9.
          OS <<
                             [ " << kAccumulator << kArrowDirection;
10.
            accumulator->ShortPrint():
11.
            os << "]" << std::endl;
12.
         }
13.
         // Print the registers.
14.
         JavaScriptFrameIterator frame_iterator(isolate);
15.
         InterpretedFrame* frame =
16.
               reinterpret_cast<InterpretedFrame*>(frame_iterator.frame());
17.
         int operand_count = interpreter::Bytecodes::NumberOfOperands(bytecode);
18.
         for (int operand_index = 0; operand_index < operand_count; operand_index++)
19.
            interpreter::OperandType operand_type =
20.
                  interpreter::Bytecodes::GetOperandType(bytecode, operand_index);
            bool should print =
                 is input
interpreter::Bytecodes::IsRegisterInputOperandType(operand_type)
interpreter::Bytecodes::IsRegisterOutputOperandType(operand_type);
25.
            if (should_print) {
26.
               interpreter::Register first_reg =
27.
                    bytecode_iterator.GetRegisterOperand(operand_index);
28.
               int range = bytecode_iterator.GetRegisterOperandRange(operand_index);
29.
               for (int reg_index = first_reg.index();
30.
                      reg_index < first_reg.index() + range; reg_index++) {</pre>
31.
                 Object reg_object = frame->ReadInterpreterRegister(reg_index);
32.
                 OS <<
                                    [ " << std::setw(kRegFieldWidth)
33.
                      << interpreter::Register(reg_index).ToString(
34.
                               bytecode_iterator.bytecode_array()->parameter_count())
35.
                      << kArrowDirection;
36.
                  reg_object.ShortPrint(os);
37.
                 os << " ]" << std::endl;
```

```
38. }
39. }
40. }
41. }
```

Lines 14-17 above calculate the number of operands. Lines 20-37 code output the value of the register. By reading the PrintRegisters() method, we can learn three useful knowledge points:

(1) How to read registers; (2)

How to print data in V8; (3) The

data structure of InterpretedFrame. These

three points can help us better understand the execution process of Bytecode. Tip: The most comprehensive printing method in V8 is logger.

#### **Technical**

**summary (1)** The content stored in the location of SharedFunction::kFunctionDataOffset may be Bytecodearray or Builtins::kCompileLazy;

(2) BytecodeDecoder::Decode() and PrintRegisters() are very important and can help us understand the execution process of Bytecode.

Okay, that's it for today, see you next time.

Personal abilities are limited, there are shortcomings and mistakes,

criticisms and corrections are welcome WeChat: qq9123013 Note: v8 communication email: v8blink@outlook.com

This article was originally

published by Huidou and reprinted from: https://www.anquanke.com/post/id/

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