

CSCE 5013 Design Automation of VLSI Circuits and Systems

Lab Report

Title: _____

Date: _____ / _____, _____

| | |
|------------|--|
| First Name | |
| Last Name | |
| Uark ID | |
| Email | |

| | Grade | Comment |
|--------------------|--------------|----------------|
| Completeness | | |
| Result Quality | | |
| Academic Writing | | |
| Figures and Tables | | |
| Attachment | | |
| | | |
| Bonus | | |
| Penalty | | |
| Total | | |