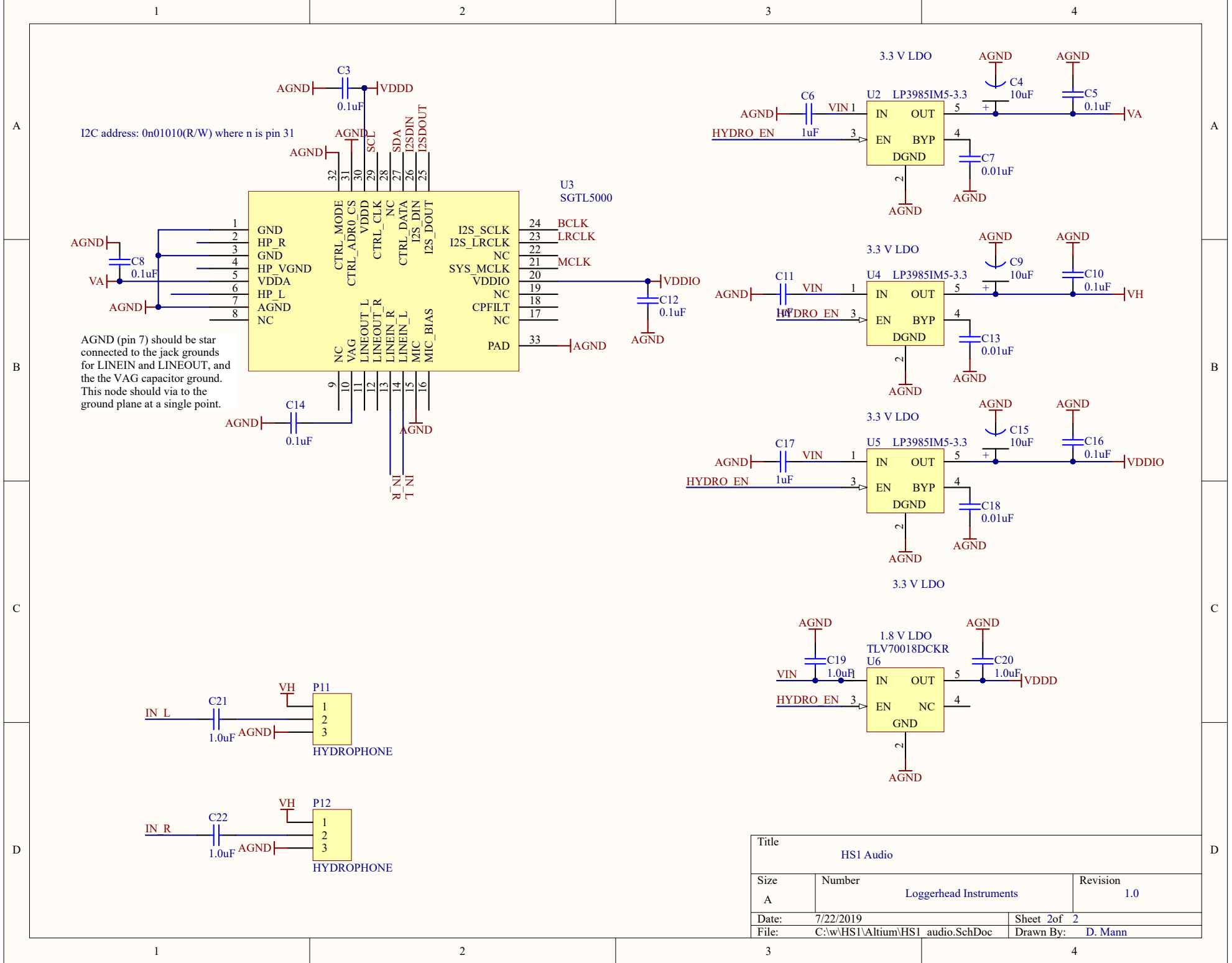
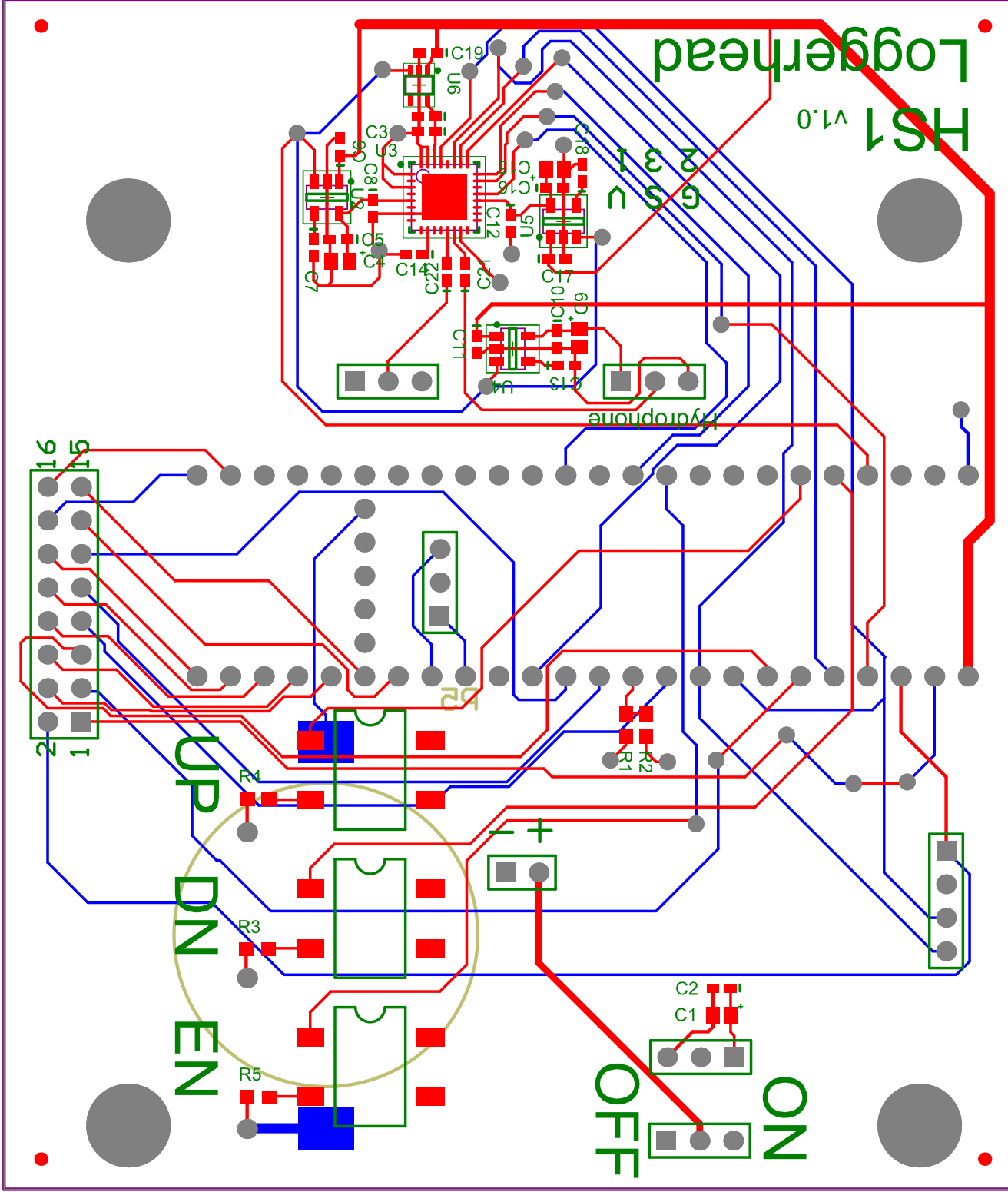


Title		
HS1-High Speed Recorder		
Size	Number	Revision
Legal	Loggerhead Instruments	1.0
Date:	7/22/2019	Sheet 1 of 2
File:	C:\w\HS1\Altium\HS1.SchDoc	Drawn By: D. Mann





Comment	Description	Designator	Footprint	LibRef	Quantity
10uF		C1, C4, C9, C15	Tant0805	Tant0805	4
0.1uF	Ceramic Chip Capacitor - Standard	C2, C3, C5, C8, C10, C12, C14, C16	0402	C0402	8
1uF	Ceramic Chip Capacitor - Standard	C6, C11, C17	0402	C0402	3
0.01uF	Ceramic Chip Capacitor - Standard	C7, C13, C18	0402	C0402	3
1.0uF	Ceramic Chip Capacitor - Standard	C19, C20, C21, C22	0402	C0402	4
Battery	Header, 2-Pin	P1	HDR1X2	Header 2	1
Header 8X2	Header, 8-Pin, Dual row	P2	HDR2X8	Header 8X2	1
DAC	Header, 3-Pin	P3	HDR1X3	Header 3	1
switch	Header, 3-Pin	P4	HDR1X3	Header 3	1
CoinCellHolder		P5	CoinCellHolder	CoinCellHolder	1
BIG CAP 1000 uF	Header, 3-Pin	P6	HDR1X3	Header 3	1
pushbutton		P7, P8, P9	SOP4	pushbutton	3
Display	Header, 4-Pin	P10	HDR1X4	Header 4	1
HYDROPHONE	Header, 3-Pin	P11, P12	HDR1X3	Header 3	2
100k	Precision Thick Film Chip Resistor, 10 Ohm to 1M Ohm Range, 1% and 0.5% Tolerance, 0603 Size, 0.063 W	R1, R2	3-0603	ERJ3RK	2
10k	Precision Thick Film Chip Resistor, 10 Ohm to 1M Ohm Range, 1% and 0.5% Tolerance, 0603 Size, 0.063 W	R3, R4, R5	3-0603	ERJ3RK	3
Teensy 3.6		U1	Teensy 3.6	Teensy3.6	1
LP3985IM5-3.3	Micropower, 150mA Low-Noise Ultra Low-Dropout CMOS Voltage Regulator	U2, U4, U5	MF05A_N	LP3985IM5-3.3	3
SGTL5000	SGTL5000	U3	QFN32	SGTL5000	1
TLV70018DCKR	Single Output LDO, 200 mA	U6	TI-DCK5_V	TLV70018DCKR	1