Part I: Purpose

X-Makina is a 16-bit load-and-store RISC emulator. To support this emulator, a set of operations on machine memory is required. Including putting program into memory and checking contents of specific memory.

To achieve that, XM3's 64 KiB of memory, its loader and debugger need to be built. Memory can be reached by bus() function, the loader will load the s-record and put corresponding data into the memory, if there is any error while reading in the file, it has the ability to handle the error and issue the warning. Debugger is to allow user to interact with XM3 emulator, for example: load file (L), access memory (M) and exit the emulator (X).

Part II: Description of the Algorithms

```
*MAIN*
CREATE 64 KiB Memory Array
WHILE DO
      GET Input Command from Console
      IF <u>Input Command</u> is "L" + <u>File</u> name
            CALL LOADXMEFILE (PASS File name)
            CALL LOADDATA (PASS File)
      ELSE IF <u>Input Command</u> is "M" + Start <u>Address</u> + End <u>Address</u>
            CALL MEMDUMP (PASS Start AND END Address)
      ELSE IF Input Command is "X"
            BREAK
      ELSE IF Input Command is "project -d fname.dbg"
            OPEN fname.dbg File
            LOAD file content as <a href="Input Commands">Input Commands</a> to Console
            CONTINUE
      ELSE
            ISSUE Warning: Invalid Command
END WHILE
*LOADXMEFILE*
OPEN <u>S-Records file</u>
IF <u>file</u> do not exist
      ISSUE Warning: Missing file
ELSE IF CHECK Records are not started with Record Type
      ISSUE Warning: File does not contain S-record
ELSE IF Record contains Non Hex characters after Record type
```

```
/OR Record length is more than number of bytes after it
ISSUE Warning: Warning, unknown error

ELSE IF CALL CHKSUM (PASS the Records)
ISSUE Warning: Incorrect checksum

ELSE
CONTINUE

ENDIF

EXIT
```

CHKSUM

READ last two character as <u>Checksum</u> byte

SUM <u>Hex data</u> in <u>Content</u> AND COMPARE WITH <u>Checksum</u> byte

IF MATCH the <u>Checksum</u>

RETURN TRUE

ELSE

RETURN FALSE

ENDIF

EXIT

LOADDATA

READ first <u>s-record</u>

WHILE (Not reach the end of <u>file</u>) DO

IF <u>Record Type</u> is SO

GET Source ASM <u>File name</u>

ELSE IF <u>Record Type</u> is S1

LOCATE Start Memory

CALL MEMBUS SAVE Content data into Memory Stack in byte

ELSE IF Record Type is S9

GET <u>Starting Address</u>

ENDIF

ENDWHILE

PRINT ASM File name and Starting Address

EXIT

MEMBUS

CHECK parameter MAR MBR*Bidirectional* RW BW

IF RW equals READ

GET data from memory address MAR

LOAD it into MBR buffer in Byte or Work as BW indicates

ELSE IF RW equals WRITE

GET data from the variable that MBR pointing to

SAVE it into $\underline{\mathsf{memory}}$ stack as $\underline{\mathsf{MAR}}$ address in Byte or Word as $\underline{\mathsf{BW}}$ indicates

ENDIF

EXIT

MEMDUMP

CALL MEMBUS PASS Memory Address AND Read Mode

CHECKING ASCII Table

CONVERT Hex Chars*In Byte* to ASCII corresponding char

PRINT Address AND Content Data AND corresponding char

Part III: Major Data Structure

S-Record

 $\underline{XME \ File} = S0 \ Record + (S1) \ Record + S9 \ Record$

S-Record = S-Record Type + Record Length + Address + (Content) + Checksum

 \underline{S} -Record Type = [S0 | S1 | S9]

<u>Record Length</u> = One Byte *Type* + One Byte *length* + Two Bytes *Address*+0{hex char}62 *Content* + One Byte *Checksum*

<u>Address</u> = *Starting to execute address in memory*

<u>Content</u> = [*Data stored in memory stack* | *File name*]

Checksum = *Check sum all current s-record*

*General Concept: *

 $\underline{Input\ Commands} = ['L' \mid 'M' \mid 'X']$

<u>Warning</u> = ["Missing file" | "File does not contain S-records" | "Incorrect checksum" | "Warning, unknown error" | "Invalid Input"]

<u>Memory Stack</u> = 0X0000 {Hex Value} 0XFFFF *64 KiB memory stack in machine*

<u>Array</u> = *Container object that holds a fixed number of values of a single type*

Program Counter = *Pointer to current address in memory*

Register = [R0 | R1 | R2 | R3 | R4 | R5 | R6 | R7]

 $\underline{BUS\ Parameter} = MAR + MBR + RW + BW$

<u>MAR</u> = *Memory address being accessed*

 \underline{MBR} = *Bidirectional pointer refers to the address of data*

<u>RW</u> = *Read-write indicator*

BW = *Byte-word indicator*

 $\underline{\text{Byte}} = *8 \text{ Bit characters*}$

Word = *16 Bit characters*

<u>ASCII Table</u> =

Dec	Hx Oct	Cha	r	Dec	Нх	Oct	Html	Chr	Dec	Нх	Oct	Html	Chr	Dec	: Нх	Oct	Html Cl	<u>nr</u>
0	0 000	NUL	(null)	32	20	040	a#32;	Space	64	40	100	a#64;	0	96	60	140	a#96;	8
1	1 001	SOH	(start of heading)	33	21	041	@#33;	1	65	41	101	A	A	97	61	141	a	a
2	2 002	STX	(start of text)	34	22	042	 4 ;	rr	66	42	102	B	В	98	62	142	b	b
3	3 003	ETX	(end of text)	35	23	043	@#35;	#	67	43	103	C	С	99	63	143	@#99;	C
4	4 004	EOT	(end of transmission)	36	24	044	\$	ş	68	44	104	4#68;	D	100	64	144	d	d
5	5 005	ENQ	(enquiry)	37	25	045	%	*	69			E					e	
6	6 006	ACK	(acknowledge)				&		70			F					f	
7	7 007	BEL	(bell)	39	27	047	%#39;	1	71	47	107	G	G		-		g	
8	8 010		(backspace)	ı			&# 4 0;		72			6#72;				-	h	
9	9 011	TAB	(horizontal tab))		73			@#73;					i	
10	A 012		(NL line feed, new line)				&#42;</td><td></td><td></td><td></td><td></td><td>@#74;</td><td></td><td></td><td></td><td></td><td>j</td><td></td></tr><tr><td>11</td><td>B 013</td><td>VT</td><td>(vertical tab)</td><td>ı</td><td></td><td></td><td>&#43;</td><td></td><td></td><td></td><td></td><td>K</td><td></td><td></td><td></td><td></td><td>k</td><td></td></tr><tr><td>12</td><td>C 014</td><td>FF</td><td>(NP form feed, new page)</td><td>44</td><td>2C</td><td>054</td><td>@#44;</td><td>1</td><td>76</td><td>4C</td><td>114</td><td>L</td><td>L</td><td></td><td></td><td></td><td>l</td><td></td></tr><tr><td>13</td><td>D 015</td><td>CR</td><td>(carriage return)</td><td></td><td></td><td></td><td>&#45;</td><td></td><td>77</td><td>_</td><td></td><td>M</td><td></td><td>109</td><td>6D</td><td>155</td><td>m</td><td>m</td></tr><tr><td>14</td><td>E 016</td><td>SO</td><td>(shift out)</td><td>46</td><td>2E</td><td>056</td><td>&#46;</td><td>•</td><td>78</td><td></td><td></td><td>@#78;</td><td></td><td></td><td></td><td></td><td>n</td><td></td></tr><tr><td>15</td><td>F 017</td><td>SI</td><td>(shift in)</td><td>47</td><td>2F</td><td>057</td><td>/</td><td>/</td><td>79</td><td></td><td></td><td>@#79;</td><td></td><td>111</td><td>6F</td><td>157</td><td>o</td><td>0</td></tr><tr><td></td><td></td><td></td><td>(data link escape)</td><td></td><td></td><td></td><td>&#48;</td><td></td><td>80</td><td></td><td></td><td>P</td><td></td><td></td><td></td><td></td><td>p</td><td></td></tr><tr><td></td><td></td><td></td><td>(device control 1)</td><td></td><td></td><td></td><td>6#49;</td><td></td><td>81</td><td></td><td></td><td>@#81;</td><td></td><td></td><td></td><td></td><td>q</td><td></td></tr><tr><td>18</td><td>12 022</td><td>DC2</td><td>(device control 2)</td><td></td><td></td><td></td><td>2</td><td></td><td>82</td><td></td><td></td><td>@#82;</td><td></td><td></td><td></td><td></td><td>r</td><td></td></tr><tr><td>19</td><td>13 023</td><td>DC3</td><td>(device control 3)</td><td></td><td></td><td></td><td>3</td><td></td><td></td><td></td><td></td><td>@#83;</td><td></td><td></td><td></td><td></td><td>s</td><td></td></tr><tr><td></td><td></td><td></td><td>(device control 4)</td><td></td><td></td><td></td><td>4</td><td></td><td> </td><td></td><td></td><td>4;</td><td></td><td></td><td></td><td></td><td>t</td><td></td></tr><tr><td></td><td></td><td></td><td>(negative acknowledge)</td><td></td><td></td><td></td><td>5</td><td></td><td></td><td></td><td></td><td>U</td><td></td><td></td><td></td><td></td><td>u</td><td></td></tr><tr><td></td><td></td><td></td><td>(synchronous idle)</td><td></td><td></td><td></td><td>4;</td><td></td><td></td><td></td><td></td><td>V</td><td></td><td> </td><td></td><td></td><td>v</td><td></td></tr><tr><td></td><td></td><td></td><td>(end of trans. block)</td><td></td><td></td><td></td><td>7</td><td></td><td>87</td><td></td><td></td><td>W</td><td></td><td></td><td></td><td></td><td>w</td><td></td></tr><tr><td></td><td></td><td></td><td>(cancel)</td><td>56</td><td></td><td></td><td>8</td><td></td><td>88</td><td></td><td></td><td>X</td><td></td><td></td><td></td><td></td><td>x</td><td></td></tr><tr><td>25</td><td>19 031</td><td>EM</td><td>(end of medium)</td><td>57</td><td></td><td></td><td>9</td><td></td><td>89</td><td></td><td></td><td>6#89;</td><td></td><td></td><td></td><td></td><td>y</td><td></td></tr><tr><td></td><td></td><td></td><td>(substitute)</td><td>58</td><td></td><td></td><td>:</td><td></td><td>90</td><td></td><td></td><td>Z</td><td></td><td></td><td></td><td></td><td>z</td><td></td></tr><tr><td></td><td></td><td></td><td>(escape)</td><td>59</td><td></td><td></td><td>;</td><td></td><td>91</td><td></td><td></td><td>[</td><td>-</td><td></td><td></td><td></td><td>{</td><td></td></tr><tr><td></td><td>1C 034</td><td></td><td>(file separator)</td><td></td><td></td><td></td><td><</td><td></td><td></td><td></td><td></td><td>\</td><td></td><td></td><td></td><td></td><td>4;</td><td></td></tr><tr><td></td><td>1D 035</td><td></td><td>(group separator)</td><td></td><td></td><td></td><td>=</td><td></td><td>93</td><td></td><td></td><td>]</td><td>_</td><td></td><td></td><td></td><td>}</td><td></td></tr><tr><td></td><td>1E 036</td><td></td><td>(record separator)</td><td></td><td></td><td></td><td>></td><td></td><td>ı</td><td></td><td></td><td>	4;</td><td></td><td></td><td></td><td></td><td>~</td><td></td></tr><tr><td>31</td><td>1F 037</td><td>US</td><td>(unit separator)</td><td>63</td><td>ЗF</td><td>077</td><td>4#63;</td><td>2</td><td>95</td><td>5F</td><td>137</td><td><u>@</u>#95;</td><td>_</td><td>127</td><td>7F</td><td>177</td><td></td><td>DEL</td></tr></tbody></table>											

*Data Type: *

```
Numeric = ["$" + [Unsigned | Signed] | """ + Char | "#" + Hex]

Unsigned = [0 .. 65535]

Signed = [-32768 .. +0 .. +65535]

Char = [Alphanumeric | Escaped] + """

Hex = 1 {0 .. 9 | A .. F | a .. f} * Hex values range from #0 to #FFFF *

Escaped = "\" + Alphanumeric

Alphabetic = [A..Z | a..z | _ ]

Alphanumeric = [A..Z | a..z | 0..9 | _ ]
```